INSTRUCTION

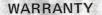
Serial Number _____

S-53
TRIGGER
RECOGNIZER
HEAD

Tektronix, Inc. ● P. O. Box 500 ● Beaverton, Oregon 97005 ● Phone 644-0161 ● Cables: Tektronix

070-1147-00

571



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Any questions with respect to the warranty, mentioned above, should be taken up with your Tektronix Field Engineer or representative.

All requests for repairs and replacement parts should be directed to the Tektronix Field Office or representative in your area. This procedure will assure you the fastest possible service. Please include the instrument Type (or Part Number) and Serial or Model Number with all requests for parts or service.

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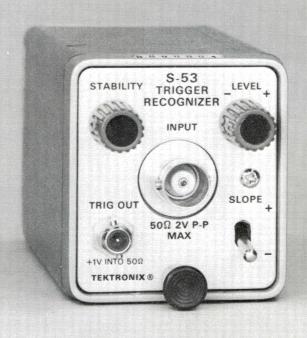


Fig. 1-1. S-53 Trigger Recognizer Head.

SECTION 1 SPECIFICATION

Change information, if any, affecting this section will be found at the rear of this manual.

General Information

The S-53 Trigger Recognizer Head produces a stable trigger signal from input signals from DC to 1 GHz. The trigger output signal is available at the front and rear panels of the S-53 to permit triggering of a sampling time-base unit.

The operating power for the S-53 is obtained when the unit is installed into a head compartment (or connected via an interconnecting cable) in Tektronix sampling instruments. The S-53 may be used with a Type 3S2, 3S5, or 3S6 in place of one of the sampling heads. The 7S12 will accept

the S-53 in place of the pulse generator, and thus operate as a general purpose sampling oscilloscope. The S-53 will also operate separately with the Type 285 Power Supply.

Electrical Characteristics

The following characteristics apply over an ambient temperature range of $0^{\circ}C$ to $+50^{\circ}C$ after a 10 minute warmup for an instrument that was calibrated at a temperature between $+20^{\circ}C$ and $+30^{\circ}C$. The required operating voltages are applied to the instrument when it is connected or installed into the sampling head compartment, or powered by a sampling head power supply.

ELECTRICAL CHARACTERISTICS

Characteristics	Performance Requirement	Supplemental Information	
INPUT Signal			
Frequency	DC to 1 GHz		
Sensitivity Range	10 mV to 1 V P-P into 50 Ω	2 V P-P maximum	
TRIG OUT Signal			
Amplitude	At least 1 V into 50 Ω positive-going		
Rise Rate	600 mV/ns		
Pulse Duration	3 ns within 2 ns at 50% amplitude level		
Period (minimum)	27 μs within 2.5 μs with input signals above 50 kHz.		
INPUT Signal to Trigger Out Signal Delay Time	15 ns or less	Conditions for Test: Use a fast rise signal (or high frequency source with low amplitude modulation) with am-	
INPUT Signal to Trigger Out Signal Jitter	15 ps or less	plitude of at least 200 mV, 7M11 50 Ω Delay Line, and 7S12.	
Kickout at Input	±10 mV or less		

ENVIRONMENTAL CHARACTERISTICS

Characteristics	Performance Requirement	Supplemental Information
Temperature		
Non-operating		-40°C to +65°C
Operating		0°C to +50°C
Altitude		
Non-operating		To 50,000 feet
Operating		To 15,000 feet
Vibration (Non-operating)		15 minutes along each axis at 0.015 inch. Vary the frequency from 10 to 55 to 10 Hz in 1-minute sweeps. Three minutes at any resonant point or at 55 Hz.
Shock (Non-operating)		Two shocks each of 500 g's (2 ms duration), 750 g's (1 ms duration) and 1000 g's (0.5 ms duration), in each direction and along each major axis for a total of 36 shocks.
Transportation		Meets National Safe Transit Committee type of test when packaged as shipped by factory.

MECHANICAL CHARACTERISTICS

Characteristics	Description
Finish	Anodized aluminum front panel, extruded aluminum blue-vinyl painted cabinet with aluminum castings front and rear.
Weight	Approximately 8 oz.
Dimensions	
Height	About 2 inches
Width	About 1 3/4 inches
Length	About 4 inches

SECTION 2 OPERATING INSTRUCTIONS

Change information, if any, affecting this section will be found at the rear of this manual.

General Information

This section of the manual provides the basic information required for operation of the S-53 Trigger Recognizer head, and includes installation and First Time Operation instructions.

The S-53 may be powered by any Tektronix instrument containing a sampling head compartment such as Tektronix 7S12, 7S11, or Types 3S2, 3S5, 3S6, or 286; or the unit may be powered separately by a Tektronix Type 285 Power Supply for S-50 Series Heads. The S-53 may be connected to a head compartment by one of two accessory extender cables. This permits a short length coaxial cable to be used to connect the input trigger signals.

A fast trigger output signal at the front and rear panel connectors which can be synchronized with input signals up to 1 GHz, makes the S-53 useful for an input triggering unit. The S-53 can be used in the Pulse Generator compartment of the 7S12 TDR Sampler unit to convert the unit to a general purpose sampling unit.

INSTALLATION

General

Since the S-53 Trigger Recognizer Head can be powered by Tektronix instruments containing sampling compartments or sampling head extender cables, many combinations of instruments are possible. Two general methods of installation are shown in Fig. 2-1. Part (A) shows the S-53 installed in the pulse generator compartment of the Tektronix 7S12 TDR Sampler unit. This allows the 7S12 to be used as a general purpose sampling unit. The 7S12 can be installed in any 7000 series oscilloscope. Part (B) shows the S-53 installed in the head compartment of the Type 285 Power Supply.

With either method of installation, the S-53 can be plugged into the sampling unit or powered as shown, or used remotely on a special extender cable. Three and six foot extender cables are available. Order the three foot extender cable by Tektronix Part No. 012-0124-00, or the six foot extender cable by Tektronix Part No. 012-0125-00. Contact your local Tektronix Field Office or representative for price and availability of these optional accessories.

Head Installation

To insert the S-53 into a compartment of the sampling unit or power supply, proceed as follows:

- 1. Pull the latch knob outward from the front panel (the latch knob will push out normally when the unit is inserted if the knob is left free to move).
- 2. Insert the unit slowly into the compartment, so the two plastic guides enter the rear connector opening.
 - 3. Push the S-53 completely into the compartment.
 - 4. Push the latch knob to lock the unit in place.

To remove the S-53 from the compartment, pull the latch knob away from the front panel, then pull the unit from the compartment.

Extender Cable Installation

To use the S-53 on an extender cable, install as follows:

- 1. Pull the latch knob extender outward from the panel (the latch knob will push out normally when the extender is inserted if the knob is free to move).
- 2. Insert the extender cable head end slowly into the desired compartment in the sampling unit so the two plastic guides in the compartment engage the unit.
 - 3. Push the head completely into the compartment.
- 4. Push the latch knob to lock the extender cable head end in place.
- 5. Connect the S-53 to the other end of the extender cable in a similar manner, and set the latch knob to hold it in place.

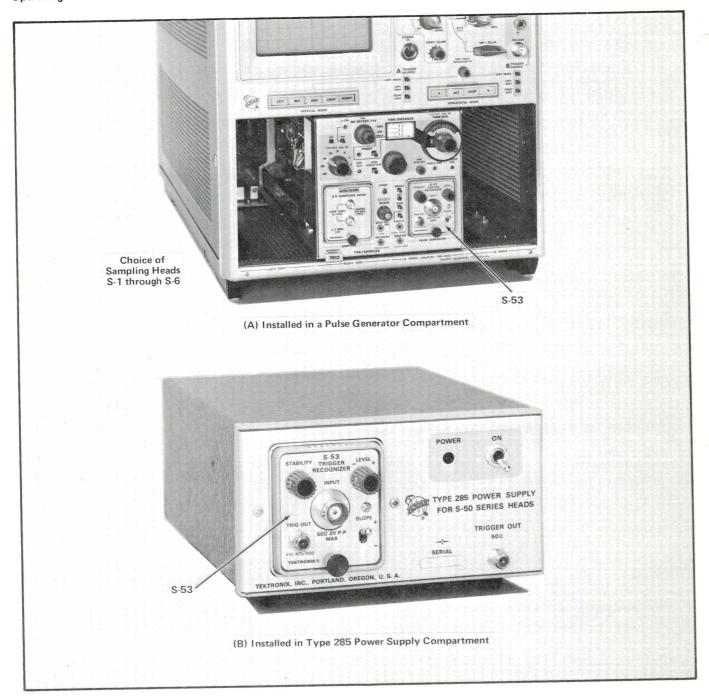


Fig. 2-1. S-53 Installation Information,

- 6. To remove the S-53 from the extender cable, pull the latch knob on the front panel of the S-53 and remove the unit from the extender cable.
- 7. To remove the extender cable head from the sampling unit compartment, pull the latch knob outward from the front of the panel, then pull the extender cable free.

CONTROLS AND CONNECTORS

A brief description of the funtion and operation of the controls and connectors of the S-53 follows:

SLOPE

Selects + (the positive-going) or — (the negative-going) slope of the triggering signal.

LEVEL

Determines the amplitude level on the triggering waveform where triggering is to occur. Also serves as a fine sync adjustment with the STABILITY control during HF

sync operation.

STABILITY

Adjusts the width of the Trigger hysteresis. Also serves as a coarse sync adjustment to place the S-53

HF sync operation.

INPUT

BNC type connector that accepts all triggering signals used in the

S-53.

TRIG OUT

BSM type connector, provides a positive pulse from the Trigger

Trigger Out

Rear Panel coaxial connector, provides positive pulse from the Trigger circuit for triggering standard

sampling time-base unit.

FIRST TIME OPERATION

General Information

One of the important uses for the S-53 Trigger Recognizer head is to convert the 7S12 TDR/Sampler plug-in unit to a general purpose sampling unit by providing an internal trigger output from a wide range of signal inputs. This First Time Operation uses the S-53 installed in the Pulse Generator compartment of the 7S12 as shown in Fig. 2-1A. Other sampling heads can be substituted for the S-6, depending upon the signal inputs and risetime required. Tektronix Type 284 Pulse Generator is used to provide signals to the S-53 and S-6.

Procedure

- 1. Insert the 7S12 TDR/Sampler into the center two compartments of the 7504 Oscilloscope. Any Tektronix 7000-series Oscilloscope may be substituted for the 7504.
 - 2. Install the S-53 in the Pulse Generator compartment.
 - 3. Install the S-6 in the 7S12 Sampling compartment.
 - 4. Set the controls as follows:

7504 Indicator Oscilloscope

A Intensity **B** Intensity

CCW **CCW**

Vertical Mode Horizontal Mode Right

7S12 with S-6 and S-53

(Two center compartments, the right vertical and the A horizontal compartments.)

Time Distance dial 0 X.1 Multiplier Time/Div 5 ns Cal in Variable

Fully clockwise Fine (Zero Set) pushed in Rep Midrange Scan pushed in Locate pushed in mV mV/Div 100 DC Offset (& Fine) Midrange

S-53

STABILITY LEVEL SLOPE

Fully clockwise Midrange

5. Turn on the Type 284 and the Oscilloscope Power. After about a 5 minute warmup time, advance the A Intensity until a free running trace is observed. Center the trace on the CRT with the 7S12 DC Offset control.

Using a Pretrigger

- 6. Install a SMA (3 mm) termination connector to the S-6 Loop Thru (upper) connector. Connect the Type 284 Pulse Output signal through a GR to BNC adapter, a BNC coaxial cable, and a BNC to SMA (3 mm) adapter to the S-6 Loop Thru (lower) connector. Connect the Type 284 Trigger Output signal (Lead Time 75 ns) through a BNC coaxial cable to the S-53 INPUT connector.
- 7. Turn the S-53 STABILITY control fully counterclockwise, and set the LEVEL control for a stable display of the Type 284 Pulse Output signal. See Fig. 2-2.

H.F. Triggering

- 8. Disconnect the Type 284 Pulse Output cable and connect the Type 284 1 ns Period sine wave output signal through a GR to BNC adapter, a BNC coaxial cable, and a BNC to SMA (3 mm) adapter to the S-6 Loop Thru (lower) connector.
- 9. Change the Time/Div to 1 ns and set both the S-53 STABILITY and the LEVEL control for a stable sine wave display; see Fig. 2-3.

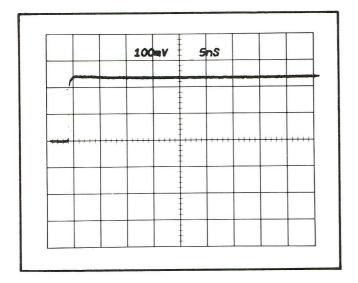


Fig. 2-2. Type 284 Pulse Output signal display using a pretrigger.

The Trigger source signal to the S-53 INPUT can be obtained from the test signal by using a tee connection, by using a power divider, or by using the Feed Thru (upper) connector of the S-6. To use the Feed Thru (upper) connector of the S-6 to obtain the trigger source, connect as follows:

10. Remove the 50 Ω termination from the S-6 Feed Thru (upper) connector, and install a SMA (3 mm) to BNC adapter in its place. Remove the BNC coaxial cable connected to the S-53 INPUT and connect a short BNC coaxial cable from the S-53 INPUT connector to the BNC to SMA adapter installed on the S-6 Feed Thru (upper) connector.

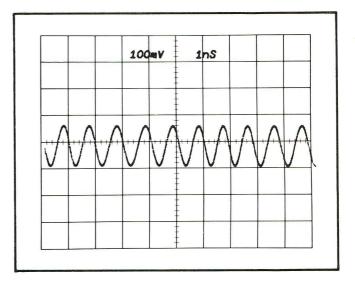


Fig. 2-3. 1 ns period sine wave display.

11. Set both the STABILITY and the LEVEL control for a stable sine wave display.

Fig. 2-4 shows the setup information for two applications to convert the 7S12 into a general purpose sampling unit with the S-53.

When the Type 285 Power Supply is used to furnish the power to the S-53 as shown installed in Fig. 2-1B, both trigger output signals are available at the front panel; one from the S-53 TRIG OUT connector, and one from the Type 285 Trigger Out connector. Either connector provides a positive pulse from the trigger circuit for triggering standard sampling time-base units or other devices.

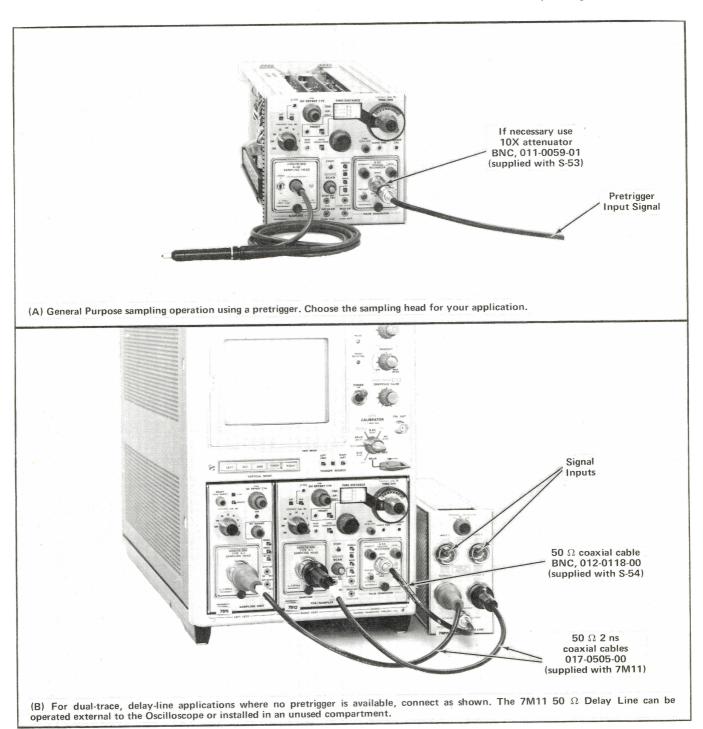


Fig. 2-4. General Purpose sampling applications.

SECTION 3 CIRCUIT DESCRIPTION

Change information, if any, affecting this section will be found at the rear of this manual.

General Information

This section of the manual contains the electrical description of the S-53 Trigger Recognizer Head circuits.

The S-53 requires +15 V and -12.2 V input power. The input power is obtained when the instrument is connected to the pulse generator compartment connector of a TDR/Sampler unit, to one compartment connector of a dual-trace sampling unit, or to the Type 285 Power Supply.

Refer to the schematic diagram in Section 7 of the manual as necessary during the Circuit Description.

BLOCK DIAGRAM

The Block diagram, Figure 3-1, shows the major circuit blocks of the S-53. A brief description of each block follows, starting with the Level and Slope block.

The S-53 trigger input signals are connected through the front panel INPUT connector to the Level and Slope amplifier. The front panel SLOPE switch selects the positive or negative going portion to be used, and the front panel

LEVEL control sets the level of the input signal at which triggering will occur. At this triggering point, the amplifier drives a current signal to the Schmitt Trigger block.

When the Schmitt Trigger is driven by the current signal from the Level and Slope, it produces a fast pulse to drive the Arming and Start Tunnel Diode. The front panel STABILITY control provides a means of setting the sensitivity of the Schmitt trigger circuit.

When the tunnel diodes are armed in the Arming and Start Tunnel Diode block, the fast trigger pulse from the Schmitt Trigger operates the circuit to provide a fast output. The Holdoff block provides a minimum holdoff time between triggers and resets the tunnel diodes.

The output amplifies the fast positive step from the Arming and Start Tunnel Diodes to provide a fast positive pulse to the front and rear panel trigger out connectors.

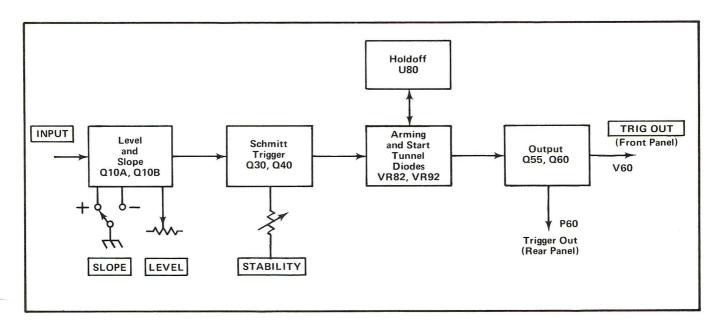


Fig. 3-1. S-53 Block diagram.

CIRCUIT DESCRIPTION

Level and Slope

The Level and Slope Amplifier consists of Q10, Q12, Q20, Q22 and associated components. The input to the circuit is from the front panel INPUT connector, and the output current from the common connection of R26, R20, and R22 drives the Schmitt Trigger circuit.

Q10 and Q12 are connected as a differential amplifier. With the LEVEL control set to midrange, and no input signal at J10, current in each transistor is about 5 mA. Assume that the + SLOPE is selected as shown in the diagram in Section 7. Q20 is reverse biased, and Q22 is conducting. The TRIG ZERO adjustment allows the current to be set to drive the Schmitt Trigger circuit.

With a positive input signal, above the level set by the LEVEL control, Q10 and Q12 cause current in Q22 to increase. When the - SLOPE is selected, Q22 is reverse biased and Q20 is conducting. With a negative input signal below the level set by the LEVEL control, Q10 and Q12 cause current in Q20 to increase. The increase in current in Q20 or Q22 increases the current to the Schmitt Trigger circuit.

Schmitt Trigger

The Schmitt Trigger circuit consists of Q30, Q40, tunnel diode CR25, and associated components. The circuit is driven by a current signal from the Level and Slope circuit through Q20 or Q22. The positive output signal is coupled through C49 to the Arming and Start Tunnel Diode circuit at Q50 base.

The tunnel diode, CR25, is connected into the positive feedback circuit of the amplifier so that an increase in the tunnel diode current will be amplified by the circuit. Once the tunnel diode has changed state in response to an increase current signal, the circuit reduces current in the tunnel diode so that it will revert to the low state when the input current signal is removed.

Input current from the common connection of R22, R20, and R26 is set by the Trig Zero, R25, during calibration to arm the tunnel diode. Part of the input current path is through R28-Q30, and the remainder is through CR25-Q40. Additional current to this circuit is supplied from +15 V through R29 and R41.

With CR armed, the next increase in current signal from the Level and Slope circuit caused an increase of current in the tunnel diode, CR25. Even if this initial increase

in current does not cause the tunnel diode to change to its high state, this change may still occur due to amplification in the circuit. Any increase in tunnel diode current produces a change in voltage at Q40 collector. A part of this voltage change, as determined by the settings of the Stability Zero Set adjustment and the STABILITY control, appears at the emitter of Q30 as a decrease in forward bias. This results in a larger percentage of current from R29. When CR25 current is sufficient to cause it to change to its high state, a positive signal is produced, and coupled through C49 to Q50 base and the 3 ns delay line in the Arming and Start Tunnel Diode circuit.

When CR25 changes to the high state, less current is supplied by the Level and Slope circuit, which results in a lower current in Q40. The decrease in Q40 current is sensed by Q30, and thus R29 supplies less current to CR25. Current is decreased to a point where removal of the initial current signal from Q20 or Q22 in the Level and Slope circuit will cause CR25 to return to its low state. Current from the Level and Slope circuit, as set by the Trig Zero adjustment, will again arm CR25 for the next current signal.

Arming and Start Tunnel Diode

The Arming and Start Tunnel Diode circuit consists of Q50, CR82 (the Arming tunnel diode) CR92 (the Start tunnel diode), a 3 ns delay line, and associated components.

The circuit is triggered from a fast positive pulse from CR25, and is controlled by the Holdoff circuit at U80A and B collectors. Circuit output at CR92 anode drives the Holdoff and Output circuits.

Before a trigger input signal, the Holdoff circuit transistors U80A and B must be cut off to allow arming current for CR82 and CR92. Arming current for CR82 is set by R82, and arming current for CR92 is set by R92. These controls set the current below the peak value requirement of each tunnel diode. Final arming of CR92 is accomplished when CR82 changes to its high state.

With CR82 armed, the next positive input signal through C49 drives Q50 base and the 3 ns delay line. The increased conduction of Q50 couples a positive pulse through C50 and R51 to cause CR82 to change to its high state. With CR82 hanged to its high state, the arming current of CR92 is increased, and CR92 is fully armed. The positive trigger signal from the 3 ns delay line through R52 causes CR92 to change to its high state. This positive output signal at CR92 anode drives the Output circuit through CR51, and the Holdoff circuit through R67.

Further operation of the Arming and Start Tunnel Diode circuit occurs when the Holdoff circuit turns on U80A and B, which reduces the current to the tunnel diodes CR82 and CR92 causing them to change to their low states. Later U80A and B are reverse biased, and the arming current to the tunnel diodes is re-established.

Holdoff

The Holdoff circuit, consisting of U80 and associated components, is driven by the positive step pulse from the Start tunnel diode. The circuit output at U80A and B collector controls the arming current of the Arming and Start tunnel diodes.

The quiescent conditions of the circuit are as follows: the Start tunnel diode, CR92, is armed (at its low state) and U80E is reverse biased. VR67, CR67, and CR68 are conducting, and C70 is charged to about 6.2 V. U80D base current in R73 turns on U80D (emitter at zero volts) and CR70 is reverse biased. With U80D on, U80A, B and C are reverse biased.

When CR92 changes to its high state, this positive voltage through R97 and R67 forward biases U80E. With U80E on, its low collector voltage reverse biases CR68 and allows C70 to charge through R70. C70 charges to a value which causes CR70 to conduct and reverse bias U80D. With U80D reverse biased, U80C base current in R75 causes U80C to conduct (emitter near +5 V), and turn on U80A and B. With U80A and B on, the current to CR82 and CR92 is reduced. This reduction in current causes CR82 and CR92 to change to their low stages. Also when U80A is on, U80E becomes reverse biased. This allows CR68 to conduct and

charge C70 toward 6.2 V. C70 voltage rises and is coupled by CR70 to U80D base until U80D is forward biased and its base conducts enough current through R73 to reverse bias CR70. With U80D on, U80A, B, and C are reverse biased. The circuit is ready for another Start tunnel diode positive signal.

Output

The Output circuit amplifies the positive step from the Start tunnel diode to provide a trigger output pulse to the front and rear panel connectors.

Before the input pulse, CR51 conducts, forward biasing Q55. Q55 conducts with its current path from +15 V through R58, R57, Q55, and R55 to the -12.2 V. Q55 collector current is low, and Q60 does not conduct.

When CR92, in the Arming and Start Tunnel Diode circuit, changes to its high state, a positive signal is coupled through CR51 to Q55 base. Q55 increases conduction through C60 and turns on Q60. Q60 collector goes positive and feedback occurs through R62 and C62 to Q55 base. CR51 is reverse biased. Fast regeneration of the circuit is caused by C57, C60, and C62. The fast positive pulse at the collector of Q60 is coupled to the TRIG OUT connector (J60) on the front panel through C65 and CR65, and to the rear panel connector (P60) through C61 and CR61.

After this fast regeneration, Q60 cuts off, and C60 charges to set Q55 emitter at a low voltage level allowing CR51 to conduct.

SECTION 4 MAINTENANCE

Change information, if any, affecting this section will be found at the rear of this manual.

Introduction

This section of the manual is a maintenance guide for the S-53 Pulse Generator Head. Information is included for parts ordering, parts removal and replacement, disassembly and assembly.

Obtaining Replacement Parts

All parts used in the S-53 can be purchased directly through your local Tektronix Field Office or representative. However, replacements for standard electronic items can be obtained locally. Consult the Electrical or Mechanical Parts List to determine the value, tolerance and rating required.

NOTE

When selecting replacement parts, it is important to remember that the physical size and shape of a component may affect its performance at high frequencies. After repair, the S-53 Pulse Generator Head may require re-calibration.

Parts Removal and Replacement

Housing and Rear Panel. To remove the S-53 from its housing, loosen the four retaining screws on the rear panel. Slide the rear panel off, and remove the housing by sliding it to the rear. With the housing and rear panel removed, the unit can be connected to an extender cable for access to adjustment controls and circuit test points for calibration. Two lengths of extender cables are available from your local Tektronix Field Office or representative. Order by Tektronix Part No. 012-0124-00 for the three-foot length and Tektronix Part No. 012-0125-00 for the six-foot length extender cable.

To install the S-53 in its housing, check that the upper and lower corners of the T.D. Driver and Delay Line board are aligned with the channels in the housing which contain the zigzag springs. Push the S-53 gently into the housing until it contacts the front panel. Be sure that the white plastic pawl in the locking knob is properly aligned as the S-53 is slid into the housing. Attach the rear casting, making sure that the hole on

one side fits over the trigger output signal connector. Insert the four long mounting bolts and tighten them securely. To ensure that the mounting bolts align with the front panel, hold the S-53 in its normal horizontal position; start the lower bolts, then turn the S-53 over and start the remaining two bolts.

Circuit Boards. To remove the T.D. Driver board, free the Delay Line coaxial cable from the slot in the Pre-Trigger (center) board and gently pull the board outward from the Pre-Trigger board. This allows access to both sides of both boards. To remove the trigger coaxial lead from the front panel, use a 5/16-inch end wrench to hold the nut located behind the front panel. Then use a 9/32-inch end wrench to remove the coaxial cable retaining nut. Once this nut is removed, gently pull the coaxial cable, together with the connector center pin, from the connector. Remove the connector shell, by first loosening the nut located behind the front panel with the 5/16-inch end wrench. The Delay Line is soldered to each board. To unsolder the Delay Line, care should be taken not to overheat the board or parts on the boards (example: carefully unsolder CR92 on the T.D. Driver board before unsoldering the Delay Line). To install the Timing board, reverse the procedure. (When inserting the coaxial cables, be sure that the outer braid is not shorted to the center conductor).

To remove the Pre-Trigger board; the connecting wire from the center conductor of the PULSE OUTPUT connector and the wires connecting the front panel controls must be unsoldered.

Use the following procedure:

- 1. Unsolder the wire from the center conductor of the BNC connector.
 - 2. Unsolder the wires from the front panel controls.
- 3. With a 5/64-inch allen wrench, remove the two screws holding the Pre-Trigger board to the BNC connector assembly. Remove the Pre-Trigger board from the connector.

4. To install the Pre-Trigger board, reverse the procedure. Be sure the two spiral pins are flush with the connector holder before tightening the allen screws.

Parts Location

Photos of the T.D. Driver and Pre-Trigger boards, with the component locations, are shown in Section 7. The Mechanical Parts Illustration in Section 8 shows locations of the mechanical parts.

Troubleshooting

As an aid to troubleshooting, use the troubleshooting conditions listed on the schematic diagram page in Section 7. A preliminary condition is to determine if the sampling unit or Power Supply is providing the proper power to the S-53. The waveform conditions are given using the TRIG OUT signal to trigger the test oscilloscope. If necessary, use internal triggering (if no TRIG OUT signal) to isolate the inoperative circuit.

A TRIG OUT signal should be available when the STABILITY control is fully clockwise and the LEVEL control midrange. This also checks the free-run operation of the Schmitt trigger circuit.

Without an input signal to the S-53, set the STABILITY control counterclockwise. The Schmitt trigger circuit should not free-run and no TRIG OUT signal should be available. For further information on the Schmitt circuit and other circuits, refer to the Circuit Description Section.

Repackaging for Shipment

If the Tektronix instrument is to be shipped to a Tektronix Service Center for service or repair, attach a tag showing: owner (with address) and the name of an individual at your firm that can be contacted, complete instrument serial number and a description of the service required.

Save and re-use the package in which your instrument was shipped. If the original packaging is unfit for use or not available, repackage the instrument as follows:

Surround the instrument with polyethylene sheeting to protect the finish of the instrument. Obtain a carton of corrugated cardboard of the correct carton strength and having inside dimensions of no less than six inches more than the instrument dimensions. Cushion the instrument by tightly packing three inches of dunnage or urethane foam between carton and instrument, on all sides. Seal carton with shipping tape or industrial stapler.

The carton test strength for your instrument is 200 pounds.

SECTION 5 PERFORMANCE CHECK/CALIBRATION

Change information, if any, affecting this section will be found at the rear of this manual.

Introduction

This section of the manual contains the Performance Check and the Calibration Procedure. When the Performance Check Procedure is completed, the instrument is checked to the "Performance" information given in Section 1. The tolerances and waveforms given in the Calibration Procedure should be considered only as calibration guides, and not as instrument specifications.

Equipment Required

The following test equipment, or its equivalent is required for both the Performance Check Procedure and the Calibration Procedure of the S-53. All test equipment must be calibrated. If other equipment is substituted, it must meet or exceed the limits stated in the equipment list.

- 1. Oscilloscope; 7000-Series such as 7704 to accept plug-in units listed in item 2.
 - 2. 7S12 TDR/Sampler.
 - 3. S-6 Sampling Head.
- 4.1 S-1 Sampling Head. (Used only to check kickout signal in Performance check step 5.)
 - 5. 7M11 50 Ω Delay Line.
- 6. Test Oscilloscope, 10X probe, 20 MHz Bandwidth, 5 μ s/div, 5 V/div. (Used only to check TRIG OUT Period, Performance check step 6.)
 - 7. Type 284 Pulse Generator.
- 8. 50 $\,\Omega$ coaxial cable, 1 ns, SMA (3 mm) connectors, Tektronix Part No. 015-1023-00 (supplied with S-52).
- 9. Two Coaxial cables, 50 Ω , 42 inch, BNC connectors, Tektronix Part No. 012-0057-01 (one supplied with S-53).

- 10. Coaxial cable, 50 $\,\Omega$, 18 inch, BNC connectors, Tektronix Part No. 012-0076-00.
- 11. Three adapters, GR to BNC Female, Tektronix Part No. 017-0063-00.
- 12. Adapter, SMA (3 mm) Male to BNC Female, Tektronix Part No. 015-1018-00.
- 13. Adapter, GR to BNC Male, Tektronix Part No. 017-0064-00.
- 14. Adapter, SMA (3 mm) Female to Female, Tektronix Part No. 015-1012-00 (supplied with S-6).
- 15. Adapter, BSM Male to BNC Female, Tektronix Part No. 103-0036-00.
- 16. 50 Ω termination male, SMA (3 mm) Tektronix Part No. 015-1022-00 (supplied with S-6).
- 17. 50 Ω 5X attenuator, BNC Tektronix Part No. 011-0060-01.
- 18. BNC T connector, Tektronix Part No. 103-0030-00.
- 19. Variable Attenuator with GR874 connectors. It consists of a 100 Ω potentiometer across a 50 Ω line, and does not have a guaranteed response. Tektronix Part No. 067-0511-00.
- 20. Sampling-Head extender, 3 foot. Tektronix Part No. 012-0124-00.

PERFORMANCE CHECK PROCEDURE

Introduction

The Performance Check provides a means of rapidly checking the S-53 without adjusting any internal con-

trols. Failure to meet any of the requirements given in this procedure indicates a need for internal checks or adjustments, and the user should refer to the Calibration Procedure in this section.

Preliminary Procedure

1. Assemble the equipment as follows: Install the 7S12 in the center two compartments of the 7504 Oscilloscope. Install the S-6 in the Sampling compartment and install the S-53 in the Pulse Generator compartment of the 7S12. The 7M11 50 Ω Delay Line requires no operating power, but can be installed in an unused compartment of 7504. Type 284 Pulse Generator is used in step 3, 4 & 5.

2. Set the controls as follows:

7504 Indicator Oscilloscope

A Intensity	CCW
B Intensity	CCW
Vertical Mode	Right
Horizontal Mode	Α

Time Distance dial

7S12 with S-6 and S-53

(Two center compartments, the right vertical and the A horizontal compartments)

0

Multiplier	X.1
Time/Div	10 ns
Variable	Cal in
Fine (Zero Set)	Fully clockwise
Rep	pushed in
Scan	Midrange
Locate	pushed in
mV	pushed in
mV/Div	100
Variable	Cal in
DC Offset (& Fine)	Midrange

S-53

STABILITY	Fully Clockwise
LEVEL	Midrange
SLOPE	+

3. Turn on the Type 284, and the Oscilloscoe power. After about five minutes warmup, advance the A Intensity until a free-running trace is observed. Center the trace with the 7S12 DC Offset control.

1. Check TRIG OUT (Front Panel) Amplitude and Duration

- a. Connect the S-53 TRIG OUT signal through a BSM to BNC adapter, a 5X attenuator, a 42 inch BNC coaxial cable, and a BNC to GR adapter to 7M11 Input 1 connector. Connect the 7M11 Output 1 through a GR to BNC adapter, a 42 inch BNC coaxial cable, and a BNC to 3 mm adapter to the S-6 Loop Thru (lower) connector. Connect a 1 ns 3 mm coaxial line and a 50 Ω termination to the upper Loop Thru connector.
- b. Turn the S-53 STABILITY control fully clockwise. Set the LEVEL control to about midrange, to observe the TRIG OUT signal on the CRT.
- c. Check the TRIG OUT amplitude to be at least one volt positive-going (1 division or greater).
- d. Change the Time/Div to 2 ns. Use the Time-Distance knob to position the the TRIG OUT display on screen. Check the TRIG OUT duration at the 50% amplitude point to be 3 ns within 2 ns.

2. Check TRIG OUT (Front Panel) Rise Rate

- a. Change the Time/Div to 500 ps. Set the mV/Div to 50. Use the Time-Distance knob to position the rising portion of the TRIG OUT signal on the CRT.
- b. Check any portion of the rise to be greater than 600 mV (1.2 divisions) in 1 ns. See Fig. 5-1.

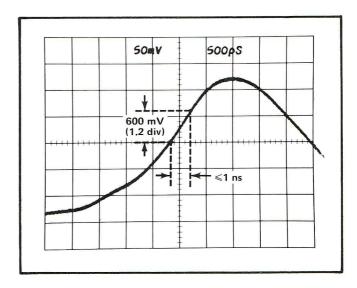


Fig. 5-1. Waveform of TRIG OUT leading edge showing Rise Rate. (2X attenuator in 7M11 and 5X attenuator in signal path for 500 mV/div.)

3. Check INPUT Signal to TRIG OUT Signal Delay Time

- a. Disconnect the cables and connect as follows: Install a BNC tee connector onto the S-53 INPUT connector. To the TRIG OUT connector, install a BSM to BNC adapter and a 5X attenuator. Connect the Type 284 Pulse Output Signal through a GR to BNC adapter and a BNC coaxial cable to one arm of the tee connector. Connect the other arm of the tee connector through a 42 inch BNC coaxial cable, a BNC to GR adapter to Input 1 on the 7M11 50 Ω Delay Line. Connect the 7M11 Output 1 through a GR to BNC adapter, a 42 inch BNC coaxial cable, and a BNC to 3 mm adapter to the S-6 Loop Thru (lower) connector.
- b. Set the mV/Div to 50. Set the Multiplier to X.1, and the Time/Div to 2 ns. Set the Time-Distance control for a dial reading of zero.
- c. Set the S-53 LEVEL control for a stable display of the Type 284 Pulse Output leading edge (the S-53 Input signal). Change the Time-Distance knob to position the pulse leading edge to a reference point 1 division from the left side of the graticule.
- d. Without moving the position, trigger controls, or other setup conditions; change the BNC cable connector from the BNC tee connector (the cable going to the 7M11) to the 5X attenuator installed with an adapter on the S-53 TRIG OUT connector.
- e. Observe the TRIG OUT signal. Check the position of the TRIG OUT signal leading edge at the 50% amplitude point to be 15 ns or less from the reference point (Pulse Output leading edge) set in part c. See Fig. 5-2.

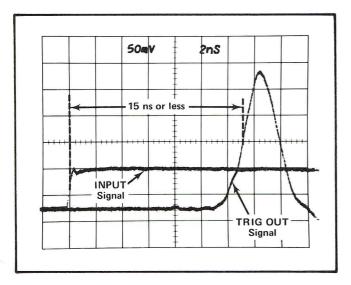


Fig. 5-2. Double exposure photo showing the INPUT signal to TRIG OUT delay time.

4. Check INPUT Signal to Trigger Out Signal Jitter

- a. Disconnect the BNC cable connector from the 5X attenuator and connect it to the remaining arm of the tee connector. (This connects the input signal to the S-6 via the 7M11).
- b. Set the Time-Distance control near zero to observe the Type 284 Pulse Output leading edge (S-53 INPUT Signal).
- c. Set the mV/Div to 50, and set the mV/Div Variable control for a display amplitude of about 5 divisions. Then change the mV/Div to 10, and center the rising portion of the Pulse at the center of the graticule.
- d. Change the Time-Div to 20 and Turn the Time-Distance knob to position the rising portion of the pulse to the center of the CRT.
- e. Use 90% of the dots displayed in the rising portion of the signal to measure the jitter to be 15 ps or less. See Fig. 5-3.

5. Check Kickout at INPUT

- a. Disconnect the cables from the Heads, remove the S-6 Sampling Head, and install a S-1 Sampling Head in its place.
- b. Connect the S-53 TRIG OUT through a BSM to BNC adapter, a 5X attenuator, a 42 inch BNC coaxial cable, a BNC to GR adapter to Input 1 on the 7M11

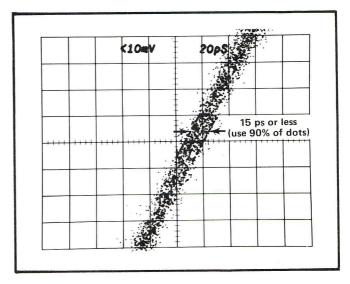


Fig. 5-3. Typical waveform to observe INPUT signal to Trigger Out signal jitter in step 4.

Delay Line. Connect the 7M11 Output 1 through a 5 ns GR coaxial cable to the S-1 Input connector.

c. Change the following controls:

Time Distance dial	0
Multiplier	X.1
Time/Div	10 ns
mV/Div	100
Variable	Cal in

- d. Observe the TRIG OUT signal on the CRT screen and record its position horizontally on the graticule. See the typical displayed signal (Fig. 5-4 waveform A).
- e. Disconnect the coaxial cable at the 5X attenuator and connect it to the S-53 INPUT connector. (This connects the INPUT through the Delay Line to the S-1 Input and maintains relative timing.)
- f. Change the mV/Div to 2, and center the trace. with the DC Offset control.
- g. Turn the SCAN control counterclockwise for about a one second sweep to observe the Kickout signal. Adjust the S-53 LEVEL and STABILITY controls for maximum amplitude of the Kickout signal. See a typical waveform (Fig. 5-4 waveform B).
- h. Observe the displayed Kickout signal to be less than 10 $\ensuremath{\text{mV}}\xspace$.

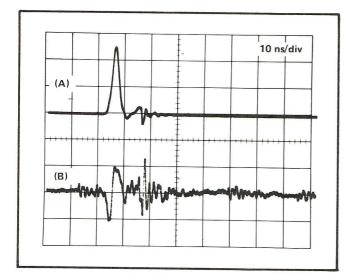


Fig. 5-4. Double exposure photo to show timing of kickout signal. (A) TRIG OUT signal; (B) Typical Kickout signal from the INPUT.

i. Disconnect the cables to the Heads, remove the S-1 Sampling Head, and install the S-6 Sampling Head in place of the S-1.

6. Check TRIG OUT Period

- a. Set the S-53 STABILITY control fully clockwise, and the LEVEL control to midrange.
- b. Connect a Test Oscilloscope (about 20 MHz bandwidth) 10X probe to the S-53 TRIG OUT connector (unterminated).
- c. Trigger the Test Oscilloscope internally on a positive signal. Use 5 $\mu s/\text{div}$ sweep rate and about 5 V/Div including the probe.
- d. Check the TRIG OUT Period to be 27 μs within 2.5 $\mu s.$

7. Check INPUT Signal and Sensitivity

- a. Disconnect the cables and connect as follows: Connect 100 ns 1 volt square wave signal from the Type 284 through a Variable attenuator, a GR to BNC adapter to a BNC tee connector. Connect one arm of the tee connector through a 42 inch BNC coaxial cable to the S-53 INPUT connector. Connect the other arm of the tee connector through a 42 inch BNC coaxial cable, and a BNC to 3 mm adapter to the S-6 Loop Thru (lower) connector.
- b. Set the Multiplier to X.1. Set the Time/Div to 10 ns, and the mV/Div to 200. Set the Variable attenuator in the signal path to clockwise (minimum attenuation).
- c. Set the S-53 STABILITY control fully counterclockwise, and set the LEVEL control for a stable display of the 100 ns period square wave. Check that the LEVEL control can be set for a stable display with the SLOPE switch in either + or -.
- d. Change the Type 284 Square Wave Amplitude to 100 mV. Change the Variable attenuator in the signal path to reduce the display to about 15 mV (set the S-53 LEVEL control to maintain a triggered display). Check that the display can be triggered with the SLOPE switch in either + or -.
- e. Change the Type 284 Period switch to 10 ns. Use both the S-53 STABILITY and LEVEL controls for a stable display of the 10 ns period sine wave display.

Further adjust the Variable control in the signal path for about a 15 mV amplitude display. Check that a stable display can be obtained with the STABILITY AND LEVEL controls.

f. Change the Type 284 Period switch to 1 ns. Use both the STABILITY and the LEVEL controls to obtain a stable display.

CALIBRATION PROCEDURE

Introduction

The Calibration Procedure contains all the adjustments required in the instrument. Troubleshooting information is contained in the Maintenance Section and in the Diagram Section.

Preliminary Procedure

- 1. Assemble the equipment as follows: Install the 7S12 in the center two compartments of the 7504 Oscilloscope. Install the S-6 in the Sampling compartment, and install the Sampling Head Extender into the Pulse Generator compartment of the 7S12. A Type 284 Pulse Generator is used.
- 2. Remove the S-53 from its housing (see the Maintenance Section), and install it onto the Sampling Head Extender.
 - 3. Set the controls as follows:

7504 Indicator Oscilloscope

A Intensity	CCW
B Intensity	CCW
Vertical Mode	Right
Horizontal Mode	Α

. .

7S12 with S-6 and (S-53 on Extender)

(Two center compartments, the right vertical and the A horizontal compartments)

Time Distance dial		Ü
Multiplier		X10
Time/Div		1 μs
Variable		Cal in
Fine (Zero Set)		Fully clockwise
Rep		pushed in
Scan		9 o'clock
Locate	8	pushed in
mV		pushed in
mV/Div		100
Variable		Cal in
DC Offset (& Fine)		Midrange

S-53

STABILITY	Fully Counterclockwise
SLOPE	+
LEVEL	Midrange (set in step 1)

4. Turn on the Type 284, and the Oscilloscope power. After about five minutes warmup, advance the A intensity until a free-running trace is observed. Center the trace on the CRT with the 7S12 DC Offset control.

1. Adjust Trig Zero (R25) and Stability Zero (R30)

- a. Preset R30 fully clockwise (minimum resistance). If the Arm or Start Tunnel Diode circuits have been changed, set R82 and R92 to midrange.
- b. With no input signal, use a Test Oscilloscope with a 1X probe or a sensitive voltmeter connected to Q12 base, and set the LEVEL control for a Q12 base voltage of about -6 mV. Then remove the probe.
- c. Adjust R25 until the oscilloscope trace shows some noise, indicating that the trace is triggered by the S-53. Further set R25 to the center of this indicated triggered range.
- d. Turn R30 counterclockwise until the indicated triggered trace stops. Then adjust R25 to restore the indicated triggered trace. Repeat R30 and R25 adjustments for a maximum counterclockwise rotation of R30 that still maintains the indicated triggered trace.
- e. Connect the Type 284 100 ns square wave output signal through a variable attenuator, GR to BNC adapter to a BNC tee connector. Connect one output of the tee connector through a BNC coaxial cable, BNC to 3 mm adapter to the S-6 Feed Thru (lower) connector. Connect the other output of the tee connector through a BNC coaxial cable to the S-53 INPUT connector.
- f. Set the Variable attenuator in the signal path for a 15~mV signal display on the CRT (15~mV input signal). Set the mV/Div to 10.
- g. Using the LEVEL control obtain a stable display. Set the Variable attenuator to reduce the input signal to about 10 mV.
- If the LEVEL control can be set for a stable display, further adjust R30 slightly counterclockwise until the display is untriggered (repeat if necessary). Then increase the input signal to 15 mV and check for a triggered display. If these conditions are not met, repeat the step.

2. Adjust Arm and Start T.D. Bias (R82), (R92)

a. Disconnect the connecting cables and reconnect as follows: Connect Type 284 Pulse Output through GR to BNC adapter to a BNC tee connector. Connect one output of the tee connector through a 42 inch BNC coaxial cable and a BNC to GR connector to Input 1 of the 7M11 50 Ω Delay Line. Connect 7M11 Output 1 through a GR to BNC adapter, a 42 inch BNC coaxial cable, and a BNC to 3 mm adapter to S-6 Loop Thru (lower connector). Connect the other output from the tee connector through an 18 inch BNC coaxial cable to S-53 INPUT connector.

- b. Push in the m ρ button. Set the m ρ /Div to 100. Set the Multiplier to X.1, and the Time/Div to 2 ns. Set the S-53 LEVEL control for a stable trace. Adjust the m ρ /Div Variable for about a 5 division display. Center the display with the DC Offset control.
- c. Change the $m\rho/Div$ to 10. Change the Time/Div to 50 ps and change the Time-Distance knob to position the center portion of the display to the center of the CRT.
- d. Adjust R82 and R92 for minimum jitter. The display may show a jump to the left before the minimum jitter point during the adjustment.

PARTS LIST ABBREVIATIONS

внв	binding head brass	int	internal
BHS	binding head steel	lg	length or long
cap.	capacitor	met.	metal
cer	ceramic	mtg hdw	mounting hardware
comp	composition	OD	outside diameter
conn	connector	OHB	oval head brass
CRT	cathode-ray tube	OHS	oval head steel
	countersunk	P/O	part of
csk	double end	РНВ	pan head brass
DE		PHS	pan head steel
dia	diameter	plstc	plastic
div	division	PMC	paper, metal cased
elect.	electrolytic	poly	polystyrene
EMC	electrolytic, metal cased	prec	precision
EMT	electrolytic, metal tubular	PT	paper, tubular
ext	external	PTM	paper or plastic, tubular, molded
F & I	focus and intensity	RHB	round head brass
FHB	flat head brass	RHS	round head steel
FHS	flat head steel	SE	single end
Fil HB	fillister head brass	SN or S/N	serial number
Fil HS	fillister head steel	S or SW	switch
h	height or high	TC	temperature compensated
hex.	hexagonal	THB	truss head brass
ННВ	hex head brass	thk	thick
HHS	hex head steel	THS	truss head steel
HSB	hex socket brass	tub.	tubular
HSS	hex socket steel	var	variable
ID	inside diameter	W	wide or width
inc	incandescent	WW	wire-wound

PARTS ORDERING INFORMATION

Replacement parts are available from or through your local Tektronix, Inc. Field Office or representative.

Changes to Tektronix instruments are sometimes made to accommodate improved components as they become available, and to give you the benefit of the latest circuit improvements developed in our engineering department. It is therefore important, when ordering parts, to include the following information in your order: Part number, instrument type or number, serial or model number, and modification number if applicable.

If a part you have ordered has been replaced with a new or improved part, your local Tektronix, Inc. Field Office or representative will contact you concerning any change in part number.

SPECIAL NOTES AND SYMBOLS

\times 000	Part first added at this serial number
$00 \times$	Part removed after this serial number
*000-0000-00	Asterisk preceding Tektronix Part Number indicates manufactured by or for Tektronix, Inc., or reworked or checked components.
Use 000-0000-00	Part number indicated is direct replacement.

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A2 TD DRIVER Circuit Board Assembly		6-3

SECTION 6 ELECTRICAL PARTS LIST

Values are fixed unless marked Variable.

Ckt. No.	Tektronix Part No.	Serial/Mod Eff	lel No. Disc		Descripti	on	
			CHAS	SIS			
			Connec	tors			
J60 J10	131-1004-00 *175-1039-00			Receptacle, elec Cable assembly	trical, BNC, fe	emale	
			Resisto	ors			
Resistors are f	fixed, composition, \pm	:10% unless oth	nerwise indicat	ed,			
R15 R40	311-0546-00 311-0091-00			10 kΩ, Var 1 kΩ, Var			
			Swite	:h			
	Wired or Unwired						
S10	260-0613-00			Toggle	SLC	PE	
		A1 PRE-TE	RIGGER Circ	cuit Board Ass	embly		
	*670-1460-00			Complete B	oard		
			Capac	itors			
Tolerance ±	20% unless otherwise	indicated.					
C3 C5 C10 C10	283-0121-00 283-0121-00 283-0140-00 283-0141-00 283-0121-00	B010100 B020000	B019999	0.001 μ F 0.001 μ F 4.7 pF 200 pF 0.001 μ F	Cer Cer Cer Cer	200 V 200 V 50 V 600 V 200 V	5% 10%
C21 C35 C41 C49 C50	283-0121-00 283-0121-00 283-0032-00 283-0159-00 283-0186-00			$0.001~\mu { m F}$ $0.001~\mu { m F}$ $470~{ m pF}$ $18~{ m pF}$ $27~{ m pF}$	Cer Cer Cer Cer Cer	200 V 200 V 500 V 50 V 50 V	5% 5% 5%

 $\begin{array}{c} \textbf{0.22}~\mu \textbf{F} \\ \textbf{0.22}~\mu \textbf{F} \end{array}$

50 V

50 V

Cer

Cer

C101 C103 283-0198-00

283-0198-00

A1 PRE-TRIGGER Circuit Board Assembly (cont)

Ckt. No.	Tektronix Part No.	Serial/Mo Eff	del No. Disc		Des	cription		
Semiconductor Device, Diodes								
CR6 CR8 CR23 CR25 VR21	*152-0185-00 *152-0185-00 *152-0185-00 152-0177-00 152-0279-00			Silicon Silicon Silicon Tunnel Zener		Replaceable by 1N4 Replaceable by 1N4 Replaceable by 1N4 10 mA, 2 pF 1N751A 400 mW, 5	152 152	
			Conne	ctor				
J50	131-0582-00			Receptacle, e	electrical			
			Transis	stors				
Q10)	*153-0594-00			Silicon	JFET	TO-106 Matched pai	r. N channel	
Q12) Q20 Q22 Q30	151-0294-00 151-0294-00 *151-0190-01			Silicon Silicon Silicon	PNP PNP NPN	μT package, MMT42 μT package, MMT42 TO-106 Tek Spec	.61	
Q40 Q50 Q50	151-0188-00 *151-0269-00 *151-0441-01	B010100 B040000	В039999	Silicon Silicon Silicon	PNP NPN NPN	TO-92 2N3906 TO-106 Selected from		
			Resist	ors				
Resistors are fixe	ed, composition, \pm	10% unless of	herwise indica	ted,				
R2 R3 R4 R5	317-0510-00 321-0228-00 307-0241-00 323-0191-00			51 Ω 2.32 kΩ 100 Ω 953 Ω 200 Ω	1/8 V 1/8 V 0.075 V 1/2 V 1/8 V	V Prec V Prec	5% 1% 1% 5%	
R8	317-0201-00			200 12	78 v	¥	3 /0	
R10 R10 R12 R13 R16	317-0430-00 317-0101-00 317-0510-00 317-0822-00 317-0471-00	B010100 B020000	B019999	43 Ω 100 Ω 51 Ω 8.2 kΩ 470 Ω	1/8 V 1/8 V 1/8 V 1/8 V	V V V	5% 5% 5% 5%	
R17 R18 R19 R20 R21 R22	321-0228-00 307-0241-00 323-0191-00 317-0101-00 317-0102-00 317-0101-00			2.32 kΩ 100 Ω 953 Ω 100 Ω 1 kΩ 100 Ω	1/8 V 0.075 V 1/2 V 1/8 V 1/8 V	V V Prec V	1% 1% 5% 5% 5%	

A1 PRE-TRIGGER Circuit Board Assembly (cont)

Ckt. No.	Tektronix Part No.	Serial/Mo Eff	odel No. Disc		Descrip	rion	
			Resistors	(cont)			
R24 R25 R26 R28 R29	315-0102-00 311-0622-00 317-0680-00 317-0101-00 317-0362-00		NOSSIGNO.	1 kΩ 100 Ω, Var 68 Ω 100 Ω 3.6 kΩ	1/ ₄ W 1/ ₈ W 1/ ₈ W 1/ ₈ W		5% 5% 5%
R30 R32 R33 R35 R41	311-0635-00 317-0202-00 317-0103-00 315-0132-00 317-0752-00			1 kΩ, Var 2 kΩ 10 kΩ 1.3 kΩ 7.5 kΩ	1/8 W 1/8 W 1/4 W 1/8 W		5% 5% 5%
R43 R50	315-0132-00 317-0393-00			1.3 kΩ 39 kΩ	1/ ₄ W 1/ ₈ W		5% 5%
		A2 TD I	ORIVER Circu	uit Board Asse	mbly		
	*670-1461-00			Complete Bo	oard		
			Capac	itors			
Tolerance ±2	20% unless otherwise	indicated.				50.1/	For
C57 C58 C60 C61 C62	283-0186-00 283-0111-00 283-0060-00 283-0175-00 283-0175-00			27 pF 0.1 pF 100 pF 10 pF 10 pF	Cer Cer Cer Cer Cer	50 V 50 V 200 V 200 V 200 V	5% 5% 5% 5%
C65 C70 C70 C85	283-0175-00 283-0142-00 283-0051-00 283-0065-00	B010100 B020000	B019999	$10~{ m pF}$ $0.0027~\mu{ m F}$ $0.0033~\mu{ m F}$ $0.001~\mu{ m F}$	Cer Cer Cer Cer	200 V 200 V 100 V 100 V	5% 5% 5%
		Se	miconductor [Device, Diodes			
CR51 CR61 CR65 CR67 CR68	*152-0185-00 *152-0185-00 *152-0185-00 *152-0185-00 *152-0185-00			Silicon Silicon Silicon Silicon Silicon	Rep Rep Rep	placeable by 1N415 placeable by 1N415 placeable by 1N415 placeable by 1N415 placeable by 1N415	52 52 52
CR70 CR82 CR92 VR67	*152-0185-00 152-0140-01 152-0177-00 152-0280-00			Silicon Tunnel Tunnel Zener	10 10	olaceable by 1N415 mA, 8 pF mA, 2 pF 753A 400 mW, 6.2	
			Conne	ctors			
P50	131-0391-00 *175-1264-00			Receptacle, ele Cable assembly		on male	

A2 TD DRIVER Circuit Board Assembly (cont)

Ckt. No.	Tektronix Part No.	Serial/Mo Eff	del No. Disc		Description	
			Transis	tors		
	*151.0070.00	0010100	D00000	C:I:	NPN TO-106	Selected from SE 3005
Q55 Q55 Q60	*151-0269-00 *151-0441-01 *151-0271-00	B010100 B040000	B039999	Silicon Silicon Silicon	NPN TO-72	Selected from 2N2857 Tek Spec
			Resist	ors		
Resistors are fix	ked, composition, \pm	:10% unless o	therwise indica	ted,		
R51 R52	317-0430-00 317-0430-00			43 Ω 43 Ω	1/ ₈ W 1/ ₈ W 1/ ₈ W	5% 5%
R53 R54 R55	317-0181-00 317-0103-00 317-0104-00			180 Ω 10 kΩ 100 kΩ	1/ ₈ W 1/ ₈ W 1/ ₈ W	5% 5% 5% 5%
R57	317-0103-00			10 kΩ	¹/ ₈ ₩	5%
R58 R60	317-0102-00 317-0750-00			1 kΩ 75 Ω 5.1 kΩ	1/ ₈ W 1/ ₈ W	5% 5% 5%
R61 R62	317-0512-00 317-0103-00			10 kΩ	1/8 W	5%
R63 R65	31 <i>7</i> -0301-00 31 <i>7</i> -0512-00			300 Ω 5.1 kΩ	1/ ₈ W 1/ ₈ W	5% 5%
R67 R68 R70	31 <i>7</i> -0101-00 31 <i>7</i> -0332-00 31 <i>7</i> -0153-00			100 Ω 3.3 kΩ 15 kΩ	1/8 W 1/8 W 1/8 W	5% 5% 5% 5%
R73	317-0434-00			430 kΩ	1/ ₈ W	5%
R75 R76	321-0306-00 317-0622-00 321-0297-00			15 kΩ 6.2 kΩ 12.1 kΩ	1/8 W 1/8 W 1/8 W	5% 5% Prec 1%
R77 R81	317-0103-00			10 kΩ	1/8 W	5%
R82 R84	311-0634-00 315-0112-00			500 Ω , Var 1.1 $k\Omega$	1/4 W	5%
R85 R91 R92	317-0201-00 317-0103-00 311-0634-00			200 Ω 10 kΩ 500 Ω, Var	1/ ₈ W 1/ ₈ W	5% 5%
R94	315-0162-00 317-0101-00			1.6 kΩ 100 Ω	1/ ₄ W 1/ ₈ W	5% 5%
R95 R97	317-0470-00			47 Ω	1/ ₈ W	5% 5%
			Integrate	d Circuit		
U80	156-0048-00			Linear	Replac	ceable by RCA CA3046
6-4						REV. B OCT. 1975

SECTION 7

DIAGRAMS AND CIRCUIT BOARD ILLUSTRATIONS

Symbols and Reference Designators

Electrical components shown on the diagrams are in the following units unless noted otherwise:

Capacitors = Values one or greater are in picofarads (pF).

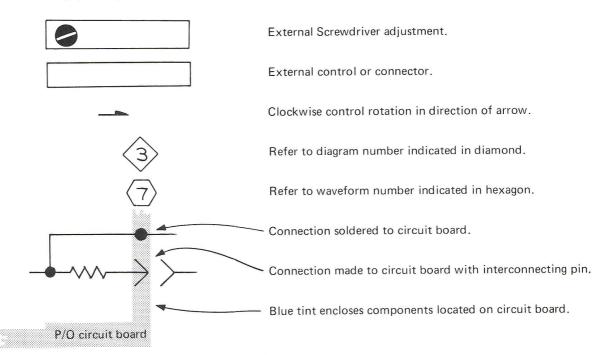
Values less than one are in microfarads (μ F).

Resistors = Ohms (Ω)

Symbols used on the diagrams are based on USA Standard Y32.2-1967.

Logic symbology is based on MIL-STD-806B in terms of positive logic. Logic symbols depict the logic function performed and may differ from the manufacturer's data.

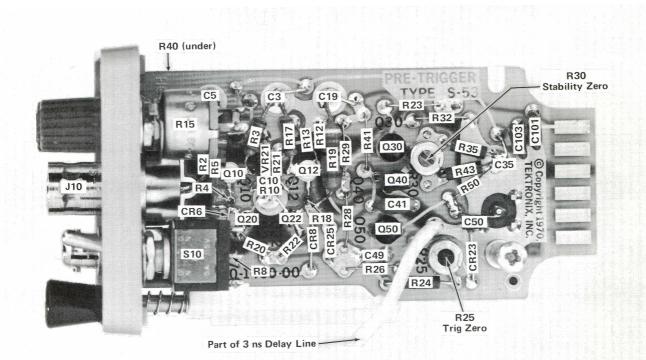
The following special symbols are used on the diagrams:



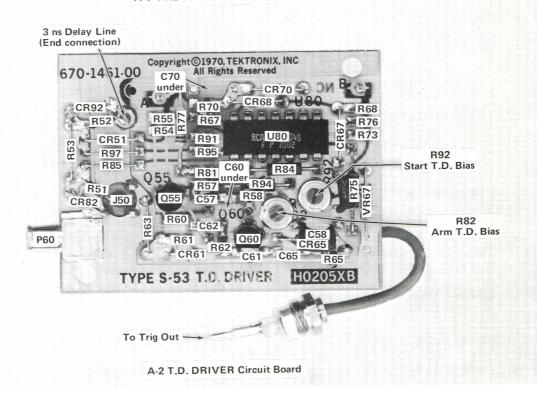
The following prefix letters are used as reference designators to identify components or assemblies on the diagrams.

- A Assembly, separable or repairable (circuit board, etc.)
- AT Attenuator, fixed or variable
- B Motor
- BT Battery
- C Capacitor, fixed or variable
- CR Diode, signal or rectifier
- DL Delay line
- DS Indicating device (lamp)
- F Fuse
- FL Filter
- H Heat dissipating device (heat sink, heat radiator, etc.)
- HR Heater
- J Connector, stationary portion
- K Relay
- L Inductor, fixed or variable

- LR Inductor/resistor combination
- M Meter
- Q Transistor or silicon-controlled rectifier
- P Connector, movable portion
- R Resistor, fixed or variable
- RT Thermistor
- S Switch
- T Transformer
- TP Test point
- U Assembly, inseparable or non-repairable (integrated circuit, etc.)
- V Electron tube
- VR Voltage regulator (zener diode, etc.)
- Y Crystal

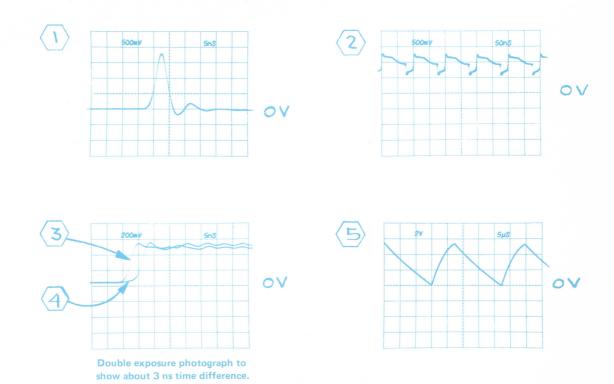


A-1 PRE-TRIGGER Circuit Board

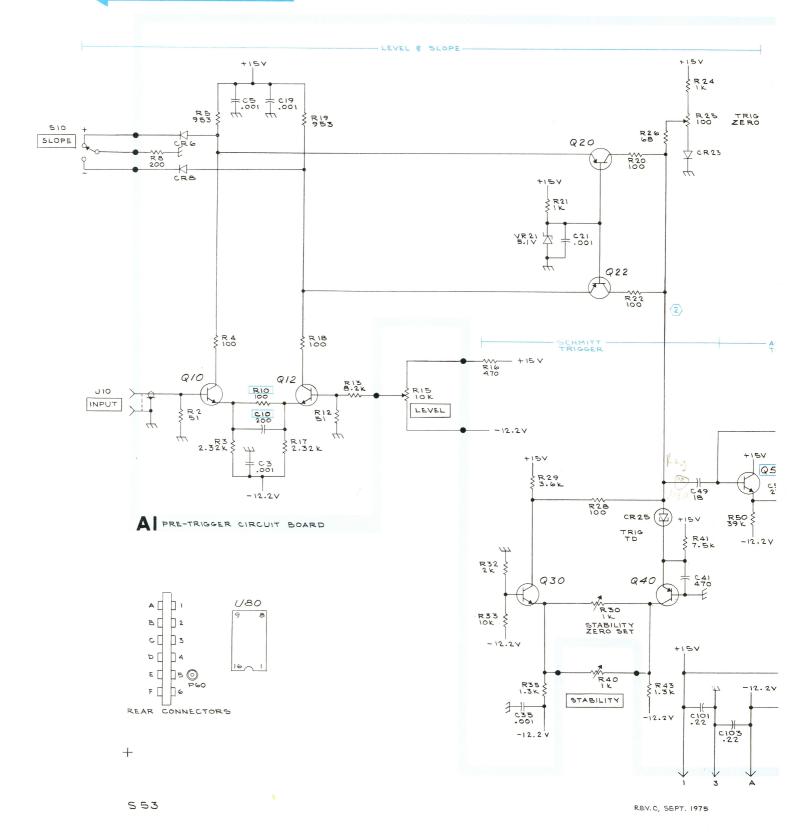


Troubleshooting Conditions

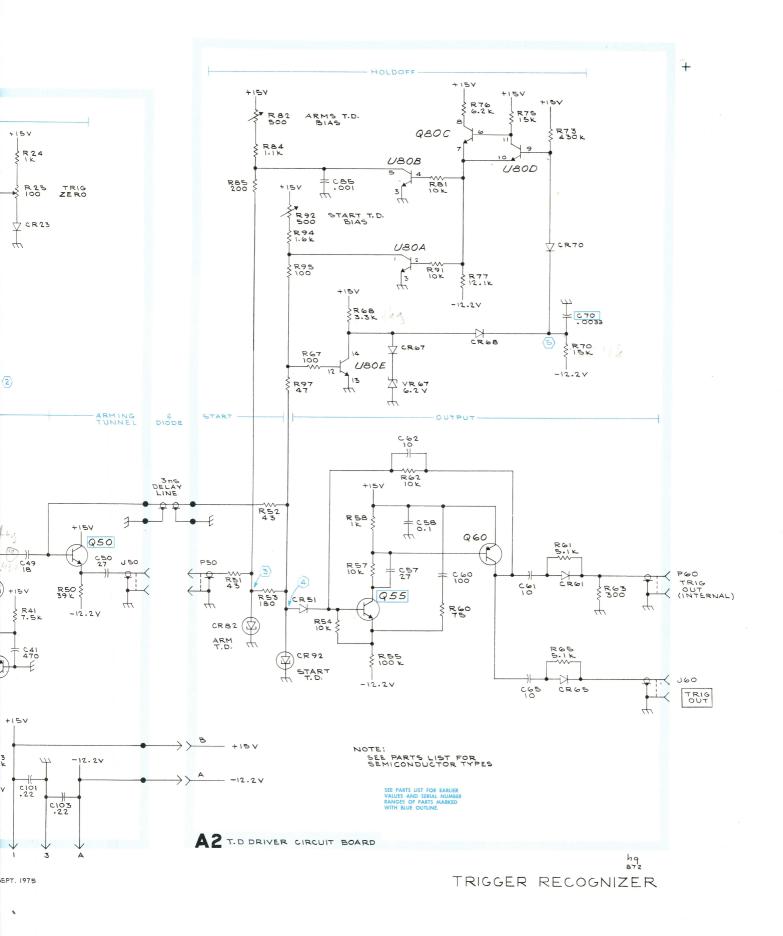
Power is obtained from Type 285 Power Supply, or sampling head compartment. Use a sampling Head Extender so that the Type S-53 case can be removed. Trigger the Test Oscilloscope from the TRIG OUT pulse to show time relationship. 100 mV-100 ns square wave input from Type 284 to S-53 Input.



Troubleshooting waveforms were obtained with 7A16, 7B50, and 7503. Signals for waveforms (No. 1) were coaxially connected and 50 Ω terminated, and all others were connected with P6053 10X probe with a short ground lead. DC coupling was used for all waveforms.



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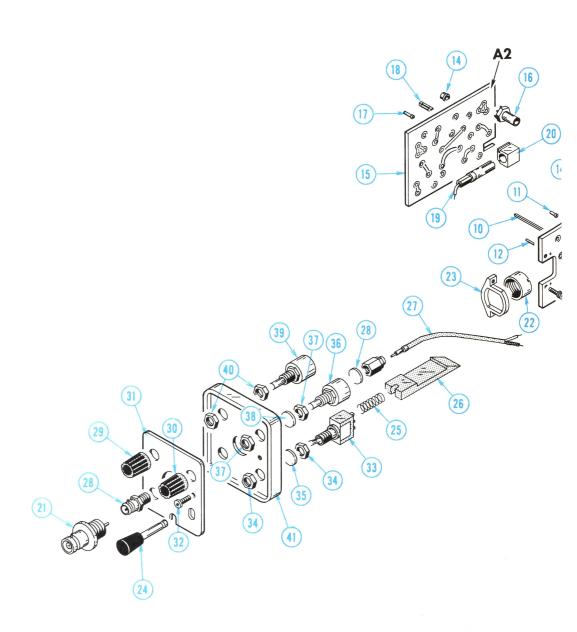
SECTION 8 MECHANICAL PARTS LIST

FIGURE 1 EXPLODED & STANDARD ACCESSORIES

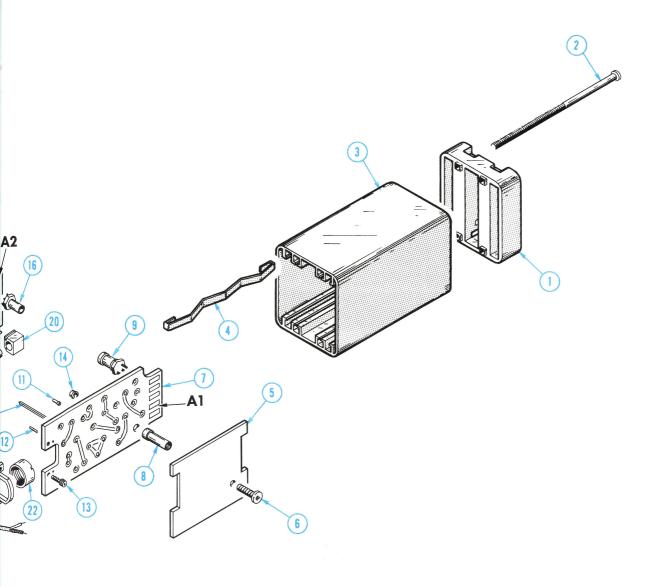
Fig. & Index No.	Tektronix Part No.	Serial/Model Eff	No. Disc	Q t y	Description 1 2 3 4 5
1-1	386-1337-15			1	PANEL, rear
-2	211-0141-00			4	mounting hardware: (not included w/panel) SCREW, 4-40 x 3.25 inches, PHS
-3	380-0233-00			1	HOUSING housing includes:
-4	131-0555-00			4	CONTACT
-4 -5	276-0174-00			i	FORM, delay line
				-	mounting hardware: (not included w/form)
-6	211-0008-00	*		1	SCREW, 4-40 x 0.25 inch, PHS
-7	670-1460-00			1	CIRCUIT BOARD ASSEMBLY—PRE-TRIGGER A1 circuit board assembly includes:
	388-2011-00			1	CIRCUIT BOARD
-8	351-0184-00			i	GUIDE-POST, lock, 4-40 x 0.58 inch long
-9	131-0582-00			1	CONNECTOR, receptacle
-10	131-0591-00			2	TERMINAL, pin, 0.835 inch long
-11	136-0252-04			9	SOCKET, pin connector
-12	214-1081-00			2	PIN, spring, spiral
				-	mounting hardware (not included w/circuit board assembly)
-13	211-0162-00			2	SCREW, 2-56 x 0.188 inch, SHS
-14	210-0707-00			2	EYELET, 0.089 inch ID
	670-1461-00			1	CIRCUIT BOARD ASSEMBLY—T.D. DRIVER A2
				_	circuit board assembly includes:
	388-2012-00			1	CIRCUIT BOARD
-16	131-0391-00			70	CONNECTOR, coaxial, male SOCKET, pin connector
-17	136-0252-04			20 2	SOCKET, pin connector SOCKET, connector pin
-18 -19	136-0263-03 175-1264-00			1	CABLE ASSEMBLY, w/connector
-20	352-0133-00			i	HOLDER, connector
-21	131-0126-00			i	CONNECTOR, receptacle, female, BNC
				-	mounting hardware: (not included w/connector)
-22	220-0545-00			1	NUT, retaining, BNC, 0.50-28 x 0.531 inch OD
-23	352-0178-01			1	HOLDER, connector

FIGURE 1 EXPLODED & STANDARD ACCESSORIES (cont)

Fig. & Index No.	Tektronix Part No.	Serial/ <i>N</i> Eff	Nodel No. Disc	Q t y	Description 1 2 3 4 5
1-24 -25	384-0687-00 214- 12 26-01	B010100 B010100 B010100	B029999 B029999 B029999	1 1 1	SHAFT, latch SPRING, helical compression STRIKE, latch
-26	105-0066-00 105-0338-00 354-0163-00	B030000	B027777	1	LATCH ASSEMBLY latch assembly includes: RING, retaining
-27	384-0687-01 105-0336-00 175-1039-00			1 1 1	SHAFT, extension, latch STRIKE, latch CABLE ASSEMBLY cable assembly includes:
-28 -29	131-0579-00 366-1023-00 366-1023-01	B010100 B020000	B019999	1 2 1	CONNECTOR, coaxial, w/hardware KNOB, gray—STABILITY KNOB, gray—STABILITY knob includes:
-30	213-0153-00 366-1023-00 366-1023-01	B010100 B020000	B019999	1 1 1	SETSCREW, 5-40 x 0.125 inch, HSS KNOB, gray—LEVEL KNOB, gray—LEVEL knob includes:
-31	213-0153-00 333-1424-00			1	SETSCREW, 5-40 x 0.125 inch, HSS PANEL, front mounting hardware: (not included w/panel)
-32	211-0022-00			1	SCREW, 2-56 x 0.188 inch, RHS SWITCH, toggle—SLOPE
-33 -34 -35	260-0613-00 210-0562-00 210-0046-00			1 - 2 1	mounting hardware: (not included w/switch) NUT, hex., 0.25-40 x 0.312 inch WASHER, lock, internal, 0.261 ID x 0.40 inch OD
-36				1	RESISTOR, variable mounting hardware: (not included w/resistor)
-37 -38	210-0583-00 210-0046-00			2	NUT, hex., $0.25-32 \times 0.312$ inch WASHER, lock, internal, 0.261 ID $\times 0.40$ inch OD
-39 -40	210-0583-00			1 - 2	RESISTOR, variable mounting hardware: (not included w/resistor) NUT, hex., 0.25-32 x 0.312 inch
-41	386-1338-17			1	SUBPANEL, front
			STANDARD	ACCI	ESSORIES (not shown)
	011-0059-01 012-0057-01 070-1147-00			1 1 1	ATTENUATOR, 50 Ω , 10X, 2 W, BNC CABLE ASSEMBLY, coaxial, 50 Ω MANUAL, instruction







MANUAL CHANGE INFORMATION

At Tektronix, we continually strive to keep up with latest electronic developments by adding circuit and component improvements to our instruments as soon as they are developed and tested.

Sometimes, due to printing and shipping requirements, we can't get these changes immediately into printed manuals. Hence, your manual may contain new change information on following pages.

A single change may affect several sections. Since the change information sheets are carried in the manual until all changes are permanently entered, some duplication may occur. If no such change pages appear following this page, your manual is correct as printed.

SERVICE NOTE

Because of the universal parts procurement problem, some electrical parts in your instrument may be different from those described in the Replaceable Electrical Parts List. The parts used will in no way alter or compromise the performance or reliability of this instrument. They are installed when necessary to ensure prompt delivery to the customer. Order replacement parts from the Replaceable Electrical Parts List.

CALIBRATION TEST EQUIPMENT REPLACEMENT

Calibration Test Equipment Chart

This chart compares TM 500 product performance to that of older Tektronix equipment. Only those characteristics where significant specification differences occur, are listed. In some cases the new instrument may not be a total functional replacement. Additional support instrumentation may be needed or a change in calibration procedure may be necessary.

Comparison of Main Characteristics

	Comparison of Main Character	istics
DM 501 replaces 7D13	2	
PG 501 replaces 107 108 111 114 115	PG 501 - Risetime less than 3.5 ns into 50 Ω. PG 501 - 5 V output pulse; 3.5 ns Risetime. PG 501 - Risetime less than 3.5 ns; 8 ns Pretrigger pulse delay. PG 501 - ±5 V output. PG 501 - Does not have Paired, Burst, Gated, or Delayed pulse mode; ±5 V dc Offset. Has ±5 V output.	 107 - Risetime less than 3.0 ns into 50 Ω. 108 - 10 V output pulse; 1 ns Risetime. 111 - Risetime 0.5 ns; 30 to 250 ns Pretrigger Pulse delay. 114 - ±10 V output. Short proof output. 115 - Paired, Burst, Gated, and Delayed pulse mode; ±10 V output. Short-proof output.
PG 502 replaces 107 108 111 114 115	PG 502 - 5 V output PG 502 - Risetime less than 1 ns; 10 ns Pretrigger pulse delay. PG 502 - ±5 V output PG 502 - Does not have Paired, Burst, Gated, Delayed & Undelayed pulse mode; Has ±5 V output. PG 502 - Does not have Paired or Delayed pulse. Has ±5 V output.	108 - 10 V output. 111 - Risetime 0.5 ns; 30 to 250 ns Pretrigger pulse delay. 114 - ±10 V output. Short proof output. 115 - Paired, Burst, Gated, Delayed & Undelayed pulse mode; ±10 V output. Short-proof output. 2101 - Paired and Delayed pulse; 10 V output.
PG 506 replaces 106 067-0502-01	PG 506 - Positive-going trigger output signal at least 1 V; High Amplitude output, 60 V. PG 506 - Does not have chopped feature.	106 - Positive and Negative-going trigger output signal, 50 ns and 1 V; High Amplitude output, 100 V. 0502-01 - Comparator output can be alternately chopped to a reference voltage.
SG 503 replaces 190, 190A, 190B 191 067-0532-01	SG 503 - Amplitude range 5 mV to 5.5 V p-p. SG 503 - Frequency range 250 kHz to 250 MHz. SG 503 - Frequency range 250 kHz to 250 MHz.	190B - Amplitude range 40 mV to 10 V p-p. 191 - Frequency range 350 kHz to 100 MHz. 0532-01 - Frequency range 65 MHz to 500 MHz.
TG 501 replaces 180, 180A	TG 501 - Marker outputs, 5 sec to 1 ns. Sinewave available at 5, 2, and 1 ns. Trigger output - slaved to marker output from 5 sec through 100 ns. One time-mark can be generated at a time. TG 501 - Marker outputs, 5 sec to 1 ns. Sine- wave available at 5, 2, and 1 ns. TG 501 - Marker outputs, 5 sec to 1 ns. Sine- wave available at 5, 2, and 1 ns. Trigger output - slaved to marker output from 5 sec through 100 ns. One time-mark can be generated at a time.	180A - Marker outputs, 5 sec to 1 μs. Sinewave available at 20, 10, and 2 ns. Trigger pulses 1, 10, 100 Hz; 1, 10, and 100 kHz. Multiple time-marks can be generated simultaneously. 181 - Marker outputs, 1, 10, 100, 1000, and 10,000 μs, plus 10 ns sinewave. 184 - Marker outputs, 5 sec to 2 ns. Sinewave available at 50, 20, 10, 5, and 2 ns. Separate trigger pulses of 1 and .1 sec; 10, 1, and .1 ms; 10 and 1 μs. Marker amplifier provides positive or negative time marks of 25 V min. Marker intervals of 1 and .1 sec; 10, 1, and .1 ms; 10 and 1 μs.
2901	TG 501 - Marker outputs, 5 sec to 1 ns. Sinewave available at 5, 2, and 1 ns. Trigger output - slaved to marker output from 5 sec through 100 ns. One time-mark can be generated at a time.	2901 - Marker outputs, 5 sec to 0.1 µs. Sinewave available to 50, 10, and 5 ns. Separate trigger pulses, from 5 sec to 0.1 µs. Multiple time-marks can be generated simultaneously.

NOTE: All TM 500 generator outputs are short-proof. All TM 500 plug-in instruments require TM 500-Series Power Module.



MANUAL CHANGE INFORMATION

M36783 __ Date: <u>4-12-79</u> Change Reference: ____

Product: _

S53 070-1147-00 EFF SN B050690

CHANGE

DESCRIPTION

ELECTRICAL PARTS LIST AND SCHEMATIC CHANGES

CHANGE TO:

A1

670-1460-01 PRE-TRIGGER Circuit Board Assembly

ADD:

R49

317-0430-00

43 Ω

1/8 W

5%

CHANGE TO:

A2

670-1461-01

TD DRIVER Circuit Board Assembly

R68

317-0392-00

3.9 $k\Omega$

1/8 W

5%

R70

317-0123-00

12 $k\Omega$

1/8 W

5%

DIAGRAM - TRIGGER RECOGNIZER - Partial

