

DESCRIPTION

This Trigger Circuit is intended for use up to 50 MHz. It contains a triggering circuit, a trigger signal preamplifier, and a trigger view amplifier. The trigger signal input and level input are designed  $\pm 0.5$  V signals referenced to ground. The M131A is designed to interface with the M132A sweep control and M133A sweep and delay pickoff integrated circuits to form a complete horizontal system. Operating voltages for the M131A are  $\pm 5$  V at a power dissipation of approximately 310 mW.

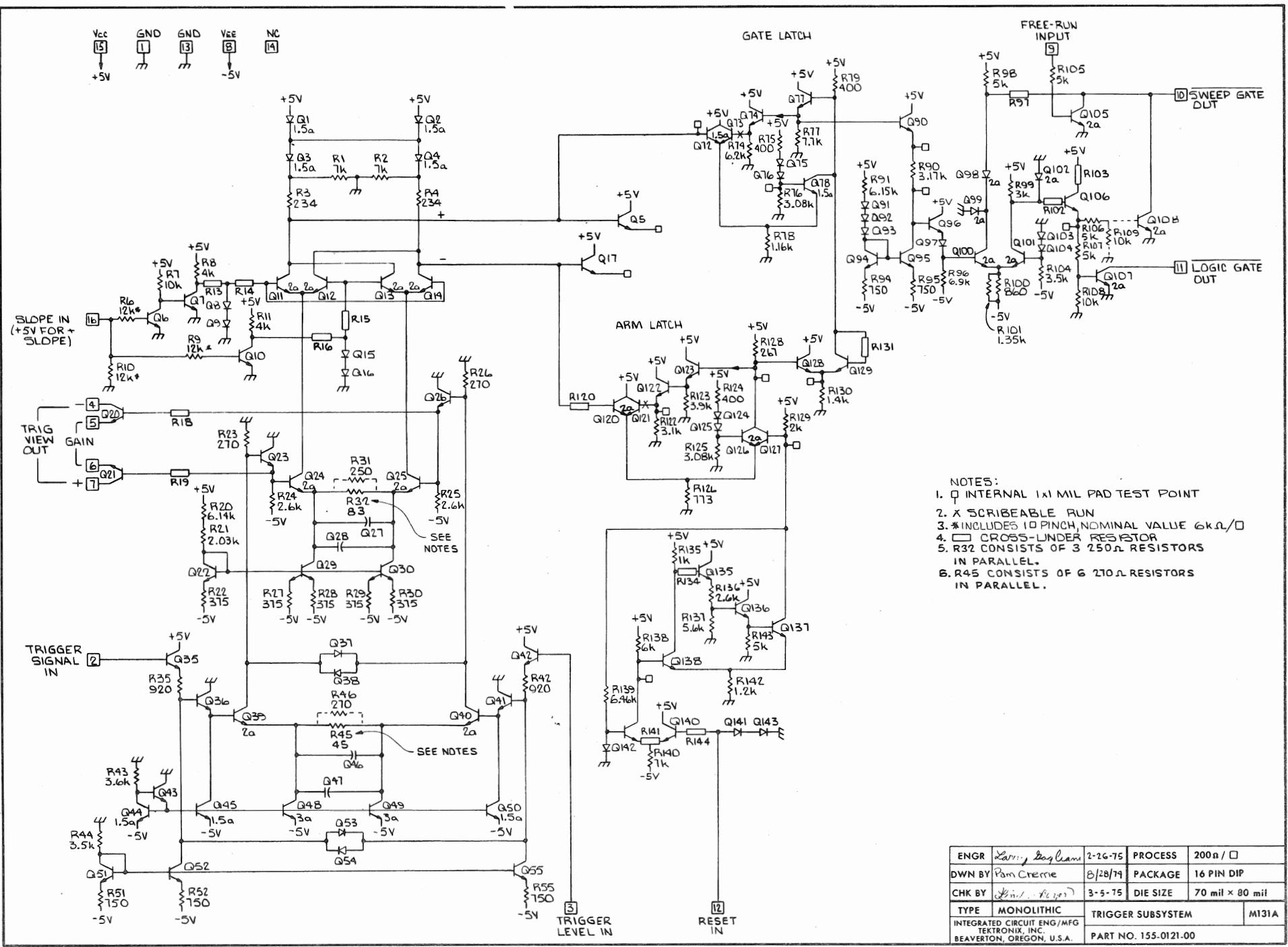
PROCESS . . . . . 200  $\Omega$ /Sq

POWER SUPPLY. . . . .  $V_{CC}$  +5V,  $V_{EE}$  -5V

PACKAGE . . . . . 16 Pin DIP

DESIGNER . . . . . Larry Gagliani

INSTRUMENT USAGE . . . . . 455



- NOTES:
- INTERNAL 1x1 MIL PAD TEST POINT
  - X SCRIBABLE RUN
  - \* INCLUDES 10 PINCH, NOMINAL VALUE 6kΩ/□
  - CROSS-UNDER RESISTOR
  - R32 CONSISTS OF 3 250Ω RESISTORS IN PARALLEL.
  - R45 CONSISTS OF 6 270Ω RESISTORS IN PARALLEL.

ENGR	Larry Englem	2-26-75	PROCESS	200n / □
DWN BY	Pam Christie	8/28/79	PACKAGE	16 PIN DIP
CHK BY	Steve Roper	3-5-75	DIE SIZE	70 mil x 80 mil
TYPE	MONOLITHIC	TRIGGER SUBSYSTEM		M131A
INTEGRATED CIRCUIT ENG/MFG			PART NO. 155-0121-00	
TEKTRONIX, INC.				
BEAVERTON, OREGON, U.S.A.				