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THE DRAWING TYPE DESIGNATION AND APPROVED SOURCES OF SUPPLY FOR THIS ITEM ARE LISTED ON THE PURCHASED ITEM SOURCE LIST (PISL) FOR THIS PART NUMBER.

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FINISH									
TITLE	HORIZONTAL CLAMP	): H	475						

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#### 1.0 DESCRIPTION

The 155-0179-00 (H475) is a symmetrical soft clamp designed for use in a 50  $\Omega$  push-pull system. The point at which clamping occurs is externally controllable, and a transistor is included on the hybrid to allow temperature compensation of the clamp point.

The circuit is on a 1.22 cm square alumina substrate, and is mounted on the circuit board with a HYPCON connector.

When used in 50  $\Omega$  system, the 50  $\Omega$  environment is maintained except when clamping occurs. The hybrid has an insertion loss of approximately 3 dB.

The active devices are manufactured using the SHF III process.

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### 2.0 ABSOLUTE MAXIMUM RATINGS

# 2.1 <u>Electrical</u>

Voltage at Pin 5 Relative to Pin 6	+2 Volts
Voltage at Pin 8 Relative to Pin 6	+2 Volts
Voltage at Pin 13 Relative to Pin 6	3.2 Volts
Voltage at Pin 16 Relative to Pin 6	3.2 Volts
Voltage at Pin 3 Relative to Pin 1	4 Volts
Voltage at Pin 2 Relative to Pin 1	**
Collector Current in Q1	30 mA
Collector Current in Q2	30 mA
Collector Current in Q3	30 mA
Voltage at Pin 16 Relative to Pin 5	2.75 Volts
Voltage at Pin 13 Relative to Pin 8	2.75 Volts
Junction Temperature, Any Device	200°C
Voltage at Pin 11 Relative to All Other Pins	25 Volts

<sup>\*\*</sup>See Section 8.0 (Applications Information) for collector-base breakdown information on Q3.

## 2.2 <u>Environmental</u>

Storage Temperature	•	•	•	•	•	•	•	•	٠.	•		•	-55°C to	125°	C

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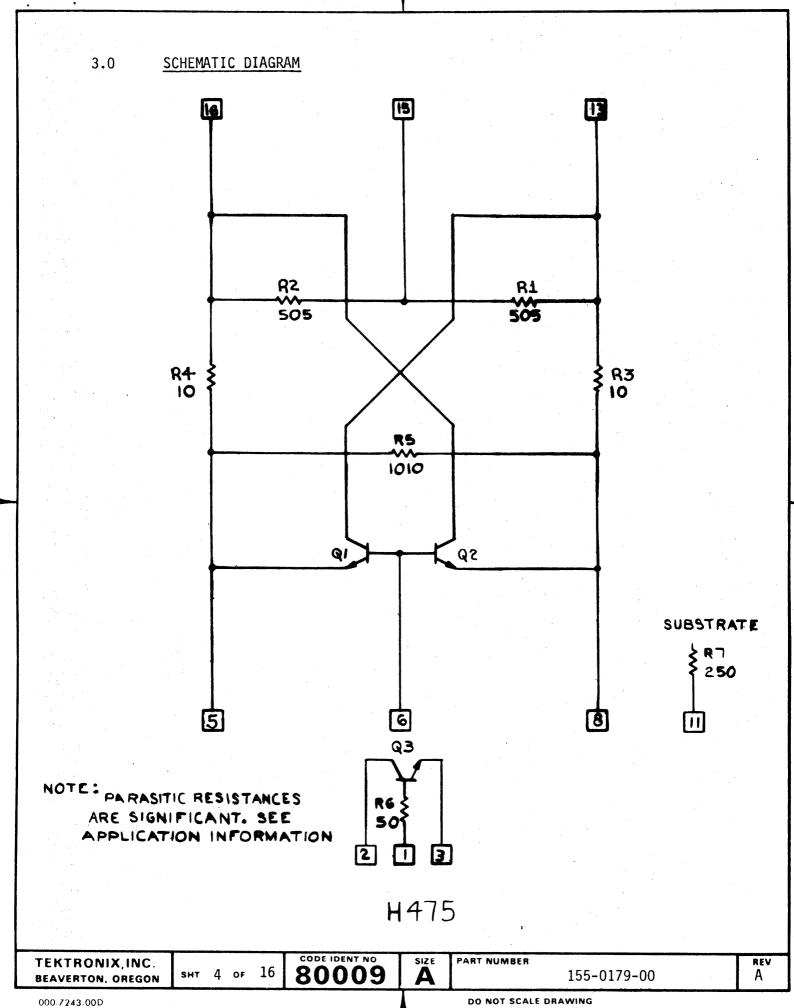
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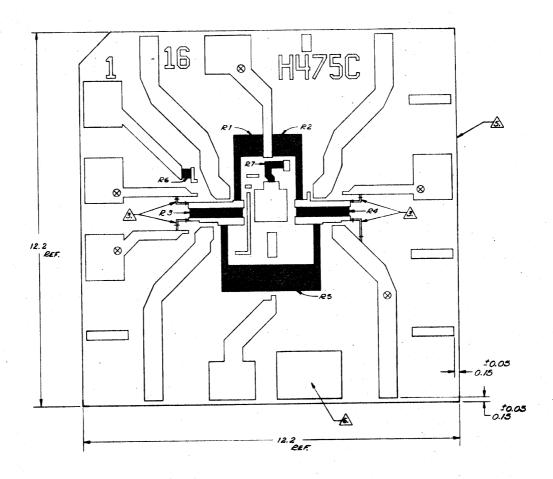
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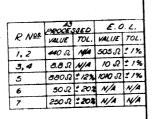
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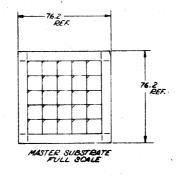
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#### 3.1 Layout Drawing







A SCALE: 25.4:1 OR (IM=Im=m).

C PAD TOR SERULIZATION BY LASER IMPRIMED NUMBERS IF DESIRED.

SONE OF 25 IDENTICAL PARTS ON MASTER SUBSTRATE.

4 SCRIBE SPECIFICATIONS # ----

A SCRUB JUMPERS (4), IN AREA SHOWN, AFTER TRIM.

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METRIC

#### 3.2 Resistor Values

$R_1$	٠.	•		•	•	•	505	Ω	±	2%
							505			
							10			
							10			
R <sub>5</sub>	•		•	•	•	•	1010	Ω	±	2%
R <sub>6</sub>	•		•	•	•	• 1	50	Ω	±	20%
R <sub>7</sub>	, •	•	•	• .	•	. •	250	Ω	±	<b>2</b> 0%

## 4.0 PARAMETRIC DEFINITIONS

# 4.1 $\frac{V_{ON_{6-5}}}{}$

The voltage at Pin 6 relative to Pin 5 necessary to cause 5 mA of current to flow through Q1.

# 4.2 $\frac{v_{on_{6-8}}}{}$

The voltage at Pin 6 relative to Pin 5 necessary to cause 5 mA of current to flow through Q2.

# 4.3 VOFFSET

The difference between  $^{V}\mathrm{ON}_{6-5}$  and  $^{V}\mathrm{ON}_{6-8}$  above. This is an indicator of clamp point non-symmetry with respect to electrical center.

### 5.0 PARAMETRIC SUMMARY

	•					
<b>VO</b>	SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNITS
	V <sub>0N6-5</sub>	Voltage at Pin 6 relative to Pin 5 which makes the	Pin 5 connected to current source of -85 mA	0.78	0.88	V
		voltage at Pin 16 relative to Pin 13 be 0.412 volts	Pin 8 connected to current source of -65 mA			
•			Pin 13 connected through 50 $\Omega$ to a positive supply			
			Pin 16 connected through 50 $\Omega$ to same positive supply			
1			Pins 1, 2, 3, and 15 are not connected			
	I <sub>6-5</sub> Bias	Bias current in- to Pin 6 which	Same as #1		125	μΑ
		makes the voltage at Pin 16 relative to Pin 13 be 0.412 volts				
	V <sub>ON</sub> 6-8	Voltage at Pin 6 relative to Pin 8 which makes the	Pin 5 connected to current source of -65 mA	0.78	0.88	٧
		voltage at Pin 13 relative to Pin 16 be 0.412 volts	Pin 8 connected to current source of -85 mA			
			Pin 13 connected through 50 $\Omega$ to a positive supply			
			Pin 16 connected through 50 $\Omega$ to same positive supply			
			Pins 1, 2, 3, and 15 are not connected			
	I <sub>6-8</sub> Bias	Bias current in- to Pin 6 which	Same as #3		125	μΑ
		makes the voltage at Pin 13 relative to Pin 16 be 0.412 volts		. •		

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# 5.0 PARAMETRIC SUMMARY (continued)

NO	SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNITS
5	V <sub>OFFSET</sub>	V <sub>OFFSET</sub>	$v_{OFFSET} = v_{ON_{6-5}} - v_{ON_{6-8}}$	-30	+30	mV
6	V <sub>ON1-3</sub>	Voltage at Pin 1 relative to Pin 3	Pin 2 is 5 volts above Pin 1	0.75	0.85	V
			Pin 3 is connected to a current source of -2 mA			
7	<sup>I</sup> 1 Bias(10)	Current into Pin 1	Pin 2 is 10 volts above Pin 1		50	μА
			Pin 3 is connected to a current source of -2 mA			
8	I <sub>1 Bias(3)</sub>	Current into Pin 1	Pin 2 is 3 volts above Pin 1		50	μΑ
			Pin 3 is connected to a current source of -2 mA			
9	<sup>R</sup> 5-16	Resistance measur- ed between Pins 5 and 16	All other pins open	10.76	10.98	Ω
10	R <sub>8-13</sub>	Resistance measur- ed between Pins 8 and 13	All other pins open	10.58	10.79	Ω

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# 6.0 PACKAGING

# 6.1 <u>Terminal Identification</u>

PIN	NAME	INPUT/OUTPUT
1	Emitter Follower	Input
2	Emitter Follower	V <sub>CC</sub>
3	Emitter Follower	Output
5	+Input	
6	Clamp Level Input	
8	-Input	
11	Substrate Bias	
13	-Output	
15	Output Common Mode Sense	
16	+Output	

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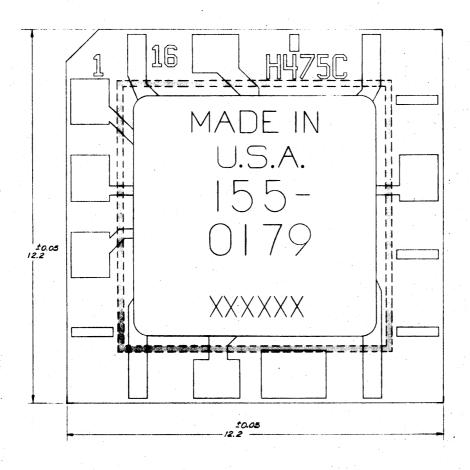
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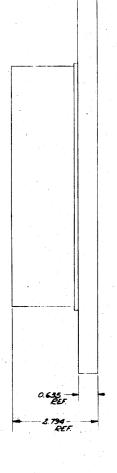
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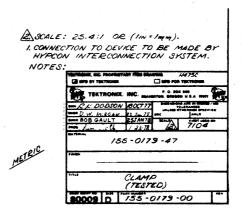
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#### 6.2 Outline Drawing







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#### 6.3 Thermal Characteristics

Not Applicable

#### 7.0 RELIABILITY STATEMENT

 $\lambda$ , Failure Rate;  $\leq$  .1%/1K hours at 75°C T  $_j$  MTTF  $\geq$   $10^6$  hours at 75°C T  $_j$  Expected instrument life; 10K hours Life Test Results (90% Lower Confidence Limits)  $\lambda$  , .078%/1K hours at 75°C T  $_j$  Life Test Report #RA-28

#### 8.0 APPLICATIONS INFORMATION

The H475 would typically be used in a CRT Deflection Amplifier to limit the dynamic range of the signal applied to the amplifier's output stages.

Approximately 75 mA of standing current per side is required for proper operation. This current would typically be obtained by using this hybrid at the output of an H442 Channel Switch, and routing the channel switch standing current through the H475.

 ${\tt Q3}\ {\tt is}\ {\tt autoprobed}\ {\tt to}\ {\tt meet}\ {\tt or}\ {\tt exceed}\ {\tt the}\ {\tt following}\ {\tt parameters:}$ 

 $BV_{CBO} = 12 \text{ Volts}$   $BV_{CES} = 8 \text{ Volts}$  $LV_{CFO} = 5.5 \text{ Volts}$ 

The following page shows a schematic diagram of the part with parasitic resistances added, and indicates the source of the resistance.

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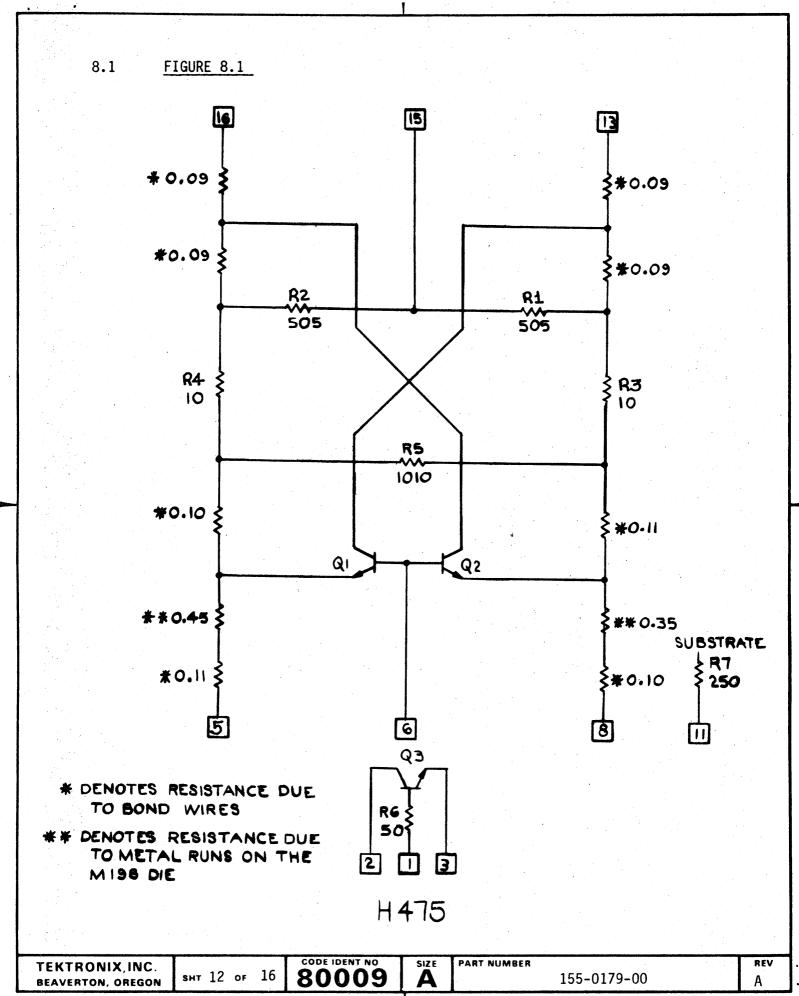
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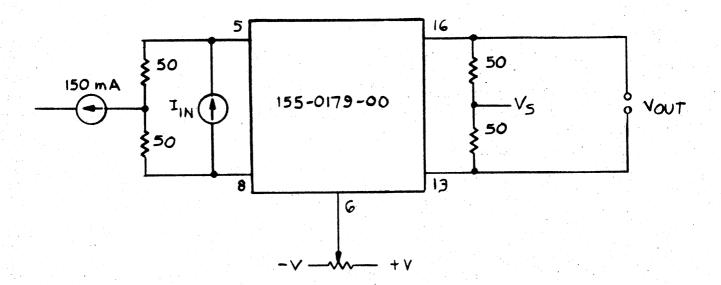
#### 8.0 APPLICATIONS INFORMATION (continued)

#### 8.2 Clamping Characteristics of the 155-0179-00 (H475)

The following graphs (on Pages 15 and 16) show the clamping characteristics of the 155-0179-00 as it is used in the Horizontal Amplifier of the 7104 Oscilloscope.

In this application, the hybrid is used to limit an input of  $\pm$  15 divisions to an output of  $\pm$  10 divisions. The graphs show clamping in divisions from electrical center.

The following is a simplified schematic to show the bias conditions of the hybrid under which the data was taken.



The signal levels are such that, on the graphs and the schematic, 1 division = 66 mV at the output.

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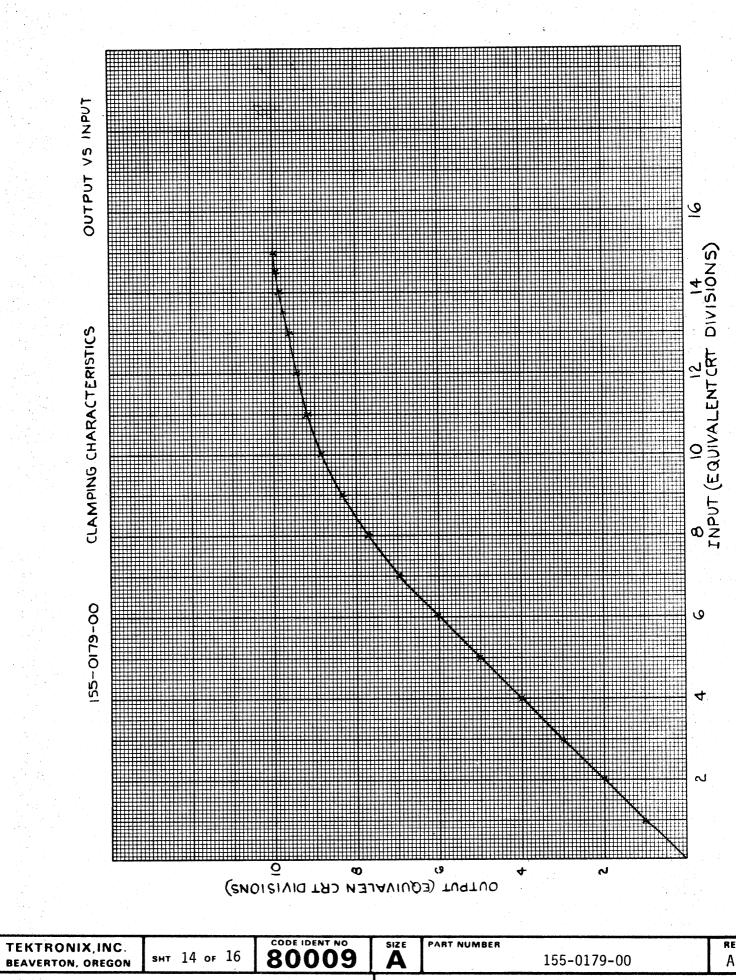
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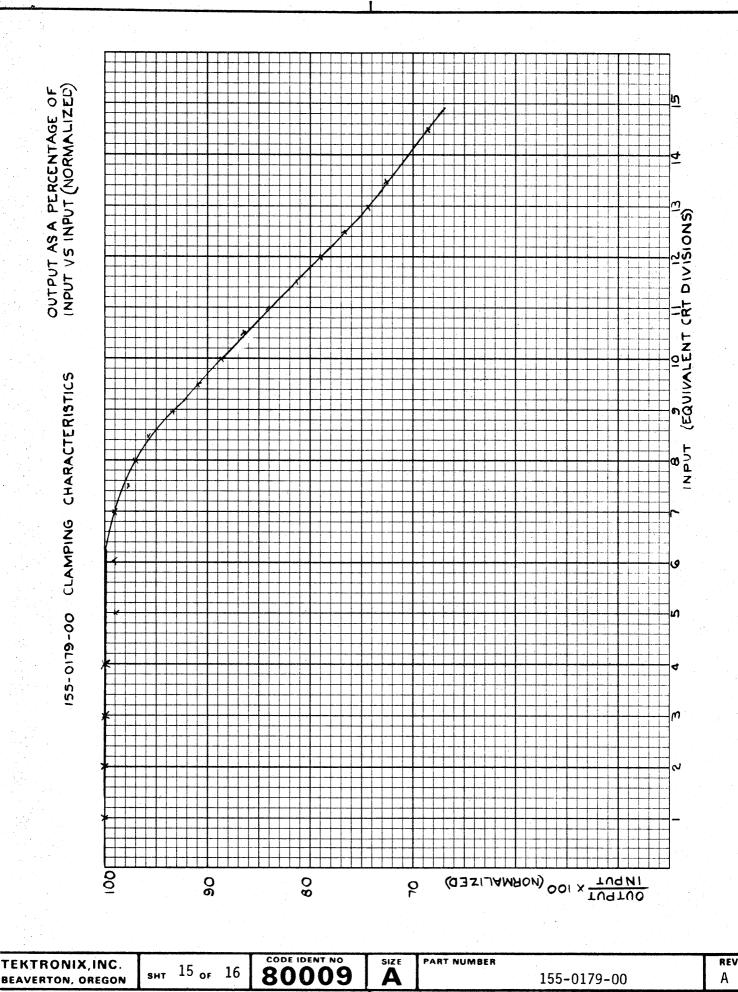
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DO NOT SCALE DRAWING

9.0 REFERENCE LIST

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SOURCE

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