

IN Production

M115A

155-0118-00

7L5 VERTICAL CONTROL

Description:

The M115 is a MOS IC designed to allow the user to select a reference level within the dynamic range of most instruments. The selection may be made through a knob command, or it may be remotely programmed. Scale factor selection is also included.

The IC accepts offset constant inputs which may be used to control the upper limit of the instruments dynamic range. An invalid state input allows the user to also truncate the lower limit of operation if desired.

The M115 counter states are direct outputs which may be used to control external circuitry such as amplifiers and attenuators.

The circuit is designed to accept TTL inputs. The outputs (other than readout) will interface directly to LPTTL or CMOS circuits.

The M115 provides for logarithmic (dB) or linear readout. It is designed to interface directly with the 7000 series CRT readout system and/or with a BCD-to-seven segment display decoder/driver.

Power Supply: $V_{DD} = -15V$  $V_{CC} = +5.0V$ Package:

40 PIN Leadless

Designer:

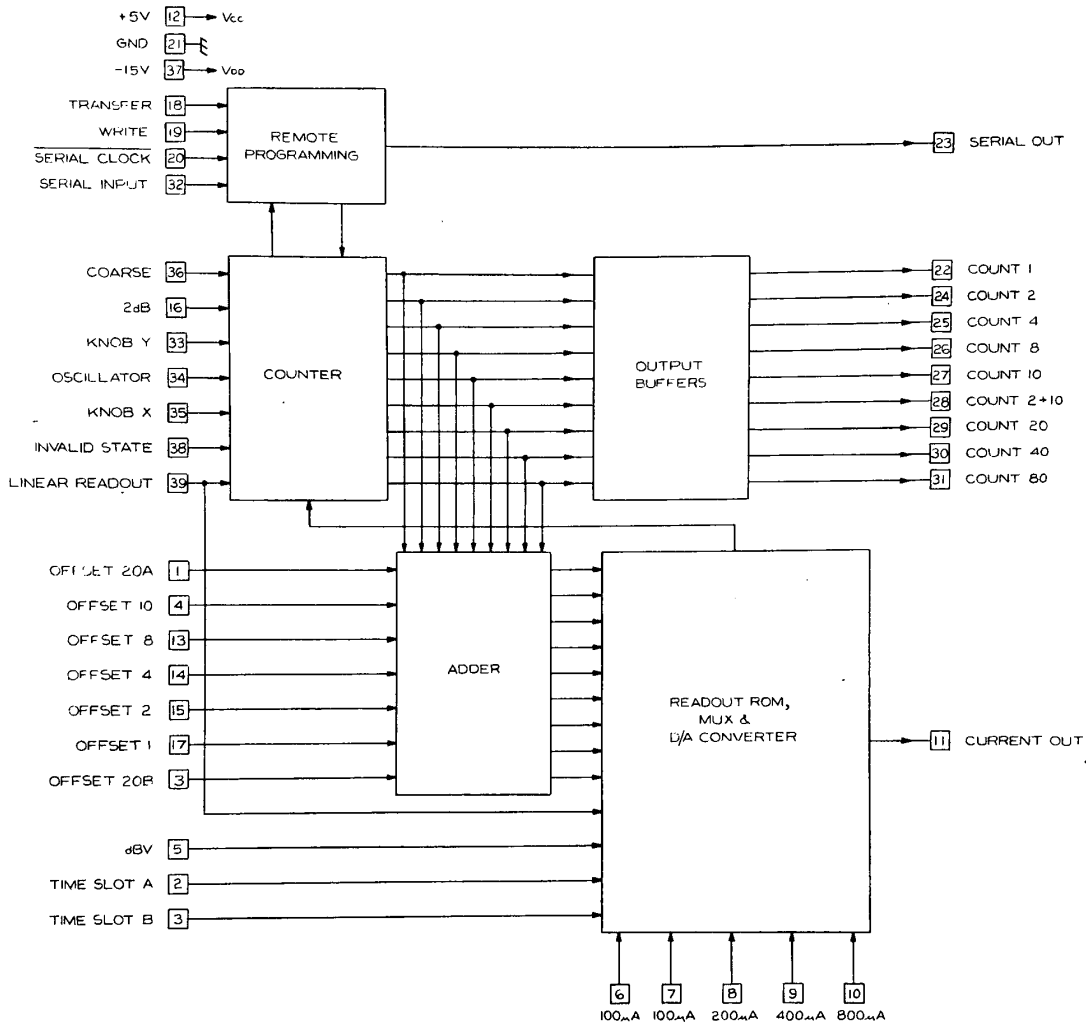
Dave Allstott

ICE Contact:

Randy Young

Instrument Usage:

7L5



ENGR	da Finke	2-10-76	PROCESS	P-MOS
DWN BY	J. Langley	2-6-76	PACKAGE	40 PIN LEADLESS
CHK BY	<i>Flunsky</i>	4-21-76	DIE SIZE	151mil X 173mil
TYPE	MONOLITHIC		7LS VERTICAL CONTROL BLOCK DIAGRAM	M115A
INTEGRATED CIRCUIT ENG/MFG TEKTRONIX, INC. BEAVERTON, OREGON, U.S.A.			PART NO. 155-0118-00	