

DESCRIPTION

This circuit is intended for use in high frequency instruments. It employs SHF III processing, ECL/E^L circuitry, dissipates about 600 mW, and is packaged in a 1.25 cm x 1.25 cm hybrid substrate (155-0150-00, H473). The circuit should be driven differentially. Input T-coiling is accomplished by a combination of the M122 integrated circuit and the hybrid (the 50 Ω load resistors are not included in the hybrid). Hysteresis at low frequencies is 25 mV and increases by a factor of 2 - 3 at 1.2 GHz. DC trigger input levels are at \emptyset volts. A slope select control is included. Input signal conditioning (ie, preamplification and source selection) is done external to the integrated circuit. Another integrated circuit, the M136 (155-0160-00), has been designed to accomplish this. Other inputs are hold-off and free-run (if that mode is to be used). Gate out is at an ECL level.

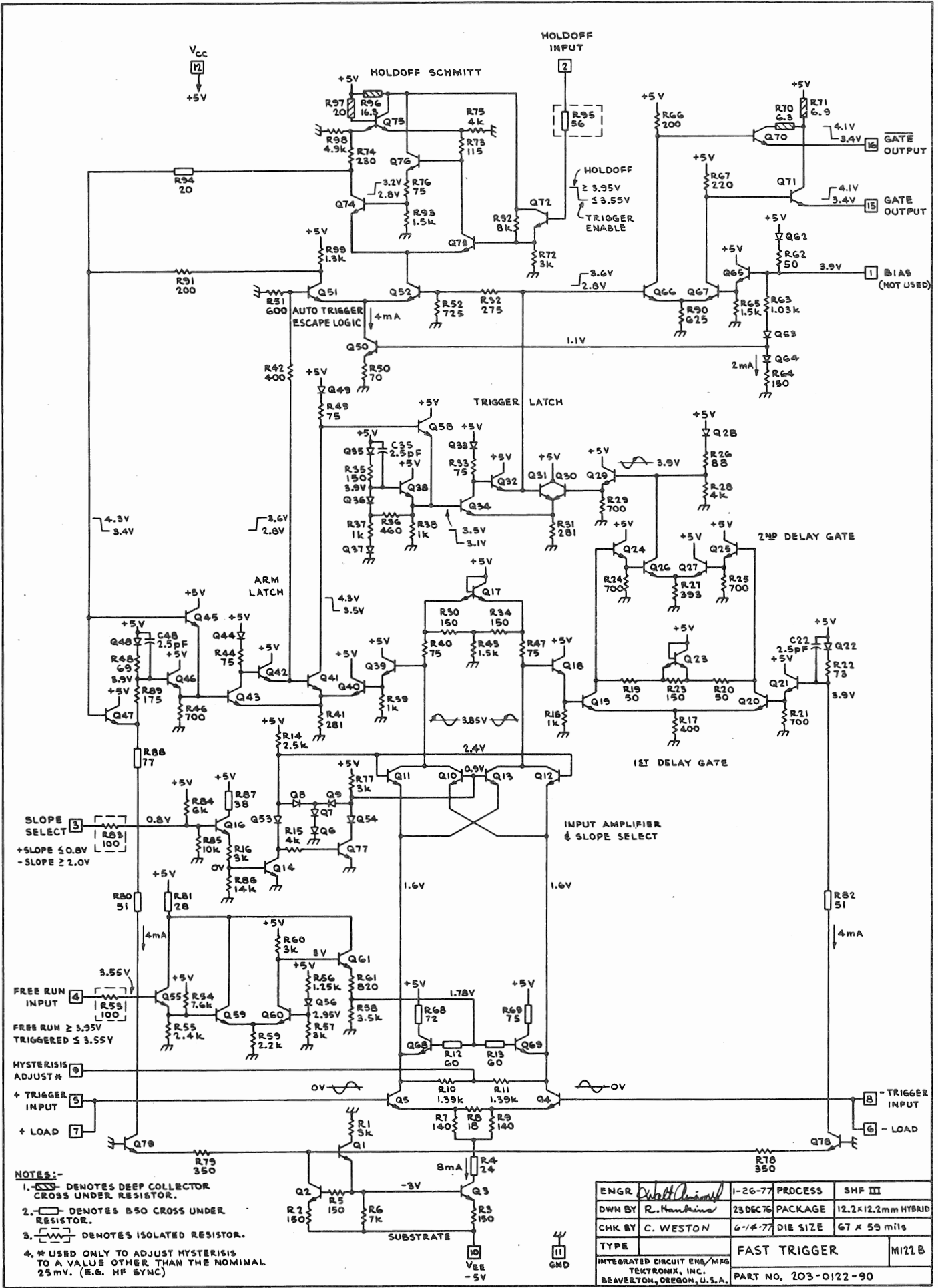
PROCESS SHF III

POWER SUPPLY. V_{CC} +5V, V_{EE} -5V

PACKAGE 1.25 cm x 1.25 cm Hybrid

DESIGNERS. Art Metz/Walt Ainsworth

INSTRUMENT USAGE 7B10
7B15
7B92A
7904
7104



NOTES:-

1. DENOTES DEEP COLLECTOR CROSS UNDER RESISTOR.
2. DENOTES B50 CROSS UNDER RESISTOR.
3. DENOTES ISOLATED RESISTOR.
4. * USED ONLY TO ADJUST HYSTERISIS TO A VALUE OTHER THAN THE NOMINAL 25mV. (E.G. HF SYNC)

ENGR	<i>Subh...</i>	1-26-77	PROCESS	SMF III
DWN BY	<i>R. H...</i>	23 DEC 76	PACKAGE	12.2x12.2mm HYBRID
CHK BY	C. WESTON	6-1-77	DIE SIZE	67 x 59 mils
TYPE			FAST TRIGGER	
INTEGRATED CIRCUIT ENG/MFG			PART NO. 203-0122-90	
TELEPHONIX, INC.			M122 B	
BEAVERTON, OREGON, U.S.A.				