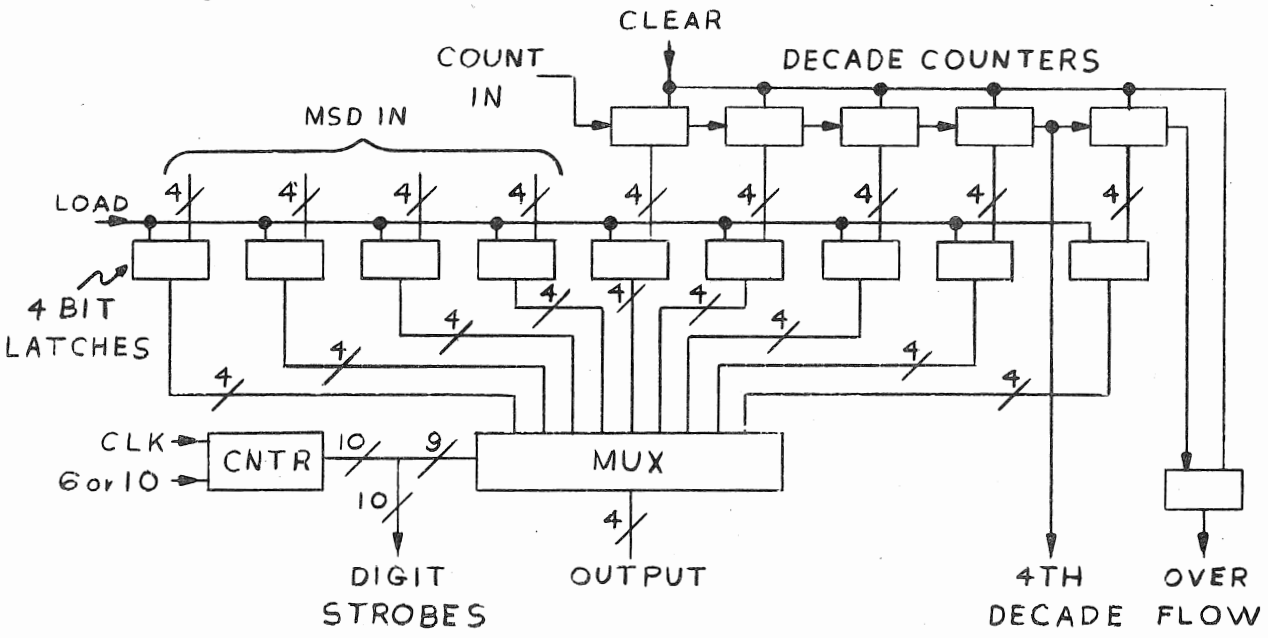
5 DIGIT BCD COUNTER 155-0119-00 M123

## DESCRIPTION

The M123 is a five digit BCD counter with the outputs buffered and multiplexed. The maximum input count frequency is 5 MHz and an overflow latch is provided to detect over range counts. The chip also contains latches for four higher order digits which can be loaded from external counter stages. All nine digits are multiplex out serially. Digit strobes are provided in order to easily interface to LED readouts. The output multiplexer can be set to strobe out only the five least significant digits. An extra strobe is provided as a time slot for points or units. The output of the fourth decade is also brought out.

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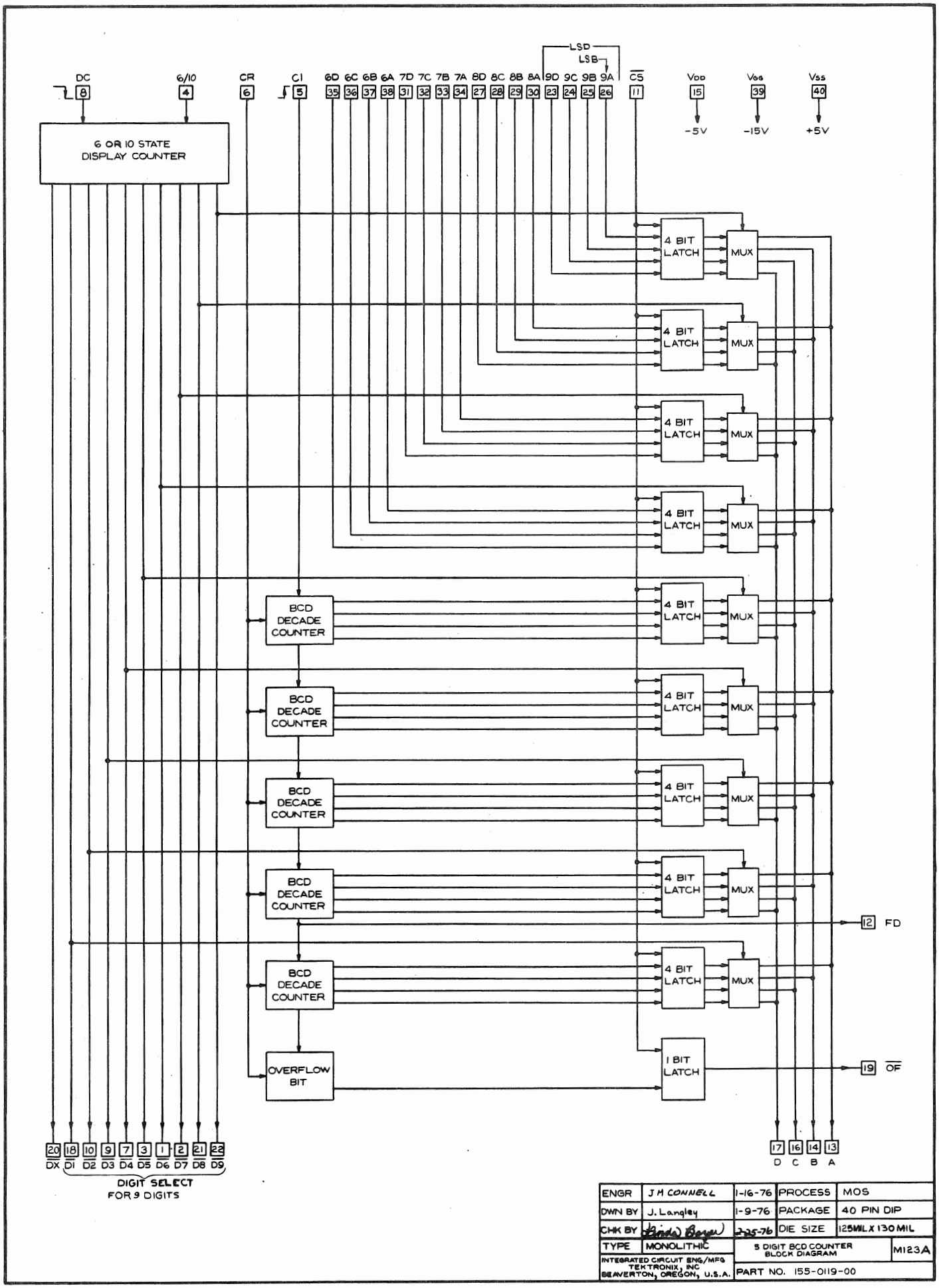
|---|---|
| PROCESS | MOS |
| POWER SUPPLY | +5V, -5V, -15V |
| PACKAGE | 40 DIP |
| DESIGNER | Dave Allstot |
| INSTRUMENT USAGE | DC 508 |

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| ENGR | J H CONNELL | 1-16-76 | PROCESS | MOS |
| DWN BY | J. Langley | 1-9-76 | PACKAGE | 40 PIN DIP |
| CHK BY | Linda Berger | 2-25-76 | DIE SIZE | 125MIL X 130 MIL |
| TYPE | MONOLITHIC | 5 DIGIT BCD COUNTER BLOCK DIAGRAM | | M123A |
| INTEGRATED CIRCUIT ENG/MFG. TEKTRONIX, INC BEAVERTON, OREGON, U.S.A. | | PART NO. 155-0119-00 | | |