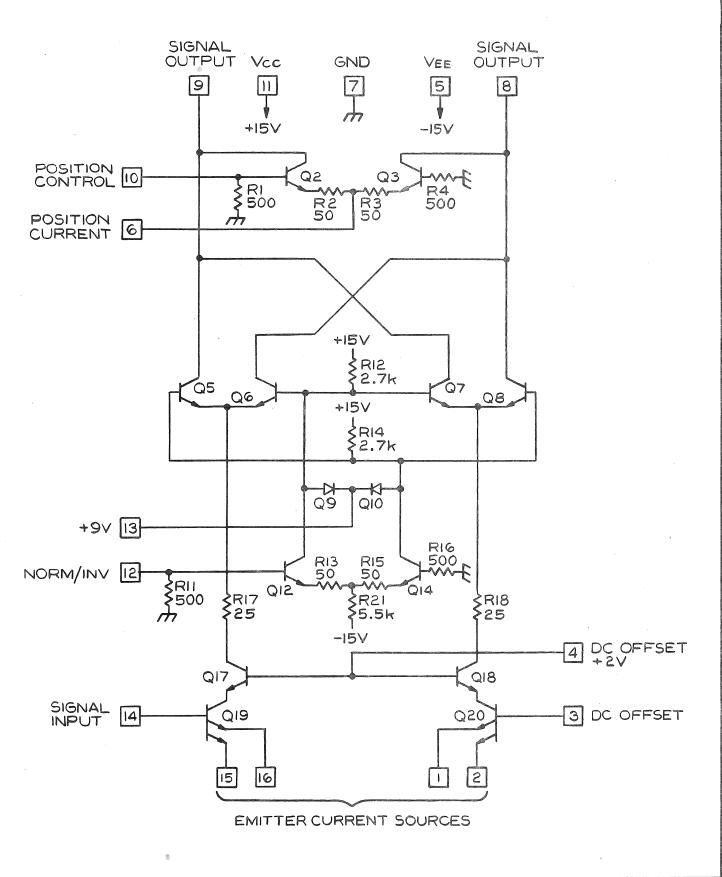
INPUT AMPLIFIER

DESCRIPTION

This circuit is a single channel amplifier designed for the dual channel 7Al2. Pins #1, #2, #15, and #16 are connected to external current sources to provide gain changes. The differential input is connected to Pins #3 and #14. Current at Pin #10 controls the ratio of the current from Pin #6 to Pins #8 and #9 for output position. Current at Pin #12 controls Pin #5 currents to provide current gain continously variable between +1 and -1.

PROCESS		•	•	•				•		•	50/450
POWER SUPPLY	•	•	•	9						•	+15V, +9V, GND -15V
PACKAGE			•	•	6	ø	•	•		٠	16 Pin DIP
DESIGNER	• •	ø	•	9	•	Ð	•	g	•	•	Roy Hayes
INSTRUMENT USAGE		•	٠	e	•	٠	•	o o	s	•	1478 671 465 1460 475 670 464 466 1440 355 Some 475 Mods



	ENGR	Emen	Time	7-1-76	PROCESS	50-45	50				
	DWN BY	J. Lan	gley	6-29-76	PACKAGE	DIP					
	CHK BY	Linda	Boyer	7-13-76	DIE SIZE GOMII X 40 mi						
1	TYPE MONOLITHIC			INPUT AMPLIFIER M055							
- 84	INTEGRATED CIRCUIT ENG/MFG										
	TEKTRONIX, INC. BEAVERTON, OREGON, U.S.A.			PART NO. 155-0032-00							