## INSTRUCTION MANUAL

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## TYPE



DUAL-TRACE AMPLIFIER

Tekłronix, Inc.

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All Tektronix instruments are warranted against defective materials and workmanship for one year. Tektronix transformers, manufactured in our own plant, are warranted for the life of the instrument.

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## SECTION 1

## CHARACTERISTICS

## Infroduction

The Type 9A2 Dual-Trace Amplifier is designed for use with Tektronix indicator units such as the Type 506 Oscilloscope. It contains two identical amplifier channels which can be used independently for a single-trace display, or the two channels can be electronically switched for a dual-trace display. Also, the two channels can be added algebraically and the resultant waveform displayed on the indicator-unit crt.

A signal delay line in the plug-in unit delays the signal to be displayed while the triggering signal passes to the time-base unit undelayed. This allows the leading edge of the triggering signal to be viewed on the crt.

## Frequency Response

Dc to 20 Mc (not more than 3 db down at 20 Mc ) with a corresponding risetime of typically 17.5 nanoseconds.

## Deflection Factors

Ten calibrated steps for each channel; .01, .02, .05, .1, .2, $5,1,2,5$ and 10 volts/division. Variable control in each channel provides uncalibrated deflection factor to at least 2.5 times the VOLTS/DIV setting or a maximum deflection factor of about 25 volts/division in the 10 position.

## Accuracy

Within $\pm 3 \%$ of indicated deflection with VARIABLE in the CALIB position.

## Input Characteristics

Impedance-1 megohm $\pm 1 \%$, paralleled by about 47 pf .
Coupling-Ac or dc coupled, selected by front-panel switch. In the AC position low-frequency response is limited to about $2 \mathrm{cps}(0.2 \mathrm{cps}$ with $10 \times$ passive probe).

Voltage Rating- 600 volts combined dc and peak ac

## Signal Delay

Approximately 260 nanoseconds.

## Operating Modes

Channel 1 only.
Channel 2 only.
Alternate between channels.
Chopped between channels.
Added algebraically.

## Polarity Inversion

Polarity of Channel 1 can be changed to provide $180^{\circ}$ inversion. No polarity inversion of Channel 2.

## Channel Isolation

$5,000: 1$ or greater at 20 Mc .

## Triggering

Internal triggering signals for time-base unit are provided from both channels or from Channel 1 only, determined by front-panel switch.

## Mechanical Features

Input connectors: BNC jacks.
Construction: Aluminum alloy chassis and panel.
Finish: Photo-etched, anodized front panel.
Net weight: $5 \mathrm{lbs}, 11 \mathrm{oz}$.

## OPERATING INSTRUCTIONS

## FUNCTION OF FRONT-PANEL CONTROLS AND CONNECTORS

The functions of all controls and connectors except the CALIB, MODE, INV (CH 1) NORM and TRIGGER CH 1 ONLY are identical for both channels.

| Input | Signal input connector. |
| :--- | :--- |
| POSITION | Controls vertical position of the display. <br> VOLTS/DIVSelects vertical deflection factor. (VARI- <br> ABLE control must be in CALIB position for <br> indicated deflection factor.) |
| VARIABLE | Provides continuously variable attenuation <br> to about 2.5 times setting of VOLTS/DIV <br> switch. |
| UNCAL | Light indicates that VARIABLE control is <br> not set to CALIB. |
| DC BAL | Screwdriver adjustment to set dc balance <br> of the amplifier. |
| AC DC GND | Selects method of coupling signal to grid <br> of input amplifier. |

AC: Blocks de component of applied signal.
DC: Directly couples the applied signal to grid of input stage.
GND: Grounds grid of input stage (does not ground applied signal).

CALIB Screwdriver adjustment to set the basic gain of the plug-in unit.

MODE Selects mode of operation.
CH 1: Displays Channel 1 signal only.
CH 2: Displays Channel 2 signal only.
ALTER: Alternate-trace display of both channels switched at end of each sweep.
CHOP: Dual-trace display of both Channel
1 and Channel 2 signals. Each channel
is displayed for about 4 microseconds at about 150 -kc frequency.
ADDED: Displays algebraic sum of Channel 1 and Channel 2 signals. NORM

TRIGGER
CH 1 ONLY PULL

INV $(\mathrm{CH}$ 1) Inverts the Channel 1 display when in the INV position.

Selects triggering from Channel 1 signal when pulled out. When pushed in, triggering signal is obtained from composite Output Amplifier signal.

## FIRST-TIME OPERATION

The following steps are intended to acquaint the operator with the basic overall operation and function of the Type 9A2. More specific operation and basic application information is given later in this section.

1. Insert the Type 9A2 into the left (Y-axis) plug-in compartment of the indicator unit. Insert a time-base plug-in unit such as a Tektronix Type 3B3 into the right (X-axis) plug-in compartment.
2. Set the front-panel controls as follows:

| AC DC GND | DC (both channels) |
| :--- | :--- |
| VOLTS/DIV | .5 (both channels) |
| VARIABLE | CALIB (both channels) |
| POSITION | Midrange (both channels) |
| MODE | CH 1 |
| INV (CH 1) NORM | NORM |
| TRIGGER | Pushed in |

3. Turn on the indicator-unit power. If there is any doubt as to the operation of the indicator unit or time-base unit, see the instruction manuals for these units.
4. Set the indicator-unit Calibrator for 1 Volt output and the time base for a sweep rate of 5 milliseconds/division.
5. Connect the calibrator output to both the Channel 1 and Channel 2 input connectors. Adjust the time-base triggering controls for a stable display.
6. Turn the Channel 1 POSITION control to move the display above the graticule centerline.
7. Set the MODE switch to CH 2. A two-division display similar to the previous display should be seen. Turn the Channel 2 POSITION control to move the display below the graticule centerline.
8. Set the MODE switch to ALTER; the Channel 1 and Channel 2 displays as set up in steps 6 and 7 should be seen. Turn the time-base time/division switch to a faster sweep rate. Notice that the flicker between traces decreases as the sweep rate is increased. Return the time/division switch to 5 milliseconds/division.
9. Set the $A C D C$ GND switch to $A C$. Notice the slope of the horizontal portion of the 60 -cycle calibrator waveform which indicates loss of low frequencies due to the coupling capacitor. Return the switch to the DC position.
10. Turn one of the VARIABLE controls throughout its range. The deflection of that channel should decrease by a factor of at least 2.5 to 1 . Return the VARIABLE to CALIB.
11. Set the MODE switch to CHOP; the display should be identical to ALTER but with less flicker between traces. Turn the time-base time/division switch to 10 microseconds. Notice the switching between channels as shown by the segmented trace. Reduce the sweep rate slowly and notice that the trace appears solid at about 0.1 milliseconds/
division. This is the fastest rate at which CHOP mode can be used for transient signals and still obtain a satisfactory display.
12. Set the time/division switch to 2 microseconds and readjust the triggering controls for a stable display. With the CRT Cathode Selector switch in the up position, turn the intensity up slightly and notice the overshoot on the display. This is the between-channel switching transient. Set the CRT Cathode Selector switch to Chopped Blanking. The switching transient should be removed from the display. Return the time/division switch to 5 milliseconds/division.
13. Set the MODE switch to ADDED. The display should be 4 divisions in amplitude. The Channel 1 and Channel 2 signals (2 divisions each) are added for this display. Note that either POSITION control will move the trace.
14. Set the INV (CH 1) NORM switch to INV. The display should be a straight line. Since both displays are two divisions in amplitude but of opposite polarity, the algebraic sum is zero.
15. Set either VOLTS/DIV switch to 1. A square wave is again displayed indicating that the algebraic sum of Channel 1 and Channel 2 is no longer zero.

## CALIB Adjustment

Whenever the Type 9A2 is inserted into a plug-in compartment, check and adjust the gain to compensate for the differences between indicator units. Allow about 10 minutes warm up before performing this adjustment.

1. Set both AC DC GND switches to DC.
2. Set the MODE switch to CH 1.
3. Set both VOLTS/DIV switches to .02 .
4. Turn the VARIABLE controls fully clockwise so the UNCAL lights are off.
5. Set the time-base controls for automatic triggering.
6. Connect the Calibrator output to Channel 1 input.
7. Set the indicator-unit Calibrator to 100 millivolts.
8. Set the CALIB control for exactly 5 major divisions of deflection.
9. Set the MODE switch to CH 2.
10. Connect the Calibrator output to Channel 2 input.
11. The deflection should be exactly 5 divisions. If incorrect, refer to the Channel 2 gain adjust procedure in Section 5.

## DC BAL Adjustment

Check. To check the dc balance of either channel, set the AC DC GND switch to GND and the VOLTS/DIV switch to
.01. Set the time-base controls for automatic triggering. Rotate the VARIABLE control throughout its range. If the trace moves vertically, adjust the DC BAL control according to the procedure which follows.

Adjustment. The following procedure can be used to adjust the dc balance of either channel. Set the MODE switch to display the desired channel. Allow about 10 minutes warm up before performing this adjustment.

1. Set the AC DC GND switch to GND.
2. Set the VOLTS/DIV switch to .01 .
3. Set the time-base controls for automatic triggering.
4. Turn the POSITION control to midrange.
5. With the VARIABLE control in the CALIB position adjust the DC BAL control to bring the trace near graticule center.

## NOTE

Both DC BAL controls are the dual-range or coarse-fine type. To use this type of control, turn the control slightly past the desired point of adjustment (coarse adjust). Then, reverse the direction of rotation and use the fine adjustment (about $30^{\circ}$ range) to establish balance.
6. Adjust the DC BAL control so there is no trace shift as the VARIABLE control is rotated throughout its range. This may take several readjustments of the DC BAL control.

## Signal Connections

In general, $10 \times$ attenuator probes offer the best means of coupling a signal to the input of the Type 9A2. The 10X attenuator probe offers a higher input impedance and allows the circuit under test to operate more closely to actual operating conditions. The signal probes are shielded to prevent pickup of any electrostatic interference.

In some cases, the signal can be connected to the Type 9A2 inputs with short unshielded leads. This is particularly true with high-level, low-frequency signals. When such leads are used, be sure to establish a common ground between the Type 9A2 and the equipment under test. Attempt to position the leads away from the source of interference to avoid errors in the display. If interference is excessive with unshielded leads, use a coaxial cable or a probe.

In high-frequency applications requiring maximum overall bandwidth, use coaxial cables terminated in their characteristic impedance at the input of the Type 9A2.

## Loading Effect of the Type 9A2

As nearly as possible, simulate actual operating conditions in the equipment under test. Otherwise, the equipment under test may not produce a normal signal. The $10 \times$ attenuator probes mentioned previously offer the least circuit loading. Tektronix $10 \times$ attenuator probes have an input resistance of 10 megohms with very low shunt capacitance.

When the signal is coupled directly to the input of the Type 9A2, the input impedance is 1 megohm ( $\pm 1 \%$ ) paralleled by about 47 pf . When the signal is coupled to the input of the Type 9A2 through a coaxial cable, the input capacitance is greatly increased. Just a few feet of coaxial cable can increase the input capacitance to well over 100 pf .

## Input Coupling

The Channel 1 and 2 AC DC GND switches allow a choice of input coupling. To display both the ac and dc components of an applied signal, set the switch to DC. In the AC position, only the ac component of the input signal is allowed to pass. The GND position connects the input grid to ground but does not ground the input signal.

The DC position can be used for most applications. However, if the dc component of the signal is large enough to drive the display off the crt viewing area or if the dc component is much larger than the ac component, use the $A C$ position.
In the AC position of the switch, the dc component of the signal is blocked by a capacitor in the input circuit. The low-frequency response in the $A C$ position is about 2 cps , 3 db down. Therefore, some low-frequency distortion can be expected near this frequency limit. Distortion will also appear in square waves which have low-frequency components.
The GND position provides a ground reference at the input of the Type 9A2. The signal applied to the input connector is internally disconnected but not grounded. The grid of the input tube is at ground potential, eliminating the need to externally ground the input to establish a zero dc reference.

## Internal Triggering

Single-Trace Displays. In the CH 2 and ADDED positions of the MODE switch, push the TRIGGER switch in. When the time-base trigger coupling switch is set to dc, the setting of the POSITION controls will affect the setting of the timebase trigger level control. Proper triggering can be obtained switch position. However, in the CH 1 ONLY position (TRIGin the CH 1 position of the MODE switch in either TRIGGER GER switch pulled out) the POSITION control does not affect de triggering.

Dual-Trace Displays. To obtain proper triggering in the CHOP mode, pull out the TRIGGER switch. Then, the internal trigger signal coupled to the time base does not contain the dual-trace chopping signal. It is difficult to get a stable display if the TRIGGER switch is pushed in because of the switching transient.
In the ALTER mode, the TRIGGER switch can be in either position, depending upon the applications. When the TRIGGER switch is pushed in, the time base will be triggered from the display of each channel. This provides stable display of two unrelated signals, but does not indicate the time relationship between the signals. To display signals showing true time relationship, pull the TRIGGER switch out to trigger from Channel 1 only. If the signals on the two channels are not time related, the Channel 2 display will not be stable.

In the ALTER mode, dc trigger coupling cannot be used if the TRIGGER switch is pushed in. Proper triggering can be obtained with the TRIGGER switch pulled out to the CHANNEL 1 ONLY position.

## Dual-Trace Operation

Chopped Mode. The chopped display is produced by electronic switching between channels. To blank out the switching transients, set the CRT Cathode Selector switch on the rear of the indicator unit to the chopped blanking position.

In general, use the CHOP position (chopped-mode operation) with sweep rates slower than 0.1 millisecond/division. At higher sweep rates the chopped switching becomes apparent.

Two signals which are time related can be displayed in the chopped mode showing true time relationship. However, if the signals are not time related, the Channel 2 display will appear unstable.

Single-shot, transient or random signals which occur within the time interval determined by the time-base time/division switch (10 times sweep rate) can be compared using the chopped mode. To trigger the sweep, the Channel 1 signal must precede the Channel 2 signal. Since the signals show true time relationship, time difference measurements can be made.

Alternate Mode. The ALTER position (alternate-mode operation) can be used at any sweep rate. However, for time comparison measurements requiring sweep rates below about 20 milliseconds/division, use the chopped mode for best results.

## Algebraic Addition

To display signals using the ADDED mode, observe the following precautions:

Do not exceed the input voltage rating of the amplifier.
Do not apply signals that will exceed an equivalent of 20 major divisions of deflection. As an example, with a VOLTS/ DIV switch setting of 1, the voltage applied to that channel should not exceed 20 volts. If this limit is exceeded, signal distortion will result.

Before displaying signals in the ADDED mode, set the MODE switch to ALTER or CHOP and position both channel signals to the center of the display area. This is also a precaution against display distortion.

## BASIC APPLICATIONS

The following information describes the procedure and techniques for making basic measurements with a Type 9A2 and an associated Tektronix indicator and time-base unit. No attempt has been made to describe these applications in detail, as each application must be adapted to the individual requirements. Familiarity with the Type 9A2 will enable these basic techniques to be applied to a wide variety of uses.

## Voltage Measurements-AC

To measure the voltage level of an ac signal, set the AC DC GND switch to AC. In this position the de component of the signal will be blocked. However, if the ac signal is very low frequency, the DC position should be used to avoid distortion of the signal.

To make a peak-to-peak measurement, use the following procedure:

1. Set the VOLTS/DIV switch to a setting at least $1 / 8$ of the expected voltage. For example, if the voltage will be about 10 volts in amplitude, set the VOLTS/DIV switch to 2 (. 2 if $10 \times$ probe is used).
2. Apply the signal to either input connector.
3. Set the MODE switch to the channel used.
4. Set the AC DC GND switch to AC.
5. Set the time-base triggering controls to obtain a stable display. Set the time/division switch to a position that will display several cycles of the waveform.
6. Turn the POSITION control so the lower portion of the waveform coincides with one of the graticule lines below the centerline. With the time-base Position control, move the display so one of the upper peaks lies near the vertical centerline (see Fig. 2-1).


Fig. 2-1. Measuring the peak-to-peak voltage of a waveform.
7. Measure the divisions of vertical peak-to-peak deflection. Make sure the VARIABLE control is in the CALIB position.
8. Multiply the peak-to-peak distance measured in step 7 by the VOLTS/DIV setting. Also include the attenuation factor of the probe, if any.

As an example of this method of measurement, assume that the peak-to-peak vertical deflection is 4.6 divisions (see Fig. 2-1) using a $10 \times$ attenuator probe and a VOLTS/DIV setting of .5 .

Using the formula:

Substituting the values given above:
Volts Peak-to-Peak $=4.6 \times .5 \times 10$
The peak-to-peak voltage would be 23 volts.

## Voltage Measurements-Instantaneous DC

To measure the dc level at a given point on a waveform, use the following procedure:

1. Set the VOLTS/DIV switch to a setting at least $1 / 8$ of the expected voltage. For example, if the voltage will be about 10 volts in amplitude, set the VOLTS/DIV switch to 2 (.2 if $10 \times$ probe is used).
2. Connect the signal to either input connector.
3. Set the MODE switch to the channel used.
4. Set the AC DC GND switch to GND.
5. Adjust the triggering controls for automatic triggering.
6. Align the trace with the bottom line of the graticule or other reference line. If the voltage is negative with respect to ground, set the INV (CH 1) NORM switch to INV and then align the trace with the reference line. Do not move the POSITION control after this reference line has been established.

## NOTE

To measure a voltage level with respect to another voltage rather than ground, make the following changes in the above procedure. Set the AC DC GND switch to DC. Apply the reference voltage to the input and position the trace to the reference line.
7. Set the AC DC GND switch to DC. The ground reference can be checked at any time by switching to the GND position.
8. Set the time-base triggering controls to obtain a stable display. Set the time/division switch to a setting that will display several cycles of the waveform.
9. Measure the distance in divisions between the reference line established in step 6 and the point on the waveform to be measured. For example, in Fig. 2-2 the measurement is made between points (a) and (b).
10. Establish the polarity of the signal. If the waveform is above the reference line with the INV (CH 1) NORM switch set to NORM, the voltage is positive. If the INV (CH 1) NORM switch is set to INV, the voltage is negative.
11. Multiply the distance measured in step 9 (include polarity) by the VOLTS/DIV setting. Include the attenuation factor of the probe, if any.

As an example of this method of measurement, assume that the vertical distance is 4.6 divisions (see Fig. 2-2), INV (CH 1) NORM switch set to INV, using a $10 \times$ attenuator probe and a VOLTS/DIV setting of 2.


Fig. 2-2. Measuring instantaneous dc voltage with respect to a reference.

Using the formula:

$$
\begin{gathered}
\text { Instantaneous } \\
\text { Voltage }
\end{gathered}=
$$

$$
\begin{gathered}
\text { vertical } \\
\text { distance } \\
(\text { divisions })
\end{gathered} \times \text { polarity } \times \quad \begin{gathered}
\text { VOLTS/DIV } \\
\text { setting }
\end{gathered} \times \begin{gathered}
\text { attebe } \\
\text { factor }
\end{gathered}
$$

Substituting the values given above:
Instantaneous Voltage $=4.6 \times-1 \times 2 \times 10$
The instantaneous voltage would be -92 volts.

## Voltage Comparison Measurements

In some applications it may be necessary to establish a set of deflection factors other than those indicated by the VOLTS/DIV switch. This is useful for comparing signals which are exact multiples of a reference voltage amplitude. To establish a set of deflection factors based upon some specific reference amplitude, proceed as follows:

1. Apply a reference signal of known amplitude to the Channel 1 input connector and adjust the size of the display for an exact number of graticule divisions using the Channel 1 VOLTS/DIV switch and VARIABLE control. Do not move the VARIABLE control after obtaining the desired deflection.
2. Divide the amplitude of the reference signal (volts) by the product of the deflection in divisions (established in step 1) and the VOLTS/DIV switch setting. This is the Deflection Conversion Factor.
$\underset{\substack{\text { Conversion } \\ \text { Factor }}}{\substack{\text { Deflection }}}=\frac{\text { reference signal amplitude (volts) }}{\text { deflection (divisions) } \times \text { VOLTS/DIV setting }}$
3. To calculate the Adjusted Deflection Factor at any setting of the Channel 1 VOLTS/DIV switch, multiply the VOLTS/DIV switch setting by the Deflection Conversion Factor obtained in step 2.
[^0]This Adjusted Deflection Factor applies only to Channel 1 and is correct only if the VARIABLE control is not moved from the position set in step 1 .
4. To determine the peak-to-peak amplitude of a signal compared to a reference, disconnect the reference and apply the signal to Channel 1.
5. Set the Channel 1 VOLTS/DIV switch to a setting that will provide sufficient deflection to make the measurement. Do not readjust the VARIABLE control.
6. Measure the vertical deflection in divisions and determine the amplitude by the following formula:

$$
\underset{\text { Amplitude }}{\text { Signal }}=\underset{\text { Deflection }}{\text { Factor }} \times \underset{\text { Adjusted }}{\text { Defletion }} \times \underset{\text { divisions) }}{\text { deflect }}
$$

As an example of this method of measurement, assume a reference signal amplitude of 30 volts, a VOLTS/DIV setting of 5 and a deflection of 4 divisions. Substituting these values in the Delection Conversion Factor formula (step 2):

$$
\underset{\substack{\text { Deflection } \\ \text { Factor }}}{\text { Conversion }}=\frac{30}{4 \times 5}=1.5
$$

Then, with a VOLTS/DIV switch setting of 10 the Adjusted Deflection Factor (step 3) would be:

$$
\underset{\text { Adjusted }}{\text { Adection }}=10 \times 1.5=15 \text { volts/division }
$$

Factor

To determine the peak-to-peak amplitude of an applied signal which produces a vertical deflection of 4.5 divisions use the Signal Amplitude formula (step 6):

$$
\underset{\text { Amplitude }}{\text { Signal }}=15 \times 4.5=67.5 \text { volts }
$$

## Time-Difference Measurements

The calibrated sweep rate of the time base and the dualtrace feature of the Type 9A2 allow measurement of time difference between two events. To measure time difference, use the following procedure:

1. Set the $A C D C$ GND switches to the same position, depending on the type of coupling desired.
2. Set the INV (CH 1) NORM switch to NORM.
3. Set the MODE switch to either CHOP or ALTER. In general, the CHOP position is more suitable for low-frequency signals and the ALTER position is more suitable for high-frequency signals. For more information on determining the mode, see "Dual-Trace Operation" and "Internal Triggering" in this section.
4. Pull the TRIGGER switch out to provide triggering from the Channel 1 signal only.
5. Set the VOLTS/DIV switches so the voltages applied to the input connectors will provide suitable deflection on the crt.
6. Apply the reference signal to Channel 1 and the comparison signal to Channel 2. Use coaxial cables or probes which have equal time delay.

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7. Set the triggering controls for a stable display.
8. Set the time/division switch for a sweep rate which shows a two to three division spacing between the two signals.
9. Adjust the POSITION control to center each display (or the points on the display between which the measurement is being made) in relation to the horizontal graticule centerline.
10. Measure the exact horizontal distance between the reference waveform and the Channel 2 waveform (see Fig. 2-3).


Fig. 2-3. Measuring time delay between pulses.
11. Multiply the measured distance by the setting of the time/division switch to obtain the apparent time interval. The actual time interval is determined by dividing this value by the sweep magnification, if sweep magnification is used.

For example, assume that the time/division switch is set to 5 microseconds, the $5 \times$ magnifier is on, and the horizontal distance between waveforms is 2.7 divisions (see Fig. 2-3). Using the formula:
Time Delay $=$

$$
\frac{\text { TIME/DIV setting } X \text { horizontal distance (divisions) }}{\text { sweep magnification }}
$$

Substituting the values given above:

$$
\text { Time Delay }=\frac{5 \times 2.7}{5}
$$

The Time Delay would be 2.7 microseconds.

## Phase Measurements

Phase comparison between two signals of the same frequency can be made using the dual-trace feature of the Type 9A2. To make the comparison, proceed as follows:


Fig. 2-4. Measuring phase shift.

1. Follow the procedure given in the first seven steps under "Time-Difference Measurements".
2. Set the time/division control for a sweep rate which displays less than 1 cycle of the waveform.
3. Adjust the VARIABLE control so the displays are equal and about six divisions in height. Reset the VOLTS/DIV switches, if necessary, to obtain equal amplitude displays.
4. Move the waveforms to the center of the graticule with the POSITION controls.
5. Turn the Variable time/division control counterclockwise until 1 cycle of the reference signal (Channel 1 signal) occupies exactly 9 divisions horizontally. Each division of the graticule represents $40^{\circ}$ of the cycle $\left(360^{\circ} \div 9\right.$ divisions $=40^{\circ} /$ divison. See Fig. 2-4).
6. Measure the horizontal distance between corresponding points on the waveforms. Note whether the Channel 2 waveform is leading or lagging the reference waveform on Channel 1.


Fig. 2-5. Accurate phase measurement with sweep rate increased 5 times.
7. Multiply the measured distance (in divisions) by $40^{\circ}$ to obtain the exact amount of phase difference.

For example, with a horizontal distance of 1.1 divisions as shown in Fig. 2-4, the phase difference would be:

Phase difference $=1.1 \times 40^{\circ}=44^{\circ}$
8. Phase difference can be measured more accurately by
increasing the sweep rate of the time base. Do not change the setting of the Variable time/division control. One of the easiest ways to increase the sweep rate is with the $5 \times$ magnifier. The adjusted phase factor is determined by dividing $40^{\circ}$ by the increase in sweep rate.

For example, if the sweep rate were increased 5 times with the $5 \times$ magnifier, the new phase factor would be $40^{\circ} \div 5=8^{\circ} /$ division (see Fig. 2-5).

# CIRCUIT DESCRIPTION 

The Type 9A2 Dual-Trace Amplifier contains two identical input amplifiers, a common output amplifier, a switching circuit and a trigger amplifier. The output of either or both input-channel amplifiers may be fed to the Output Amplifier, depending on the condition of the switching circuit. Thus, the switching circuit makes it possible to display one signal as a single trace on the crt, two signals alternately in a dual-trace display, or the algebraic sum or difference of two signals as a single trace. Fig. 3-1 shows a block diagram of the Type 9A2.

## Inpuł Amplifier

Each input amplifier consists of an attenuation network, an input cathode follower and a two-stage differential amplifier with switched outputs. Input cathode followers V113 and V123 (Channel 1) and V213 and V223 (Channel 2) isolate the emitter followers Q133 and Q143 (Channel 1) and Q233 and Q243 (Channel 2) from the input attenuators. Q134 and Q144 (Channel 1), and Q234 and Q244 (Channel 2) drive the Output Amplifier.

The input attenuators are resistive dividers with capacitive compensation. An input impedance of 1 megohm ( $\pm 1 \%$ ) paralleled by about 47 pf is maintained in each position of the attenuator switch. When the VOLTS/DIV switch is in the .01 and .02 positions, input signals are coupled directly through the attenuator switch to cathode-follower stages V 113 and V213. In the remaining attenuator positions resistive dividers are used to increase the basic deflection factor. In the .05 position, the $2.5 \times$ attenuator increases the basic $20-\mathrm{mv} /$ div. deflection factor to $50 \mathrm{mv} /$ div. The $5 \times$ attenuator is used to increase the deflection factor to 100 $\mathrm{mv} /$ div. in the .1 position. The $2.5 \times$ and $5 \times$ attenuators are used in conjunction with the $10 \times$ or $100 \times$ attenuators to provide the higher deflection factors in the remaining positions of the VOLTS/DIV switch.

The DC BAL controls in the grid circuit of V123 and V223 adjust the dc voltage level of each input amplifier so current does not flow through R139 (Channel 1) and R239 (Channel 2) under no-signal conditions. Otherwise, the position of a no-signal trace would shift on the crt as the VARIABLE control was turned or gain adjustments were made.
The gain of the last stage in each input amplifier is set by two internal calibration adjustments: R149 (Channel 1) and R249 (Channel 2) set the gain of the stage in the . 01 position of the VOLTS/DIV switches; R147 (Channel 1) and R247 (Channel 2) set the gain of the stage in the remaining positions of the VOLTS/DIV switches. The VARIABLE controls R139 (Channel 1) and R239 (Channel 2) increase emitter degeneration when moved from the CALIB position.
In the INV position, the INV (CH 1) NORM switch (SW155) inverts the Channel 1 output to the diode gates.

## Switching Circuit

The switching circuit consists of two diode gates, a switching multivibrator, a switching blocking oscillator and a blanking emitter follower.

In the $\mathrm{CH} 1, \mathrm{CH} 2$ and ADDED positions of the MODE switch, the switching multivibrator is inoperative (i.e., neither transistor is conducting). In the ALTER and CHOP positions of the MODE switch, the switching multivibrator operates as a bistable multivibrator. In this case, the multivibrator is switched by the blocking oscillator. The blocking oscillator operates as either a "triggered" oscillator in the ALTER mode or an "astable" (free running) oscillator in the CHOP mode.

Each diode gate consists of two series diodes and two shunt diodes. For a signal to pass through the diode gates to the bases of Q304 and Q314 in the Output Amplifier, the series diodes must be forward biased, and the shunt diodes must be back biased.

When the MODE switch is in the CH 1 position, collector current of Q134 and Q144 flows through the series diodes. The MODE switch SW290 reverse biases Q285 and holds it in the "off" state. Q275 is also "off", and holds the junction of D156 and D157 at -1.85 volts. Since the dc level in the signal output from Channel 1 Input Amplifier is -2.6 volts, the shunt diodes are back biased. The cathodes of the Channel 2 shunt diodes, D256 and D257, are held at about -4 volts by Q285. D256 and D257 are conducting and hold the Channel 2 output at about -3.8 volts. Since the output of the diode gates is at about -3 volts, D255 and D258 are back biased. With D255 and D258 cut off, the Channel 2 Input Amplifier passes through D255 and fier. In addition, since D256 and D257 are conducting, they form a low-impedance path for any signals coming to the diode gate from the Channel 2 Input Amplifier.

When the MODE switch is in the CH 2 position, the conditions of the diode gates are reversed. The Channel 2 shunt diodes D256 and D257 and the Channel 1 series diodes D155 and D158 are back biased. Therefore, the signal from the Channel 2 Input Amplifier passes through D255 and D258 to the Output Amplifier and the Channel 1 signal is blocked by D155 and D158.

When the MODE switch is in either the ALTER or CHOP positions, Q275 and Q285 operate as a bistable multivibrator. Positive pulses from the switching blocking oscillator, Q260, pass through D278 or D288 to the collector of the "off" state transistor. This pulse is transmitted by either C277 or C287 to the base of the "on" transistor, turning it "off". The collector of this "off" transistor moves toward the -12.2 -volt supply, turning the other transistor "on" because of the coupling through either C277 or C287. Resistive dividers R277-R286 and R276-R287 set the levels at the transistor bases. The voltage levels allow switching action to occur when a pulse is received from the blocking oscillator.

Operation of the diode gates in ALTER and CHOP is the same as described in the CH 1 and CH 2 positions of the MODE switch. However, the multivibrator is free to switch states when it receives a trigger signal from the blocking oscillator, and thus operate the diode gates and transmit a signal to the Output Amplifier from Channel 1 and Channel 2 alternately. In the ALTER and CHOP positions of the MODE switch, R292 is bypassed and the collector loads,


Fig. 3-1. Block diagram of Type 9A2.

R278 and R288, of the multivibrator transistors are connected to the -12.2 -volt supply, allowing the multivibrator to switch.

In the CHOP and ALTER positions of the MODE switch, Q260 (switching blocking oscillator) is energized and supplies trigger pulses to the switching multivibrator. In the ALTER mode, the emitter of Q260 is connected to the -12.2volt supply through R261. The collector is connected to the - 100-volt supply through the collector winding of transformer T263. When a positive pulse is fed from the time-base plug-in unit through pin 3 of the interconnecting plug, the emitter voltage rises. The transistor conducts, drawing current through the collector winding of transformer T263, driving Q260 further into conduction. A normal blockingoscillator cycle occurs, with the backswing cutting the transistor off. The transistor is biased so it does not turn on again until another sync pulse is received.

In the CHOP position of the MODE switch, the emitter of Q260 is connected to ground through R262. The base is connected to the -12.2 -volt supply, forward biasing the transistor. The blocking oscillator is automatically triggered and free runs at about a 300 -kc rate. Fig. 3-2 shows the blocking oscillator waveforms with the MODE switch in the CHOP position.

The positive output pulses produced by Q260 in the ALTER or CHOP position of the MODE switch are coupled to the switching multivibrator through D263 and C271. These positive pulses forward bias diode D278 or D288 to switch
the multivibrator. Q260 also provides the blanking pulse to blank out the chopping transient. The emitter follower Q263 provides a fast trailing edge on the blanking pulse. Q263 is biased into operation only during the trailing edge of the blanking pulse.
When the MODE switch is in the ADDED position, current is supplied to the Output Amplifier through R293 and R294, holding the series diodes in both diode gates in conduction. Since the multivibrator transistors are both near cut off, the shunt diodes are back biased and thus inoperative. The signals from both Channel 1 and Channel 2 are allowed to pass to the Output Amplifier and algebraically add or subtract, depending on their polarity.

## Output Amplifier

The output of the diode gates passes to the bases of Q304 and Q314. These transistors are connected as a common-emitter differential amplifier. The feedback networks from collector to base of Q304 and Q314 form an amplifier with low input and output impedance. R300-C300 R303-C303, R305-C305, and R307-C307 boost the gain at high frequencies to compensate for the losses in the delay line. R308 and R318 add to the low output impedance of the amplifier and match the stage to the delay line. However, the output impedance of the stage tends to increase at high frequencies. R309 and C309 are added to the circuit to compensate for this by effectively lowering the output impedance as frequency increases.

(a) Signal at base of Q260. 5 volts/div.

(b) Signal at collector of Q260. 20 volts/div.

(c) Signal at pin 24 of interconnecting plug. 20 volts/div.

Fig. 3-2. Blocking oscillator waveforms for chopped mode operation. Test oscilloscope ac coupled at sweep rate of $2 \mu \mathrm{sec} /$ division.

The delay line has a characteristic impedance of about $186 \Omega$. Signal delay through the cable is 260 nsec . The output of the delay line is coupled to the Q324-Q334, V344AV344B stage. Since this stage has a low input impedance, R321 and R331 are added so the delay line is terminated in its characteristic impedance. C321 and C331 improve high-frequency response.
The T-coils in the plate circuits of V344A and V344B improve the high-frequency response. The signal from
the plates of V344A and V344B is coupled directly to the control grids of the cathode followers V363A and V363B. These cathode followers drive the transistors Q364 and Q374. Q364, V384, Q374 and V394 form a hybrid pushpull, cascode amplifier. The cathodes of V384 and V394 are current driven by Q364 and Q374. V384 and V394 elevate the dc level of the stage to the voltage necessary to drive the crt deflection plates.

OUTPUT DC LEVEL Adjustment. This adjustment R346 sets the operating level of the last few stages of the instrument so the average output voltage to the deflection plates of the crt is +150 volts.
CALIB Adjustment. This adjustment R398 adjusts the gain of the last stage by changing the emitter degeneration of Q364 and Q374. This permits the gain of the plug-in unit to be matched to the deflection factor of the indicator unit cathode-ray tube.

## Trigger Amplifier

The Type 9A2 contains a trigger amplifier to supply an internal trigger signal to the time-base circuitry. The TRIGGER switch selects the triggering signal from either of two similar trigger pickoff amplifiers: Q164-Q174 in Channel 1 or Q404-Q414 in the Output Amplifier. Each of these trigger pickoff amplifiers is an emitter-coupled difference amplifier with double-ended input and single ended output. The triggering signal from either pickoff amplifier circuit is obtained prior to signal delay. This allows the internal trigger signal to trigger the time-base circuitry before the vertical signal reaches the crt deflection plates.

The Channel 1 trigger signal is obtained from the Q133Q143 stage. The CHAN 1 TRIG DC LEVEL control R171 provides a means of adjusting the dc level of the Channel 1 trigger signal.

The composite trigger signal is obtained from the Q304Q314 stage. The COMP TRIG DC LEVEL control R412 provides a means of adjusting the dc level of the composite trigger signal. The dc level of the composite trigger signal is affected by the setting of the POSITION controls. Therefore, the COMP TRIG DC LEVEL adjustment is set with the POSITION controls at midrange.

The TRIGGER switch SW420 selects the triggering signal from the output of either Q164 (Channel 1 only) or Q414 (composite). The collector of the unused trigger pickoff amplifier is connected to the -12.2 -volt supply through R420.

The trigger output amplifier is a low input impedance amplifier having a transimpedance of 15 k ( $\Delta 3$ volt output $\div \Delta 0.2$ milliamp input). D425 prevents Q444 from saturating by limiting the positive input signal. D424 provides a path to the -12.2 -volt supply when D425 limits the positive input signal. Diode D423 limits the negative voltage excursion of the input and prevents overshoot when a large trigger signal is present.

The Zener diode D447 drops the voltage level of the trigger amplifier output signal about 15 volts so that the output to pin 11 of the interconnecting plug is nominally zero volts with a no-signal trace positioned to crt center.

## SECTION 4

## MAINTENANCE

## PREVENTIVE MAINTENANCE

## Recalibration

To assure accurate measurements, check the calibration of this instrument after each 500 hours of operation or every six months if used intermittently. Complete calibration instructions are given in Section 5.

## Visual Inspection

The Type 9A2 should be inspected occasionally for such defects as broken connections, broken or damaged ceramic strips, improperly seated tubes or transistors and heat-damaged parts.

The remedy for most visible defects is obvious; however, particular care must be taken if heat-damaged parts are located. Overheating is usually only a symptom of trouble. For this reason, it is essential to determine the actual cause of overheating before the heat-damaged parts are replaced; otherwise, the damage may be repeated.

## Cleaning

The Type 9A2 should be cleaned as often as operating conditions require. Accumulation of dirt in the instrument can cause overheating and component breakdown. Dirt on components acts as an insulating blanket and prevents efficient heat dissipation. It also provides an electrical conduction path.

To clean the interior of the Type 9A2, blow off the accumulated dust with dry, low-pressure air. Remove any dirt which remains with a soft paint brush or a cloth dampened with a mild detergent and water solution. A cottontipped applicator is useful for cleaning in narrow spaces or for cleaning ceramic terminal strips.

The front panel of the Type 9A2 can be cleaned with a soft cloth dampened with a mild solution of water and detergent. Do not use abrasive cleaners.

## CAUTION

Avoid the use of chemical cleaning agents which might damage the plastics used in this unit. Avoid chemicals such as benzene, toluene, xylene, acetone, or similar solvents.

## CORRECTIVE MAINTENANCE

## Soldering

Ceramic Terminal Strips. Solder used on the ceramic terminal strips should contain about $3 \%$ silver. Ordinary tin-lead solder can be used occasionally without damage to the ceramic terminal strips. Use a 40 - to 75 -watt soldering iron with a $1 / 8^{\prime \prime}$ wide chisel-shaped tip. If ordinary solder is used repeatedly or if excesive heat is applied, the solder-to-ceramic bond can be broken.

Solder containing 3\% silver is usually available locally or it can be purchased from Tektronix in one-pound rolls; order by Tektronix Part Number 251-514.

The following precautions should be observed when soldering ceramic terminal strips:

1. Use a hot iron for a short time. Apply only enough heat to make the solder flow freely.
2. Maintain a clean, properly tinned tip.
3. Avoid putting pressure on the ceramic terminal strip.
4. Do not attempt to fill the terminal-strip notch with solder; use only enough solder to cover the wires adequately.

Metal Terminals. When soldering metal terminals (e.g., interconnecting plug pins, switch terminals, potentiometers, etc.), ordinary $60 / 40$ solder can be used. The soldering iron should have a 40 - to 75 -watt rating with a $1 / 8$ " wide chiselshaped tip.

Observe the following precautions when soldering metal terminals.

1. Apply only enough heat to make the solder flow freely.
2. If a wire extends beyond the solder joint, clip the excess close to the joint.
3. Apply only enough solder to form a solid connection. Excess solder may impair the function of the part.

## Component Replacement

Certain parts in the instrument are easier to replace if a definite procedure is followed. The procedures for replacing these parts are outlined in the following paragraphs.
Many electrical components are mounted in a particular manner to reduce or control stray capacitance. Duplicate the original location and mounting when replacing components. When selecting replacement parts, remember that the physical nature of a component can affect its performance at high frequencies. After repair, check the instrument calibration.

## NOTE

Turn off the indicator unit power before replacing any components.

## Standard Parts

All electrical and mechanical part replacements for the Type 9A2 can be obtained through your local Tektronix Field Office or representative. However, since many of the electronic components are standard parts, they can generally be obtained locally in less time than is required to order them from the factory. Before purchasing replacement parts, consult the Parts List for values, tolerances, ratings and Tektronix Part Number.

## Special Parts

In addition to the standard electronic components, some special parts are used in the production of the Type 9A2. These parts are manufactured or selected by Tektronix to meet specific performance requirements, or are manufactured for Tektronix in accordance with our specifications. Most of the mechanical parts used in this instrument have been manufactured by Tektronix. These special parts are indicated in the Parts List by an asterisk preceding the part number. Order all special parts directly from your Tektronix Field Office or representative.

## Ceramic Terminal Strip Replacement

A complete ceramic terminal strip assembly is shown in Fig. 4-1. Replacement strips (including studs) and spacers are supplied under separate part numbers. The old spacers may be reused unless they are damaged.


Fig. 4-1. Ceramic terminal strip assembly.

After the damaged strip has been removed, place the undamaged spacers in the chasis holes. Then, carefully press the studs into the spacers until they are completely seated. If necessary, use a soft mallet and tap lightly, directly over the stud area of the strip.

## Tubes and Transistors

Do not replace tubes or transistors unless they are actually defective. If tubes or transistors are removed during routine maintenance, return them to their original sockets.
Static tube- or transistor-testers are not recommended for locating a defective tube or transistor. These testers often indicate a defective component when it is operating satisfactorily in a circuit, or may fail to indicate a characteristic which affects circuit performance. Since dynamic testers check operation under simulated circuit conditions, they provide a better check of component operation. However, the best overall test of tube or transistor performance is to substitute a new component or one which has been previously checked.

If a tube or transistor performs satisfactorily, do not replace it. Unnecessary replacement of components may require recalibration of the instrument. If tubes or transistors are replaced, check the operation of the unit.

## Rotary Switches

Individual wafers or mechanical parts of rotary switches are normally not replaced. If a switch is defective, replace the entire assembly. Replacement switches can be ordered either wired or unwired; refer to the Parts List for part numbers.

## TROUBLESHOOTING

## Introduction

The following information is provided to facilitate troubleshooting of the Type 9A2 if trouble develops. During troubleshooting, information contained in this section of the manual should be used along with informaton obtained from other sections (e.g., Diagrams, Operating Instructions, etc.).

## Troubleshooting Aids

Schematic Diagrams. Circuit diagrams are given on pullout pages in Section 6. The circuit numbers for each electronic component in this unit along with important voltages are shown on these diagrams.

Switch wafers shown on the diagrams are coded to indicate the position of the wafter in the complete switch assembly. The number portion of the code refers to the wafer number counting from the front or mounting end of the switch toward the rear. The letters " $F$ " and " $R$ " indicate whether the front or rear of the wafer is used to perform the particular switching function.

Wiring Color-Code. All insulated wire used in the Type 9A2 is color-coded to facilitate circuit tracing. The widest color stripe identifies the first color of the code. Regulated voltages supplied by the indicator unit are identified by three color stripes and the following background colorcode: white, positive voltage; tan, negative voltage.

$$
\begin{array}{ll}
+300 \text { volts } & \text { Orange-black-brown on white } \\
+125 \text { volts } & \text { Brown-red-brown on white } \\
-12.2 \text { volts } & \text { Brown-red-black on tan } \\
-100 \text { volts } & \text { Brown-black-brown on tan }
\end{array}
$$

The heater wiring is indicated by a white background with a blue first stripe. The remainder of the wiring in the Type 9A2 is color-coded to facilitate point-to-point circuit tracing.
Resistor Color-Code. Some stable metal-film resistors are used in this instrument. These resistors can be identified by their gray body color. If a metal-film resistor has a value indicated by three significant figures and a multiplier, it will be color-coded according to the EIA standard. If it has a value indicated by four significant figures and a multiplier, the value will be printed on the body of the resistor. For example, a $333-\mathrm{k}$ resistor will be color-coded, but a $333.5-\mathrm{k}$ resistor will have its value printed on the resistor body. The color-code sequence is shown in Fig. 4-2 and Table 4-1.


Fig. 4-2. Color-coding of metal-film resistors.

Composition resistors are color-coded according to the EIA standard resistor color-code.

TABLE 4-1
Color-Code Sequence

| Color | 1st <br> Sig. <br> Fig. | 2nd <br> Sig. <br> Fig. | 3rd <br> Sig. <br> Fig. | Multiplier | (士) \% <br> Toler- <br> ance |
| :--- | :---: | :---: | :---: | :--- | :---: |
| Black | 0 | 0 | 0 | 1 | - |
| Brown | 1 | 1 | 1 | 10 | 1 |
| Red | 2 | 2 | 2 | 100 | 2 |
| Orange | 3 | 3 | 3 | 1,000 | - |
| Yellow | 4 | 4 | 4 | 10,000 | - |
| Green | 5 | 5 | 5 | 100,000 | 0.50 |
| Blue | 6 | 6 | 6 | $1,000,000$ | 0.25 |
| Violet | 7 | 7 | 7 | $10,000,000$ | 0.10 |
| Gray | 8 | 8 | 8 | $100,000,000$ | 0.05 |
| White | 9 | 9 | 9 | $1,000,000,000$ | - |
| Gold | - | - | - | 0.1 | 5 |
| Silver | - | - | - | 0.01 | - |
| No Color | - | - | - | - | 10 |

## Test Equipment

The following equipment will be useful in troubleshooting the Type 9A2.

1. Dynamic Transistor Tester

Purpose: To test transistors and diodes used in Type 9A2.

Description: Tektronix Type 575 Transistor-Curve Tracer or equivalent.
2. Dc Voltmeter

Purpose: To check operating voltages in the unit.
Description: 20,000 ohms/volt.
3. Test Oscilloscope

Purpose: To check circuit operation.

Description: Tektronix Type 561A with Type 3A75 and Type 2B67 plug-in units, or equivalent.
4. Flexible Plug-In Extension Cable

Purpose: Permits maximum accessibility to the Type 9A2 while operating the unit outside of the plug-in compartment.

Description: 30", 24-pin. Tektronix Part Number 012-066.

## Check Front-Panel Controls

Before proceeding with extensive troubleshooting, check the front-panel control settings. In addition, check the frontpanel screwdriver adjustments for proper adjustment. An incorrect control setting can produce an apparent trouble. If in doubt as to the proper setting of a control or adjustment, see "First-Time Operation" in Section 2.

## Check Indicator Unit and Time Base

The indicator unit and time base can be checked for proper operation by substituting another amplifier plug-in unitpreferably another Type 9A2-which is known to be operating properly. If the trouble persists after substitution, the indicator unit or time base is defective.

## Trouble Location

If the Type 9A2 is definitely at fault, make a careful operational check of the unit. Note the effect that each frontpanel control has on the symptom. Also check the effect of the calibration adjustments. The normal or abnormal operation of each control or adjustment may help isolate the trouble to the defective circuit.

After the trouble has been isolated to a particular circuit, perform a complete visual check of that circuit. Many troubles can be found most easily by visual means. If a visual check fails to detect the cause of trouble, check the tubes or transistors used in the circuit by replacing them with tubes or transistors known to be good (or check with a dynamic tester). Most of the troubles which occur result from tube or transistor failures. Be sure to return any tubes or transistors found to be good to their original sockets.

The following procedure may aid in location of the defective component after the tubes or transistors have been found to be good.

1. Isolate the trouble to a portion of the circuit if possible.
2. Recheck the reaction of the front-panel controls and calibration adjustments on the affected circuit.
3. Check the voltages in the circuit. Typical operating voltages are given on the schematic diagrams.
4. Check waveforms in the circuit with a test oscilloscope.
5. Check the components in the circuit (i.e., check for faulty capacitors, off-tolerance resistors, etc.).

## NOTES

## SECTION 5

## CALIBRATION

## Introduction

The Type 9A2 should be calibrated every 500 hours or every six months if used intermittently. If transistors, tubes, or other components are replaced, the calibration of the repaired circuit should be checked.

The following procedure is arranged in a sequence which will allow the instrument to be calibrated with the least interaction of adjustments and reconnection of equipment. If desired, the steps may be performed out of sequence or a step may be performed individually. However, it may be necessary to refer to the preceding step(s) and/or preliminary procedure for additional setup information.

All calibration adjustments in this unit are shown in Fig. $5-1$. Fig. 5-2 shows the holes in the indicator unit which provide access to the calibration adjustments on the right side of the Type 9A2.

NOTE
This procedure is written to provide a performance check of the instrument along with complete calibration. Steps entitled "Check" provide a check of an operational standard of the unit. "Adjust" steps provide a check of performance and adjustment procedure if necessary.

## EQUIPMENT REQUIRED

The following equipment, or equivalent, is required for a complete calibration of the Type 9A2.

1. Calibrated Type 506 Oscilloscope with Type 3B3 timebase plug-in unit.
2. Dc voltmeter; 20,000 ohms/volt, $2 \%$ accuracy.
3. Square-wave generator: frequency, 100 cps to 300 kc ; risetime, 20 nanoseconds or less; output amplitude, about 0.8 volts into 50 ohms. Tektronix Type 105 Square-Wave Generator recommended.
4. Fast-rise square-wave generator: frequency, 400 kc ; risetime, 3.5 nanoseconds or less; amplitude, 0.1 volt or greater. Tektronix Type 107 Square-Wave Generator recommended.
5. Constant amplitude sine-wave generator: frequency, 50 kc and 25 Mc ; amplitude, 40 millivolts. Tektronix Type 190B Constant-Amplitude Signal Generator recommended.
6. Input capacitance standardizer; 47 pf, BNC connectors. Tektronix Part No. 011-068.
7. Termination; 50 ohm, BNC connectors. Tektronix Part No. 011-049.
8. Attenuator; 50 ohm, 5XT, BNC connectors. Tektronix Part No. 011-060.
9. Cable; 50 ohm, $30^{\prime \prime}$ long, BNC connectors. Tektronix Part. No. 012-057.
10. BNC T connector. Tektronix Part No. 103-030.
11. Screwdriver; non-metallic. Tektronix Part No. 003-000.
12. Alignment tool; non-metallic. Tektronıx Part No. 003-301.
13. Flexible plug-in extension. Tektronix Part No. 012-066.

## PRELIMINARY PROCEDURE

1. Remove both side panels from the indicator unit.
2. Insert the Type 9A2 into the left plug-in compartment.
3. Insert the time-base plug-in into the right plug-in compartment using the plug-in extension.
4. Preset the indicator unit front-panel controls as follows:

## Calibrator <br> 50 mVolts

The remaining controls can be adjusted to provide a suitable display.
5. Preset the time-base controls as follows:

| Mode | Norm. |
| :--- | :--- |
| Time/Div. | 1 mSec |
| Triggering |  |
| Level | Centered |
| Slope | + |
| Coupling | Auto. |
| Source | Int. |

6. Preset the Type 9A2 controls (both channels where applicable) as follows:

| POSITION | Midrange |
| :--- | :--- |
| AC DC GND | GND |
| VOLTS/DIV | .01 |
| VARIABLE | CALIB |
| MODE | CH 1 |
| INV (CH 1) NORM | NORM |
| TRIGGER | Pushed in |

7. Connect the indicator unit to the correct line voltage and turn the POWER switch ON. Allow 20 minutes warm up before making adjustments.

## CALIBRATION PROCEDURE

## 1. Adjust Channel 1 and 2 Dc Balance

a. Set the Channel 1 VOLTS/DIV switch to .01 .
b. Check-Rotate Channel 1 VARIABLE throughout its range. If the trace moves vertically, adjust the DC BAL control as follows:
c. Turn the DC BAL control to bring the trace near graticule center.

## NOTE

Both DC BAL controls are the dual-range or coarse-fine type. To use this type of control, turn the control slightly past the desired point of adjustment (coarse adjust). Then, reverse the direction of rotation and use the fine adjustment (about $30^{\circ}$ range) to establish balance.
d. Adjust the DC BAL control so there is no trace shift as the VARIABLE control is rotated throughout its range. This may take several readjustments of the DC BAL control.
e. Set the MODE switch to CH 2 and repeat the above procedure for Channel 2.

## 2. Check Chopped Mode Operation

a. Set the MODE switch to CHOP and position the two traces about 2 major divisions apart.
b. Set the time-base Time/Div. switch for a sweep rate of $5 \mu \mathrm{Sec}$.
c. Check the display for about one trace segment per major graticule division.

## 3. Check Alternate Mode Operation

a. Set the MODE switch to ALTER.
b. Turn the time-base Time/Div. switch throughout its range.
c. Check for trace alternation between Channel 1 and 2 at each Time/Div. setting.

## 4. Adjusł Output Dc Level

a. Position both traces to the graticule centerline.
b. Connect a jumper between the crt vertical deflection pins (on left side of crt).
c. Measure the dc voltage from the vertical deflection pins to ground.
d. Check-Voltage should be +150 volts. If not, adjust as follows:
e. Adjust OUTPUT DC LEVEL R346 for an average voltage of +150 volts.

## 5. Adjust Channel 1 Trigger Dc Level

a. Turn the MODE switch to CH 1 .


Fig. 5-1. Calibrafion adjusîmenf̂s in Type 9A2 (leff-side view).
b. Pull the TRIGGER switch out to the CHANNEL 1 ONLY position.
c. Connect the de voltmeter to pin 12 of the Horizontal plug-in interconnecting plug.
d. Check-Voltage reading should be zero volts. If not, adjust as follows:
e. Adjust CHAN 1 TRIG DC LEVEL R171 for zero volts reading on the meter.

## 6. Adjust Composite Trigger Dc Level

a. Push the TRIGGER switch in.
b. Position the trace to the graticule centerline.
c. Check-Voltage reading should be zero volts. If not, adjust as follows:
d. Adjust COMP TRIG DC LEVEL R312 for zero volt reading on the meter.
e. Disconnect the voltmeter.

## 7. Adjust Channel 110 Mv Gain

a. Set the Channel 1 AC DC GND switch to DC.
b. Make sure the VARIABLE control is set to CALIB (UNCAL light off).
c. Connect the 50 -millivolt signal from the indicator unit Cal Out connector to Channel 1 input.
d. Turn the CALIB control to midrange.
e. Check-Check for 5 major divisions of deflection. If incorrect, adjust as follows:
f. Adjust 10 MV GAIN R149 for 5 major divisions of vertical deflection.

## 8. Adjust Channel 120 Mv Gain

a. Set the Channel 1 VOLTS/DIV switch to .02 .
b. Set the indicator-unit Calibrator to . 1 Volts.
c. Check-Check for 5 major divisions of deflection. If incorrect, adjust as follows:
d. Adjust 20 MV GAIN RT47 for 5 major divisions of vertical deflection.

## 9. Adjust Channel 210 Mv Gain

a. Connect the calibrator output to both inputs using the BNC T connector and a 50 -ohm cable.
b. Set the Channel 2 AC DC GND switch to DC.
c. Set both VOLTS/DIV switches to .01 .
d. Make sure both VARIABLE controls are set to CALIB (UNCAL light off).
e. Set the Calibrator for 50 mVolts output.
f. Position both traces to the center of the graticule.
g. Set the MODE switch to ADDED and the INV (CH 1) NORM switch to INV.
h. Check-Check for a straight line indicating identical gain in Channels 1 and 2. If square-wave signal is seen, adjust as follows:
i. Adjust 10 MV GAIN R249 for complete cancellation of the Channel 1 signal indicated by a straight line.

## 10. Adjust Channel 220 Mv Gain

a. Set the Calibrator to .1 Volts.
b. Set both VOLTS/DIV switches to .02 .
c. Check—Check for a straight line indicating identical gain in Channels 1 and 2. If square-wave signal is seen, adjust as follows:
d. Adjust 20 MV GAIN R247 for a straight line.

## 11. Check Both Variable Controls

a. Set both VOLTS/DIV switches to .01 and both VARIABLE controls to CALIB.
b. Set the INV (CH 1) NORM switch to NORM.
c. Connect a 50 mVolts signal from the Calibrator to the input of both channels.
d. Set the MODE switch to CH 1 . The display should be 5 divisions high.
e. Turn the Channel 1 VARIABLE control fully counterclockwise. The display should be reduced to 2 major divisions or less.
f. Check that the Channel 1 UNCAL light is on.
g. Return the Channel 1 VARIABLE to CALIB; the UN. CAL light should go out.
h. Set the MODE switch to CH 2. Repeat steps e, $f$ and g with Channel 2 controls to check the Channel 2 VARIABLE control.
i. Disconnect the calibrator signal from the input connectors.

## 12. Check Grid Current

a. Set both VOLTS/DIV switches to .02 and VARIABLE controls to CALIB.
b. Set both $A C$ DC GND switches to GND.
c. Set the MODE switch to CH 1.
d. Note Channel 1 trace vertical position in the GND position and then set the Channel 1 AC DC GND switch to $A C$. Trace shift should not exceed 2 minor divisions $(4 \mathrm{~mm})$.
e. Set the MODE switch to CH 2 and repeat step $d$ for Channel 2.

## 13. Adjust Channel 1 and 2 Input Capacitance

a. Connect the square-wave generator (Type 105) to the Channel 1 input through the 5XT and the capacitance standardizer.
b. Set the generator output frequency to 1 kc .
c. Set the Channel 1 VOLTS/DIV switch to .01 .

## Calibration-Type 9A2

d. Set the Channel 1 AC DC GND switch to DC.
e. Set the MODE switch to CH 1 and the INV (CH 1) NORM switch to NORM.
f. Set the generator output amplitude for 4 major divisions of deflection.
g. Set the time-base Time/Div. switch to .2 .
h. Adjust C111 for optimum flat-top waveform.
i. Set the Channel 1 VOLTS/DIV switch to .02 .
i. Adjust Cl 12 for optimum flat-top waveform.
k. Remove the signal from Channel 1 input and connect to Channel 2 input.
I. Set the MODE switch to CH 2 .
m . Set the Channel 2 VOLTS/DIV switch to . 01 .
n. Set the Channel 2 AC DC GND switch to DC.
o. Adjust C211 for optimum flat-top waveform.
p. Set the Channel 2 VOLTS/DIV switch to .02 .
q. Adjust C212 for optimum flat-top waveform.

## 14. Adjust Channel 1 and 2 VOLTS/DIV Compensation

a. Use the same test setup used in step 13.
b. Adjust the Channel 2 VOLTS/DIV compensation as shown in Table 5-1. Readjust the generator output with each setting of the VOLTS/DIV switch to provide 4 divisions of deflection (except in the 2 position where the maximum deflection will be about 1 division).
c. Connect the generator signal to Channel 1 input.
d. Set the MODE switch to CH 1 and adjust the Channel 1 VOLTS/DIV compensation as shown in Table 5-1.

TABLE 5-1

| VOLTS/DIV <br> Switch <br> Setting | Channel 1 <br> Adjust for Optimum |  | Channel 2 <br> Adjust for Optimum |  |
| :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |
|  | Square | Flat | Square | Flat |
|  | Corner | Top | Corner | Top |
| . 05 | Cl03C | C103B | C203C | C203B |
| . 1 | C105C | C105B | C205C | C205B |
| . 2 | C107C | C107B | C207C | C207B |
| 2 | C109C | C109B | C209C | C209B |

## 15. Adjust High-Frequency Compensation

a. Set the Channel 1 VOLTS/DIV switch to .01 and the VARIABLE to CALIB.
b. Set the MODE switch to CH 1 .
c. Set the time-base Time/Div. switch to $.5 \mu \mathrm{Sec}$.
d. Connect the fast-rise square-wave generator (Type 107) signal to Channel 1 input through a 5XT attenuator and a 50 -ohm termination.


Fig. 5-2. Indicator unit access holes used to calibrate the Type 9 A2.
e. Set the generator output frequency to 400 kc .
f. Set the generator output amplitude for a 4-division display.
g. Adjust C339 (see Fig. 5-2) for the flattest top on the square wave.
h. Pull the time-base magnifier switch on and set the POSITION control to display the rising edge of the waveform.
i. Adjust L344 and L354 for optimum square corner.
i. Adjust L384 and L394 for flattest area immediately following the leading edge of the waveform.
k. Repeat step i if necessary to maintain a square corner.
I. Set Channel 1 VOLTS/DIV switch to 01.
m . Adjust the square-wave generator output amplitude for 4 divisions of deflection.
n. Adjust Cl 49 for optimum square corner.
o. Repeat the preceding steps, making minor adjustments, to obtain a square corner and flat top on the display waveform. Maximum aberrations are 1 mm .
p. Set the MODE switch to CH 2.
q. Remove the signal from Channel 1 input and apply it to Channel 2 input.
r. Set the Channel 2 VOLTS/DIV switch to .02 and adjust the output of the generator for 4 divisions of deflection.
s. Check the waveform for maximum aberrations of 1 mm .
t. Set the Channel 2 VOLTS/DIV switch to .01 and adjust the output of the generator for 4 divisions of deflection.
u. Adjust C249 for a square leading corner on the waveform.

## 16. Check VOLTS/DIV Attenuation Ratios

a. Set the indicator unit Calibrator for 50 mVolts output.
b. Connect the Cal Out connector to the Channel 1 input with a 50 -ohm cable.
c. Set the time-base Time/Div. switch to 5 mSec .
d. Set the MODE switch to CH 1 .
e. Check for proper vertical deflection in each position of the Channel 1 VOLTS/DIV switch using Table 5-2 as a guide.
f. Set the MODE switch to CH 2 and connect the Ca Out signal to Channel 2 input. Check the attenuation of Channel 2 VOLTS/DIV switch using Table 5-2 as a guide.
g. Disconnect the calibrator signal from the input connector.

NOTE
Since the indicator unit calibrator accuracy is $\pm 3 \%$ and the rated accuracy of the input attenuators is $\pm 3 \%$, the maximum allowable deviation from the indicated deflection is $\pm 6 \%$.

TABLE 5-2

| VOLTS/DIV <br> Switch <br> Setting | Calibrator <br> Output <br> (peak-to-peak) | Vertical <br> Deflection <br> in Divisions |
| :---: | :---: | :---: |
| .01 | 50 mVolts | 5 |
| .02 | .1 Volts | 5 |
| .05 | .2 Volts | 4 |
| .1 | .5 Volts | 5 |
| .2 | 1 Volt | 5 |
| .5 | 2 Volts | 4 |
| 1 | 5 Volts | 5 |
| 2 | 10 Volts | 5 |
| 5 | 20 Volts | 4 |
| 10 | 50 Volts | 5 |

## 17. Check Bandpass

a. Set the Channel 1 VOLTS/DIV switch to .01 .
b. Set the MODE switch to CH 1 .
c. Connect the constant amplitude sine-wave generator to CH 1 input through a 50 -ohm termination.
d. Set the generator output frequency to 50 kc and adjust the output amplitude for a 4-division display.
e. Without changing the output amplitude, change the generator frequency to 20 Mc .
f. Amplitude of the display should be 2.8 divisions or greater.

## 18. Check Channel Isolation

a. Use the same test setup used in step 17.
b. Set the generator output frequency to 20 Mc .
c. Set the Channel 1 VOLTS/DIV switch to 10 and the Channel 2 VOLTS/DIV switch to . 01 .
d. Set the MODE switch to ALTER.
e. Adjust the generator output amplitude for 1 division of deflection on Channel 1.
f. Channel 2 deflection should be 2 mm or less, indicating channel isolation of $5,000: 1$ or better.

## NOTE

The attenuator are not shielded adequately unless the indicator-unit side panels are in place. Therefore, to obtain an accurate measurement of channel isolation, replace the left side panel.

## NOTES

# SECTION 6 <br> PARTS LIST AND SCHEMATICS 

## PARTS ORDERING INFORMATION

Replacement parts are available from or through your local Tektronix Field Office.
Changes to Tektronix instruments are sometimes made to accommodate improved components as they become available, and to give you the benefit of the latest circuit improvements developed in our engineering department. It is therefore important, when ordering parts, to include the following information in your order: Part number including any suffix, instrument type, serial number, and modification number if applicable.

If a part you have ordered has been replaced with a new or improved part, your local Tektronix Field Office will contact you concerning any change in part number.

## ABBREVIATIONS AND SYMBOLS

| a or amp | amperes | mm | millimeter |
| :---: | :---: | :---: | :---: |
| BHS | binding head steel | meg or M | megohms or mega (10 ${ }^{6}$ ) |
| C | carbon | met. | metal |
| cer | ceramic | $\mu$ | micro, or $10^{-6}$ |
| cm | centimeter | n | nano, or $10^{-9}$ |
| comp | composition | $\Omega$ | ohm |
| cps | cycles per second | OD | outside diameter |
| crt | cathode-ray tube | OHS | oval head steel |
| CSK | counter sunk | p | pico, or $10^{-12}$ |
| dia | diameter | PHS | pan head steel |
| div | division | piv | peak inverse voltage |
| EMC | electrolytic, metal cased | plstc | plastic |
| EMT | electroyltic, metal tubular | PMC | paper, metal cased |
| ext | external | poly | polystyrene |
| f | farad | Prec | precision |
| F \& 1 | focus and intensity | PT | paper tubular |
| FHS | flat head steel | PTM | paper or plastic, tubular, molded |
| Fil HS | fillister head steel | RHS | round head steel |
| $g$ or $G$ | giga, or $10^{9}$ | rms | root mean square |
| Ge | germanium | sec | second |
| GMV | guaranteed minimum value | Si | silicon |
| h | henry | S/N | serial number |
| hex | hexagonal | t or T | tera, or $10^{12}$ |
| HHS | hex head steel | TD | toroid |
| HSS | hex socket steel | THS | truss head steel |
| HV | high voltage | tub. | tubular |
| ID | inside diameter | $v$ or V | volt |
| incd | incandescent | Var | variable |
| int | internal | w | watt |
| k or K | kilohms or kilo (103) | w/ | with |
| kc | kilocycle | w/o | without |
| m | milli, or $10^{-3}$ | WW | wire-wound |
| mc | megacycle |  |  |

## SPECIAL NOTES AND SYMBOLS

Part first added at this serial number.
000X Part removed after this serial number.
*000-000 Asterisk preceding Tektronix Part Number indicates manufactured by or for Tektronix, or reworked or checked components.

Use 000-000
Part number indicated is direct replacement.
Internal screwdriver adjustment.
Front-panel adjustment or connector.


EXPLODED VIEW


EXPLODED VIEW (Cont'd)


EXPLODED VIEW (Cont'd)

| REF. NO. | $\begin{aligned} & \text { PART } \\ & \text { NO. } \end{aligned}$ | SERIAL/MODEL NO. |  | $\begin{aligned} & \mathrm{Q} \\ & \mathrm{~T} \\ & \mathrm{Y} \end{aligned}$ | DESCRIPTION |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | EFF. | DISC. |  |  |
| 33 | $\begin{aligned} & \hline 131-149 \\ & -70-0 \\ & 210-004 \\ & 210-201 \\ & 211-406 \\ & 21-008 \end{aligned}$ |  |  | 1 - 1 1 2 2 | CONNECTOR, chassis mounted, 24 contact mounting hardware: (not included w/connector) LOCKWASHER, internal \#4 <br> LUG, solder, SE \#4 <br> NUT, hex, $4-40 \times 3 / 16$ inch <br> SCREW, $4-40 \times 1 / 4$ inch, BHS |
| 34 | 131-182 358-136 |  |  | 7 | CONNECTOR, terminal feed thru mounting hardware for each: (not included w/connector) BUSHING, teflon |
| 35 |  |  |  | $1$ | CONNECTOR, terminal mounting hardware: (not included w/connector) BUSHING, teflon |
| 36 | 136-015 213-044 |  |  | $\begin{aligned} & 2 \\ & 2 \\ & 2 \end{aligned}$ | SOCKET, STM9G <br> mounting hardware for each: (not included w/socket) SCREW, $5-32 \times 3 / 16$ inch, PHS |
| 37 | 136-101 213-055 |  |  | $4$ | SOCKET, 5 pin mounting hardware for each: (not included w/socket) SCREW, $2-56 \times 3 / 16$ inch, PHS |
| 38 | 136-174 213-044 |  |  | 2 <br>  <br> 2 | SOCKET, 9 pin mounting hardware for each: (not included w/socket) SCREW, $5-32 \times 3 / 16$ inch, PHS |
| 39 | 136-181 354-234 |  |  | $\begin{array}{r} 22 \\ - \\ 1 \end{array}$ | SOCKET, transistor, 3 pin mounting hardware for each: (not included w/socket) RING, mounting |
| 40 | $\begin{aligned} & 214-052 \\ & --- \\ & 210-004 \\ & 210-406 \end{aligned}$ |  |  | 1 - 2 2 | FASTENER, right, w/stop mounting hardware: (not included w/fastener) LOCKWASHER, internal, \#4 NUT, hex, $4-40 \times 3 / 16$ inch |
| 41 | $\begin{aligned} & 214-276 \\ & --0 \\ & 210-006 \\ & 210-407 \\ & 211-504 \end{aligned}$ |  |  | $\begin{gathered} 2 \\ - \\ 1 \\ 1 \\ 1 \end{gathered}$ | SPRING, ground mounting hardware for each: (not included w/spring) LOCKWASHER, internal, \#6 NUT, hex, $6-32 \times 1 / 4$ inch SCREW, $6-32 \times 1 / 4$ inch, BHS |
| $\begin{aligned} & 42 \\ & 43 \end{aligned}$ | $\begin{aligned} & 343-089 \\ & 351-037 \\ & \hdashline--- \\ & 210-004 \\ & 210-406 \\ & 211-013 \end{aligned}$ |  |  | $\left\|\begin{array}{l} 2 \\ 1 \\ - \\ 1 \\ 1 \\ 1 \end{array}\right\|$ | CLAMP, cable, delrin GUIDE, delrin mounting hardware: (not included w/guide) LOCKWASHER, internal, \#4 <br> NUT, hex, $4-40 \times 3 / 16$ inch SCREW, $4-40 \times 3 / 8$ inch, RHS |
| $\begin{aligned} & 44 \\ & 45 \\ & 46 \end{aligned}$ | $\begin{aligned} & 358-075 \\ & 387-647 \\ & 387-660 \end{aligned}$ |  |  | 2 1 1 | BUSHING, $1 / 4-32 \times 0.295$ inch long PLATE, rear PLATE, front |

EXPLODED VIEW (Cont'd)


EXPLODED VIEW (Cont'd)

| REF. NO. | $\begin{aligned} & \text { PART } \\ & \text { NO. } \end{aligned}$ | SERIAL/MODEL NO. |  | Q <br> T <br> Y. | DESCRIPTION |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | EFF. | DISC. |  |  |
| 65 | $\begin{array}{\|cc} -\cdots \\ \hdashline-\cdots- \\ 210-583 \\ 210-940 \end{array}$ |  |  | 1 1 | POT <br> mounting hardware: (not included w/pot) <br> NUT, hex, $1 / 4-32 \times 5 / 16$ inch <br> WASHER, $1 / 4$ ID $\times 3 / 8$ inch OD |
| 66 | $\begin{aligned} & 210-259 \\ & 213-055 \end{aligned}$ |  |  | $\begin{gathered} 6 \\ i \end{gathered}$ | LUG, solder, \#2 mounting hardware for each: (not included w/lug) SCREW, thread forming, $2-32 \times 3 / 16$ inch, PHS |
| 67 | $213-088$ |  |  | $1$ | COIL <br> mounting hardware: (not included w/coil) <br> SCREW, thread forming, \# $4 \times 1 / 4$ inch, PHS |
| 68 | $\begin{aligned} & -\cdots \\ & \hdashline-\cdots \\ & 210-223 \\ & 210-583 \end{aligned}$ |  |  | 1 | POT <br> mounting hardware: (not included w/pot) <br> LUG, solder, $1 / 4$ inch <br> NUT, hex, $1 / 4-32 \times 5 / 16$ inch |
| 69 | \| 213-054 |  |  | 1 | COIL <br> mounting hardware: (not included w/coil) <br> SCREW, thread forming, $6-32 \times 5 / 16$ inch, PHS |
| 70 | $\begin{gathered} -\cdots \\ \cdots \\ 210-203 \\ 210-478 \\ 210-601 \\ 211-538 \\ 211-553 \end{gathered}$ |  |  | $\begin{aligned} & - \\ & 1 \\ & 1 \\ & 1 \\ & 1 \\ & 1 \end{aligned}$ | RESISTOR, 10 watt mounting hardware: (not included w/resistor) <br> LUG, solder, SE \#6 <br> NUT, hex, $5 / 16 \times 21 / 32$ inch long <br> EYELET, tapered barrel <br> SCREW, $6-32 \times 5 / 16$ inch, FHS <br> SCREW, $6-32 \times 1 \frac{1}{2}$ inches, RHS |
| 71 | $\begin{array}{\|c} -\cdots \\ -\cdots- \\ 210-478 \\ 211-507 \\ 211-544 \end{array}$ |  |  | $\begin{aligned} & - \\ & 1 \\ & 1 \\ & 1 \end{aligned}$ | RESISTOR, 5 watt <br> mounting hardware: (not included w/resistor) <br> NUT, hex, $5 / 16 \times 21 / 32$ inch long <br> SCREW, $6-32 \times 5 / 16$ inch, BHS <br> SCREW, $6-32 \times 3 / 4$ inch, THS |
| 72 | $\begin{aligned} & 343-006 \\ & -- \\ & 210-457 \\ & 210-803 \\ & 211-538 \end{aligned}$ |  |  | $\begin{aligned} & 2 \\ & - \\ & 1 \\ & 1 \\ & 1 \end{aligned}$ | CLAMP, cable, $1 / 2$ inch, plastic mounting hardware for each: (not included w/clamp) NUT, keps, $6-32 \times 5 / 16$ inch WASHER, $6 \mathrm{~L} \times 3 / 8$ inch, flat SCREW, $6-32 \times 5 / 16$ inch, FHS |
| 73 | $119-033$ $-131-272$ $200-568$ $210-457$ $211-507$ $211-529$ $343-108$ |  |  | $\begin{aligned} & 1 \\ & 2 \\ & 2 \\ & 1 \\ & 1 \\ & 3 \\ & 1 \\ & 1 \end{aligned}$ | DELAY LINE ASSEMBLY assembly includes: CONNECTOR, cable COVER, delay line NUT, keps, $6-32 \times 5 / 16$ inch SCREW, $6-32 \times 5 / 16$ inch, BHS SCREW, $6-32 \times 1 \frac{1}{4}$ inches, BHS CLAMP, delay line |

EXPLODED VIEW (Cont'd)


## ELECTRICAL PARTS

Values are fixed unless marked Variable.

| Ckt. No. | Tektronix <br> Part No. |  |
| :--- | ---: | :--- |
|  |  |  |
| B113 | $150-027$ | Neon, NE-23 |
| B150 | $150-025$ | Neon, NE-2E |
| B213 | $150-027$ | Neon, NE-23 |
| B250 | $150-025$ | Neon, NE-2E |

Description

## Bulbs

$$
\begin{aligned}
& \text { Neon, NE-23 } \\
& \text { Neon, NE-2E } \\
& \text { Neon, NE-23 } \\
& \text { Neon, NE-2E }
\end{aligned}
$$

## Capacitors

Tolerance $\pm 20 \%$ unless otherwise indicated.

| C101 | *285-609 | $0.1 \mu \mathrm{f}$ | PTM |  | 600 v | 10\% |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| C103A | 281-501 | 4.7 pf | Cer |  | 500 v | $\pm 1 \mathrm{pf}$ |
| C103B | 281-103 | 1.8-13 pf | Air | Var |  |  |
| C103C | 281-103 | 1.8-13 pf | Air | Var |  |  |
| CI03D | 281-541 | 6.8 pf | Cer |  | 500 v | 10\% |
| C105A | 281-501 | 4.7 pf | Cer |  | 500 v | $\pm 1 \mathrm{pf}$ |
| C105B | 281-103 | 1.8-13 pf | Air | Var |  |  |
| C105C | 281-101 | 1.5-9.1 pf | Air | Var |  |  |
| C105E | 281-503 | 8 pf | Cer |  | 500 v | $\pm 0.5 \mathrm{pf}$ |
| C107A | 281-509 | 15 pf | Cer |  | 500 v | 10\% |
| C107B | 281-103 | 1.8-13 pf | Air | Var |  |  |
| C107C | 281-101 | 1.5-9.1 pf | Air | Var |  |  |
| C107E | 281-513 | 27 pf | Cer |  | 500 v |  |
| C109A | 281-509 | 15 pf | Cer |  | 500 v | 10\% |
| C109B | 281-103 | 1.8-13 pf | Air | Var |  |  |
| C109C | 281-101 | 1.5-9.1 pf | Air | Var |  |  |
| C109E | 283-541 | 50 pf | Mica |  | 500 v | 10\% |
| C110 | 281-509 | 15 pf | Cer |  | 500 v | 10\% |
| C111 | 281-103 | 1.8-13 pf | Air | Var |  |  |
| C112 | 281-101 | 1.5-9.1 pf | Air | Var |  |  |
| C113 | 283-068 | $0.01 \mu \mathrm{f}$ | Cer |  | 500 v |  |
| C114 | 281-500 | 2.2 pf | Cer |  | 500 v | $\pm 0.5 \mathrm{pf}$ |
| C115 | 283-057 | $0.1 \mu \mathrm{f}$ | Cer |  | 200 v |  |
| C123 | 283-003 | $0.01 \mu \mathrm{f}$ | Cer |  | 150 v |  |
| Cl 25 | 283-003 | $0.01 \mu \mathrm{f}$ | Cer |  | 150 v |  |
| C134 | 283-000 | $0.001 \mu \mathrm{f}$ | Cer |  | 150 v |  |
| C144 | 283-000 | $0.001 \mu \mathrm{f}$ | Cer |  | 150 v |  |
| C145 | 281-512 | 27 pf | Cer |  | 500 v | 10\% |
| C149 | 281-022 | $8-50 \mathrm{pf}$ | Cer | Var |  |  |
| C167 | 281-510 | 22 pf | Cer |  | 500 v |  |

Parts List-Type 9A2

| Capacitors (Cont'd) |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Ckt. No. | Tektronix Part No. |  | Descrip |  |  |  | S/N Range |
| C201 | *285-609 | $0.1 \mu \mathrm{f}$ | PTM |  | 600 v | 10\% |  |
| C203A | 281-501 | 4.7 pf | Cer |  | 500 v | $\pm 1 \mathrm{pf}$ |  |
| C203B | 281-103 | 1.8-13 pf | Air | Var |  |  |  |
| C203C | 281-103 | 1.8-13 pf | Air | Var |  |  |  |
| C203D | 281-541 | 6.8 pf | Cer |  | 500 v | 10\% |  |
| C205A | 281-501 | 4.7 pf | Cer |  | 500 v | $\pm 1 \mathrm{pf}$ |  |
| C205B | 281-103 | 1.8-13 pf | Air | Var |  |  |  |
| C205C | 281-101 | 1.5-9.1 pf | Air | Var |  |  |  |
| C205E | 281-503 | 8 pf | Cer |  | 500 v | $\pm 0.5 \mathrm{pf}$ |  |
| C207A | 281-509 | 15 pf | Cer |  | 500 v | 10\% |  |
| C207B | 281-103 | 1.8-13 pf | Air | Var |  |  |  |
| C207C | 281-101 | 1.5-9.1 pf | Air | Var |  |  |  |
| C207E | 281-513 | 27 pf | Cer |  | 500 v |  |  |
| C209A | 281-509 | 15 pf | Cer |  | 500 v | 10\% |  |
| C209B | 281-103 | 1.8-13 pf | Air | Var |  |  |  |
| C209C | 281-101 | 1.5-9.1 pf | Air | Var |  |  |  |
| C209E | 283-541 | 500 pf | Mica |  | 500 v | 10\% |  |
| C210 | 281-509 | 15 pf | Cer |  | 500 v | 10\% |  |
| C211 | 281-103 | 1.8-13 pf | Air | Var |  |  |  |
| C212 | 281-101 | 1.5-9.1 pf | Air | Var |  |  |  |
| C213 | 283-068 | $0.01 \mu \mathrm{f}$ | Cer |  | 500 v |  |  |
| C214 | 281-500 | 2.2 pf | Cer |  | 500 v | $\pm 0.5 \mathrm{pf}$ |  |
| C215 | 283-057 | $0.1 \mu \mathrm{f}$ | Cer |  | 200 v |  |  |
| C223 | 283-003 | $0.01 \mu \mathrm{f}$ | Cer |  | 150 v |  |  |
| C225 | 283-003 | $0.01 \mu \mathrm{f}$ | Cer |  | 150 v |  |  |
| C234 | 283-000 | $0.001 \mu \mathrm{f}$ | Cer |  | 150 v |  |  |
| C244 | 283-000 | $0.001 \mu \mathrm{f}$ | Cer |  | 150 v |  |  |
| C245 | 281-512 | 27 pf | Cer |  | 500 v | 10\% |  |
| C249 | 281-022 | $8-50 \mathrm{pf}$ | Cer | Var |  |  |  |
| C256 | 281-593 | 3.9 pf | Cer |  |  | 10\% |  |
| C260 | 281-523 | 100 pf | Cer |  | 350 v |  |  |
| C262 | 283-051 | $0.0033 \mu \mathrm{f}$ | Cer |  | 100 v | 5\% |  |
| C263 | 283-003 | $0.01 \mu \mathrm{f}$ | Cer |  | 150 v |  |  |
| C265 | 283-057 | $0.1 \mu \mathrm{f}$ | Cer |  | 200 v |  |  |
| C269 | 283-003 | $0.01 \mu \mathrm{f}$ | Cer |  | 150 v |  |  |
| C271 | 281-513 | 27 pf | Cer |  | 500 v |  |  |
| C277 | 281-523 | 100 pf | Cer |  | 350 v |  |  |
| C287 | 281-523 | 100 pf | Cer |  | 350 v |  |  |
| C291 | 283-003 | $0.01 \mu \mathrm{f}$ | Cer |  | 150 v |  |  |
| C300 | 281-621 | 12 pf | Cer |  | 500 v | 1\% |  |
| C303 | 281-622 | 47 pf | Cer |  | 500 v | 1\% |  |
| C305 | 281-546 | 330 pf | Cer |  | 500 v | 10\% |  |
| C307 | 281-575 | 39 pf | Cer |  | 500 v | 1\% |  |
| C309 | 281-578 | 18 pf | Cer |  | 500 v | 5\% |  |
| C321 | 281-578 | 18 pf | Cer |  | 500 v | 5\% |  |

Ckt. No. $\begin{aligned} & \text { Tektronix } \\ & \text { Part No. }\end{aligned}$

C331

## C339

C345
C349
C363

C374

## C379

C383
C388
C392

C403

## C407

C413
C447
C450

C451

## C452

C453
C457
C458

C461
C462
$283-003$
$283-026$
$290-134$
$283-004$
$283-003$

$283-059$
$283-059$
$283-003$
$283-026$
$290-134$
$283-004$
$283-003$

$283-059$
$283-059$
$281-578$
$281-036$
$283-000$
$281-619$
$283-003$

$283-003$
$283-026$
$283-006$
$281-504$
$283-001$

283-003
281-50
283-003
283-003
283-006

283-003

290-134
283-004
283-003

Description
S/N Range

| Cer |  | $500 v$ |
| :--- | :---: | :---: |$\quad 5 \%$


| Cer | 150 v |  |
| :--- | :--- | :--- |
| Cer | 500 v | $\pm 1 \mathrm{pf}$ |
| Cer | 150 v |  |
| Cer | 150 v |  |
| Cer | 500 v |  |


| Cer | $150 v$ |  |
| :---: | :---: | :---: |
| Cer | $25 v$ |  |
| EMT | $15 v$ |  |
| Cer | $150 v$ |  |
| Cer | $150 v$ |  |
|  |  |  |
| Cer | $25 v$ | $-20 \%+80 \%$ |
| Cer | $25 v$ | $-20 \%+80 \%$ |

Diodes

| D130 | $152-008$ | Germanium |  |
| :--- | ---: | :--- | :--- |
| D131 | $152-008$ | Germanium |  |
| D132 | $152-141$ | Silicon | 1N3605 |
| D140 | $152-141$ | Silicon | 1N3605 |
| D155 | $152-071$ | Germanium | ED-2007 |
|  |  |  |  |
|  |  |  |  |
| D156 | $152-071$ | Germanium | ED-2007 |
| D157 | $152-071$ | Germanium | ED-2007 |
| D158 | $152-071$ | Germanium | ED-2007 |
| D230 | $152-008$ | Germanium |  |
| D231 | $152-008$ | Germanium |  |
|  |  |  |  |
| D232 | $152-141$ | Silicon | 1N3605 |
| D240 | $152-141$ | Silicon | 1N3605 |
| D255 | $152-071$ | Germanium | ED-2007 |
| D256 | $152-071$ | Germanium | ED-2007 |
| D257 | $152-071$ | Germanium | ED-2007 |
|  |  |  |  |
| D258 | $152-071$ | Germanium | ED-2007 |
| D260 | $152-141$ | Silicon | 1N3605 |
| D263 | $152-141$ | Silicon | 1N3605 |
| D267 | $152-061$ | Silicon | Tek Spec |
| D278 | $152-141$ | Silicon | 1N3605 |

## Diodes (Cont'd)

| Ckt. No. | Tektronix Part No. | Description |  |  | S/N Range |
| :---: | :---: | :---: | :---: | :---: | :---: |
| D288 | 152-141 | Silicon | 1N3605 |  |  |
| D364 | 152-141 | Silicon | 1N3605 |  |  |
| D374 | 152-141 | Silicon | 1N3605 |  |  |
| D379 | 152-024 | Zener | 1N3024B | $1 \mathrm{w}, 15 \mathrm{v}, 5 \%$ |  |
| D423 | 152-141 | Silicon | 1N3605 |  |  |
| D424 | 152-141 | Silicon | 1N3605 |  |  |
| D425 | 152-141 | Silicon | 1N3605 |  |  |
| D447 | 152-031 | Zener | 1N718A | 1/4w, 15v, $5 \%$ |  |
| D451 | 152-057 | Zener | 1N3807B | $1 \mathrm{w}, 56 \mathrm{v}, 5 \%$ |  |


| LR103 | *108-283 | $0.13 \mu \mathrm{~h}$ | (wound on a $43 \Omega$ resistor) |  |
| :---: | :---: | :---: | :---: | :---: |
| LR105 | *108-286 | $0.17 \mu \mathrm{~h}$ | (wound on a $36 \Omega$ resistor) |  |
| LR107 | *108-283 | $0.13 \mu \mathrm{~h}$ | (wound on a $43 \Omega$ resistor) |  |
| LR203 | *108-283 | $0.13 \mu \mathrm{~h}$ | (wound on a $43 \Omega$ resistor) |  |
| LR205 | *108-286 | $0.17 \mu \mathrm{~h}$ | (wound on a $36 \Omega$ resistor) |  |
| LR207 | *108-283 | $0.13 \mu \mathrm{~h}$ | (wound on a $43 \Omega$ resistor) |  |
| L319 | *119-033 | Delay Line | Assy |  |
| L344 | *114-072 | $4.7 .5 \mu \mathrm{~h}$ | Var | Core 276-506 |
| L354 | *114-072 | $4.7 .5 \mu \mathrm{~h}$ | Var | Core 276-506 |
| L360 | *108-262 | $0.6 \mu \mathrm{~h}$ |  |  |
| 1361 | *108-262 | $0.6 \mu h$ |  |  |
| L379 | *108-200 | $40 \mu \mathrm{~h}$ |  |  |
| L384 | *114-053 | 3.3-6 $\mu \mathrm{h}$ | Var | Core 276-506 |
| L394 | *114-053 | $3.3-6 \mu \mathrm{~h}$ | Var | Core 276-506 |
| L452 | *108-016 | $29 \mu \mathrm{~h}$ |  |  |
| L461 | 108-245 | $3.9 \mu \mathrm{~h}$ |  |  |

Transistors

| Q133 | $151-089$ | 2N962 |
| :--- | :--- | :--- |
| Q134 | $151-131$ | 2N964 |
| Q143 | $151-089$ | 2N962 |
| Q144 | $151-131$ | 2N964 |
| Q164 | $151-076$ | 2N2048 |
|  |  |  |
| Q174 | $151-076$ | 2N2048 |
| Q233 | $151-089$ | 2N962 |
| Q234 | $151-131$ | 2N964 |
| Q243 | $151-089$ | 2N962 |
| Q244 | $151-131$ | 2N964 |
|  |  |  |
| Q260 | $151-063$ | 2N2207 |
| Q263 | $151-063$ | 2N2207 |
| Q275 | $151-076$ | 2N2048 |
| Q285 | $151-076$ | 2N2048 |
| Q304 | $151-131$ | 2N964 |

# Transistors (Cont'd) 

| Ckt. No. | Tektronix <br> Part No. | Description | S/N Range |
| :---: | :---: | :---: | :---: |
| Q314 | 151-131 | 2N964 |  |
| Q324 | 151-131 | 2N964 |  |
| Q334 | 151-131 | 2N964 |  |
| Q364 | *151-127 | Selected from 2N2369 |  |
| Q374 | *151-127 | Selected from 2N2369 |  |
| Q404 | 151-076 | 2N2048 |  |
| Q414 | 151-076 | 2N2048 |  |
| Q433 | 151-107 | 2N967 |  |
| Q444 | *151-121 | Selected from TA1938 |  |

## Resistors

Resistors are fixed, composition, $\pm 10 \%$ unless otherwise indicated.

| R101 | 302-100 | $10 \Omega$ | 1/2w |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| R103C | 322-643 | 600 k | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R103E | 322-644 | 666.6 k | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R105C | 322-620 | 800 k | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R105E | 322-614 | 250 k | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R107C | 322-621 | 900 k | 1/4w |  | Prec | 1\% |
| R107E | 322-608 | 111 k | 1/4w |  | Prec | 1\% |
| R109A | 315-101 | $100 \Omega$ | $1 / 4 \mathrm{w}$ |  |  | 5\% |
| R109C | 322-624 | 990 k | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R109D | 315-510 | $51 \Omega$ | 1/4w |  |  | 5\% |
| R109E | 322-605 | 10.1 k | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R109F | 316-150 | $15 \Omega$ | $1 / 4 \mathrm{w}$ |  |  |  |
| R110 | 315-470 | $47 \Omega$ | $1 / 4 \mathrm{w}$ |  |  | 5\% |
| R111 | 322-481 | 1 meg | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R113 | 315-105 | 1 meg | $1 / 4 \mathrm{w}$ |  |  | 5\% |
| R114 | 316-101 | $100 \Omega$ | $1 / 4 \mathrm{w}$ |  |  |  |
| R115 | 302-221 | $220 \Omega$ | $1 / 2 w$ |  |  |  |
| R116 | 301-562 | 5.6 k | 1/2w |  |  | 5\% |
| R119 | 311-321 | $2 \times 500 \mathrm{k}$ |  | Var |  | CH 1 DC BAL |
| R120 | 316-335 | 3.3 meg | $1 / 4 \mathrm{w}$ |  |  |  |
| R121 | 302-274 | 270 k | 1/2w |  |  |  |
| R123 | 316-182 | 1.8 k | $1 / 4 w$ |  |  |  |
| R124 | 316-101 | $100 \Omega$ | $1 / 4 \mathrm{w}$ |  |  |  |
| R125 | 316-101 | $100 \Omega$ | $1 / 4 \mathrm{w}$ |  |  |  |
| R126 | 301-562 | 5.6 k | 1/2w |  |  | 5\% |
| R133 | 301-431 | $430 \Omega$ | $1 / 2 w$ |  |  | 5\% |
| R134 | 315-391 | 390 ת | $1 / 4 \mathrm{w}$ |  |  | 5\% |
| R135 | 322-224 | 2.1 k | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R138 | 315-391 | $390 \Omega$ | $1 / 4 \mathrm{w}$ |  |  | 5\% |
| R139 $\dagger$ | *311-319 | $375 \Omega$ |  | Var |  | CH 1 VARIABLE |
| $\dagger$ Furnish | it with SW |  |  |  |  |  |

Resistors (Cont'd)

| Cki. No. | Tektronix <br> Part No. |  | Descri |  |  | S/N Range |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| R142 | 322-210 | 1.5 k | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R143 | 301-431 | $430 \Omega$ | $1 / 2 \mathrm{w}$ |  |  | 5\% |
| R144 | 315-391 | 390 ת | $1 / 4 \mathrm{w}$ |  |  | 5\% |
| R145 | 322-224 | 2.1 k | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R146 | 316-470 | $47 \Omega$ | $1 / 4 \mathrm{w}$ |  |  |  |
| R147 | 311-258 | $100 \Omega$ |  | Var |  | CH 120 MV GAIN |
| R148 | 315-101 | $100 \Omega$ | $1 / 4 \mathrm{w}$ |  |  | 5\% |
| R149 | 311-258 | $100 \Omega$ |  | Var |  | CH 110 MV GAIN |
| R150 | 316-104 | 100 k | $1 / 4 \mathrm{w}$ |  |  |  |
| R151 | 302-683 | 68 k | $1 / 2 \mathrm{w}$ |  |  |  |
| R152 | 301-163 | 16 k | 1/2 w |  |  | 5\% |
| R153 | 311-114 | $2 \times 250 \mathrm{k}$ |  | Var |  | CH 1 POSITION |
| R154 | 302-683 | 68 k | 1/2w |  |  |  |
| R160 | 315-471 | $470 \Omega$ | $1 / 4 \mathrm{w}$ |  |  | 5\% |
| R164 | 315-622 | 6.2 k | $1 / 4 \mathrm{w}$ |  |  | 5\% |
| R167 | 322-617 | $47.7 \Omega$ | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R170 | 315-471 | $470 \Omega$ | $1 / 4 \mathrm{w}$ |  |  | 5\% |
| R171 | 311-448 | 20 k |  | Var |  | CH 1 TRIG DC LEVEL |
| R172 | 316-333 | 33 k | $1 / 4 \mathrm{w}$ |  |  |  |
| R174 | 315-622 | 6.2 k | $1 / 4 \mathrm{w}$ |  |  | 5\% |
| R201 | 302-100 | $10 \Omega$ | 1/2w |  |  |  |
| R203C | 322-643 | 600 k | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R203E | 322-644 | 666.6 k | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R205C | 322-620 | 800 k | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R205E | 322-614 | 250 k | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R207C | 322-621 | 900 k | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R207E | 322-608 | 111 k | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R209A | 315-101 | $100 \Omega$ | $1 / 4 \mathrm{w}$ |  |  | 5\% |
| R209C | 322-624 | 990 k | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R209D | 315-510 | $51 \Omega$ | $1 / 4 \mathrm{w}$ |  |  | 5\% |
| R209E | 322-605 | 10.1 k | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R209F | 316-150 | $15 \Omega$ | $1 / 4 \mathrm{w}$ |  |  |  |
| R210 | 315-470 | $47 \Omega$ | $1 / 4 \mathrm{w}$ |  |  | 5\% |
| R211 | 322-481 | 1 meg | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R213 | 315-105 | 1 meg | $1 / 4 \mathrm{w}$ |  |  | 5\% |
| R214 | 316-101 | $100 \Omega$ | $1 / 4 \mathrm{w}$ |  |  |  |
| R215 | 302-221 | $220 \Omega$ | $1 / 2 \mathrm{w}$ |  |  |  |
| R216 | 301-562 | 5.6 k | $1 / 2 \mathrm{w}$ |  |  |  |
| R219 | 311-321 | $2 \times 500 \mathrm{k}$ |  | Var |  | CH 2 DC BAL |
| R220 | 316-335 | 3.3 meg | $1 / 4 \mathrm{w}$ |  |  |  |
| R221 | 302-274 | 270 k | 1/2w |  |  |  |
| R223 | 316-182 | 1.8 k | $1 / 4 \mathrm{w}$ |  |  |  |
| R224 | 316-101 | $100 \Omega$ | $1 / 4 \mathrm{w}$ |  |  |  |
| R225 | 316-101 | $100 \Omega$ | $1 / 4 \mathrm{w}$ |  |  |  |
| R226 | 301-562 | 5.6 k | $1 / 2 \mathrm{w}$ |  |  | 5\% |

Resistors (Cont'd)

| Ckt. No. | Tektronix Part No. |  | Descri |  |  | S/N Range |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| R223 | 301-431 | $430 \Omega$ | $1 / 2 \mathrm{w}$ |  |  | 5\% |
| R234 | 315-391 | $390 \Omega$ | $1 / 4 \mathrm{w}$ |  |  | 5\% |
| R235 | 322-224 | 2.1 k | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R238 | 315-391 | $390 \Omega$ | $1 / 4 \mathrm{w}$ |  |  | 5\% |
| R239 $\dagger$ | *311-319 | $375 \Omega$ |  | Var |  | CH 2 VARIABLE |
| R242 | 322-210 | 1.5 k | $1 / 4$ w |  | Prec | 1\% |
| R243 | 301-431 | $430 \Omega$ | $1 / 2 \mathrm{w}$ |  |  | 5\% |
| R244 | 315-391 | $390 \Omega$ | $1 / 4 \mathrm{w}$ |  |  | 5\% |
| R245 | 322-224 | 2.1 k | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R246 | 316-470 | $47 \Omega$ | $1 / 4 \mathrm{w}$ |  |  |  |
| R247 | 311-258 | $100 \Omega$ |  | Var |  | CH 220 MV GAIN |
| R248 | 315-101 | $100 \Omega$ | $1 / 4 \mathrm{w}$ |  |  | 5\% |
| R249 | 311-258 | $100 \Omega$ |  | Var |  | CH 210 MV GAIN |
| R250 | 316-104 | 100 k | 1/4 w |  |  |  |
| R251 | 302-683 | 68 k | $1 / 2 \mathrm{w}$ |  |  |  |
| R252 | 301-163 | 16 k | 1/2w |  |  | 5\% |
| R253 | 311-114 | $2 \times 250 \mathrm{k}$ |  | Var |  | CH 2 POSITION |
| R254 | 302-683 | 68 k | $1 / 2 \mathrm{w}$ |  |  |  |
| R260 | 316-102 | 1 k | $1 / 4 \mathrm{w}$ |  |  |  |
| R261 | 315-221 | $220 \Omega$ | $1 / 4 \mathrm{w}$ |  |  | 5\% |
| R262 | 315-682 | 6.8 k | $1 / 4$ w |  |  | 5\% |
| R263 | 302-470 | $47 \Omega$ | 1/2w |  |  |  |
| R264 | 316-102 | 1 k | $1 / 4 \mathrm{w}$ |  |  |  |
| R265 | 301-822 | 8.2 k | $1 / 2 \mathrm{w}$ |  |  | 5\% |
| R266 | 316-331 | $330 \Omega$ | $1 / 4 \mathrm{w}$ |  |  |  |
| R267 | 303-822 | 8.2 k | 1 w |  |  | 5\% |
| R268 | 301-183 | 18 k | $1 / 2 \mathrm{w}$ |  |  | 5\% |
| R269 | 316-101 | $100 \Omega$ | $1 / 4 \mathrm{w}$ |  |  |  |
| R271 | 302-103 | 10 k | 1/2 w |  |  |  |
| R273 | 301-202 | 2 k | $1 / 2 \mathrm{w}$ |  |  | 5\% |
| R275 | 301-202 | 2 k | $1 / 2 \mathrm{w}$ |  |  | 5\% |
| R276 | 322-287 | 9.53 k | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R277 | 322-239 | 3.01 k | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R278 | 322-155 | $402 \Omega$ | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R279 | 322-171 | $590 \Omega$ | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R285 | 301-202 | 2 k | $1 / 2 \mathrm{w}$ |  |  | 5\% |
| R286 | 322-287 | 9.53 k | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R287 | 322-239 | 3.01 k | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R288 | 322-155 | $402 \Omega$ | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R289 | 322-171 | $590 \Omega$ | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R291 | 302-100 | $10 \Omega$ | 1/2w |  |  |  |
| R292 | 315-152 | 1.5 k | $1 / 4 \mathrm{w}$ |  |  | 5\% |
| R293 | 322-207 | 1.4 k | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R294 | 322-207 | 1.4 k | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |
| R300 | 322-201 | 1.21 k | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |

Resisfors (Conf'd)

| Ckt. No. | Tektronix Part No. |  | Descri |  |  |  | S/N Range |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| R301 | 323-330 | 26.7 k | $1 / 2 \mathrm{w}$ |  | Prec | 1\% |  |
| R302 | 322-123 | $187 \Omega$ | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |  |
| R303 | 322-218 | 1.82 k | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |  |
| R304 | 322-123 | $187 \Omega$ | $1 / 4$ w |  | Prec | 1\% |  |
| R305 | 315-273 | 27 k | $1 / 4 \mathrm{w}$ |  |  | 5\% |  |
| R306 | 322-130 | $221 \Omega$ | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |  |
| R307 | 322-297 | 12.1 k | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |  |
| R308 | 322-089 | $82.5 \Omega$ | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |  |
| R309 | 316-221 | $220 \Omega$ | $1 / 4 \mathrm{w}$ |  |  |  |  |
| R311 | 323-330 | 26.7 k | $1 / 2 \mathrm{w}$ |  | Prec | 1\% |  |
| R312 | 322-123 | $187 \Omega$ | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |  |
| R314 | 322-123 | $187 \Omega$ | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |  |
| R318 | 322-089 | $82.5 \Omega$ | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |  |
| R321 | 323-089 | $82.5 \Omega$ | $1 / 4$ w |  | Prec | 1\% |  |
| R322 | 322-251 | 4.02 k | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |  |
| R324 | 322-151 | $365 \Omega$ |  |  | Prec | 1\% |  |
| R326 | 322-147 | $332 \Omega$ | $1 / 4$ w | - | Prec | 1\% |  |
| R331 | 323-089 | $82.5 \Omega$ | $1 / 2 \mathrm{w}$ |  | Prec | 1\% |  |
| R332 | 322-251 | 4.02 k | $1 / 4$ w |  | Prec | 1\% |  |
| R334 | 322-151 | $365 \Omega$ | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |  |
| R339 | 315-822 | 8.2 k | 1/4 w |  |  | 5\% |  |
| R341 | 316-470 | $47 \Omega$ | $1 / 4$ w |  |  |  |  |
| R344 | 322-200 | 1.18 k | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |  |
| R345 | 316-101 | $100 \Omega$ | $1 / 4 \mathrm{w}$ |  |  |  |  |
| R346 | 311-323 | 1.5 k |  | Var |  | OUTPU | LEVEL |
| R347 | 322-154 | $392 \Omega$ | 1/4 w |  | Prec | 1\% |  |
| R348 | 305-682 | 6.8 k | 2 w |  |  | 5\% |  |
| R349 | 315-821 | $820 \Omega$ | $1 / 4 \mathrm{w}$ |  |  | 5\% |  |
| R351 | 316-470 | $47 \Omega$ | $1 / 4 \mathrm{w}$ |  |  |  |  |
| R354 | 322-200 | 1.18 k | $1 / 4 \mathrm{w}$ |  | Prec | 1\% |  |
| R357 | 322-154 | $392 \Omega$ | 1/4 w |  | Prec | 1\% |  |
| R360 | 316-470 | 6.8 k | 2 w |  |  | 5\% |  |
| R358 | 305-682 | $47 \Omega$ | $1 / 4 \mathrm{w}$ |  |  |  |  |
| R361 | 316-470 | $47 \Omega$ | $1 / 4 \mathrm{w}$ |  |  |  |  |
| R363 | 301-471 | $470 \Omega$ | $1 / 2 \mathrm{w}$ |  |  | 5\% |  |
| R365 | 301-622 | 6.2 k | $1 / 2 \mathrm{w}$ |  |  | 5\% |  |
| R366 | 301-622 | 6.2 k | $1 / 2 \mathrm{w}$ |  |  | 5\% |  |
| R374 | 321-375 | 78.7 k | $1 / 8 \mathrm{w}$ |  | Prec | 1\% |  |
| R375 | 321-286 | 9.31 k | 1/8 w |  | Prec | 1\% |  |
| R376 | 308-232 | $320 \Omega$ | 5 w |  | WW | 5\% |  |
| R377 | 302-272 | 2.7 k | 1/2w |  |  |  |  |
| R380 | 315-470 | $47 \Omega$ | $1 / 4 \mathrm{w}$ |  |  | 5\% |  |
| R381 | 315-104 | 100 k | $1 / 4 \mathrm{w}$ |  |  | 5\% |  |
| R382 | 308-055 | 1.5 k | 10 w |  | WW | 5\% |  |
| R383 | 306-221 | $220 \Omega$ | 2 w |  |  |  |  |

Resistors (Cont'd)


Switches (Cont'd)

## Tektronix

Ckt. No.

## Part No.

| SW210 | $260-607 * 262-653$ |
| :--- | ---: |
| SW250 | $* 311-319$ |
| SW290t† | $260-442$ |
| SW420 | $260-447$ |

Rotary
Rotary
Slide CH 2 CALIB MODE

Description
S/N Range

CH 2 VOLTS/DIV TRIGGER CH 1 ONLY PULL

## Transformers

T155
T255
T263
$* 120-349$
*120-286
*120-193

Toroid
2T Bifilar
2T Bifilar
7T Bifilar

## Electron Tubes

| V113 | $154-306$ | 7586 |
| :--- | :--- | :--- |
| V123 | $154-306$ | 7586 |
| V213 | $154-306$ | 7586 |
| V223 | $154-306$ | 7586 |
| V344 | $154-187$ | 6 DJ8 |
|  |  |  |
| V363 | $154-187$ | 6 DJ8 |
| V384 | $154-361$ | 8233 |
| V394 | $154-361$ | 8233 |

$\dagger$ SW250 and R229 furnished as a unit.
$\dagger$ SW290 and SW155 are concentric. Furnished as a unit.



## MANUAL CHANGE INFORMATION

At Tektronix, we continually strive to keep up with latest electronic developments by adding circuit and component improvements to our instruments as soon as they are developed and tested.

Sometimes, due to printing and shipping requirements, we can't get these changes immediately into printed manuals. Hence, your manual may contain new change information on following pages. If it does not, your manual is correct as printed.


[^0]:    Adjusted
    Deflection $=$ VOLTS/DIV setting $\times$ Deflection Conversion Factor

