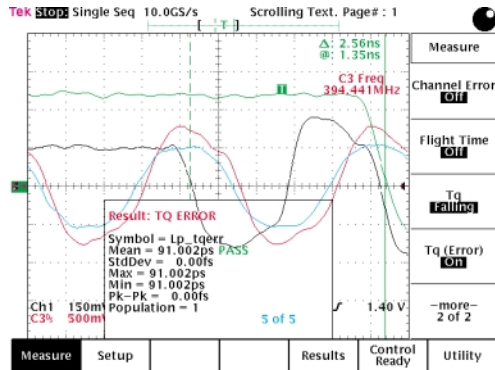


TDSRBS1

RAMBUS® TIMING MEASUREMENT SOFTWARE



FEATURES AND BENEFITS

- Measure Rambus-specific Timing Parameters of: Setup Time, Hold Time, Timing Quality, Timing Error, Channel Error, Flight Time, Rise Time, and Fall Time
- User-selectable Quiescent Time and Clock Frequency
- Measurements on Read and Write Cycles
- Automatically Determines Read or Write Cycle
- Measurements on Odd and Even Data Fields
- Measurement Results Can be Returned as Statistics, Profile or Histograms
- Installs Inside the TDS694C Oscilloscope Requiring no External GPIB, RS-232 or PC
- Provided on Floppy Disk for Easy Customer Installation
- Uses Familiar, User-friendly TDS User Interface
- Offers Full Measurement Automation and Control Through GPIB

APPLICATIONS

Characterize the Timing Performance of your Rambus Technology Designs

USER-INSTALLED, OSCILLOSCOPE RESIDENT RAMBUS TIMING ANALYSIS PACKAGE

Rambus Inc. has developed a scalable bandwidth, chip-to-chip memory technology, which delivers up to 1.6 GTransfers/sec. This high-speed, pipeline architecture technology has very stringent timing requirements. Semiconductor vendors and PC board integrators require very specialized measurements to viably characterize their Rambus components and PC boards.

The Tektronix TDSRBS1 Timing Analysis software provides a comprehensive set of Rambus-specific timing parameter measurements. These measure-

ments are specifically designed to meet the measurement needs of today's Rambus technology integrators. One of the most difficult tasks in characterizing Rambus signals is deciphering between the Read and Write cycles. The TDSRBS1 application addresses that by analyzing the data, automatically determining the correct cycle, and performing the appropriate measurement. With the double-pumped data architecture of Rambus® components, another challenge is characterizing both clock edges of data. TDSRBS1 allows analysis of the Odd and Even data fields.

Tektronix®



TDSRBS1 Rambus Timing Analysis Software is a software package from Tektronix that delivers industry standard Rambus technology timing analysis capabilities. It comes on floppy disks, is easily installed in a TDS oscilloscope, and doesn't require any external processing or connections. After installation, the application is accessible from the oscilloscope front panel. Coupling TDSRBS1 with the TDSJIT1 Jitter Analysis and TDSPSM1 Processor Specification SW packages, you have the most comprehensive timing analysis measurements available.

MINIMAL RECOMMENDED CONFIGURATION

The extremely fast rise-times, high frequency clock, read/write deciphering and single-shot requirements of Rambus Data and Clock signals on all four channels requires the following configuration for using TDSRBS1. Instrumentation other than the minimal recommended configuration below will result in measurement results that are not accurate.

- ▶ 1 TDS694C 3 GHz, 10 GS/s oscilloscope w/Option HD
- ▶ 2 P6248 1.7 GHz differential probes
- ▶ 2 P6249 4 GHz active probes
- ▶ 4 PPM203B Articulated arms

TEKTRONIX DIGITAL OSCILLOSCOPES SUPPORTED

TDS500D, TDS600C, TDS700D Series oscilloscopes with Option HD.

ORDERING INFORMATION

TDSRBS1

Rambus Timing Analysis Software for TDS500/600/700 oscilloscopes.

Includes: Software on 3.5-in. Disks, Manual.

NOTE: Requires TDS Run Time Environment v1.2.

TDSRBS1 MINIMAL RECOMMENDED CONFIGURATION

- 1 TDS694C w/Opt. HD.
- 2 P6248 differential probes.
- 2 P6249 active probes.
- 4 PPM203B probe arms.

TDSRBS1 RECOMMENDED ACCESSORIES

TDSJIT1 – Jitter Analysis SW.

TDSPSM1 – Processor Specification Measurement SW.

For further information, contact Tektronix:



Worldwide Web: for the most up-to-date product information visit our web site at: www.tektronix.com/Measurement/scopes/

ASEAN Countries (65) 356-3900; Australia & New Zealand 61 (2) 9888-0100; Austria, Central Eastern Europe, Greece, Turkey, Malta, & Cyprus +43 2236 8092 0; Belgium +32 (2) 715 89 70; Brazil and South America 55 (11) 3741-8360; Canada 1 (800) 661-5625; Denmark +45 (44) 850 700; Finland +358 (9) 4783 400; France & North Africa +33 1 69 86 81 81; Germany + 49 (221) 94 77 400; Hong Kong (852) 2585-6688; India (91) 80-2275577; Italy +39 (2) 25086 501; Japan (Sony/Tektronix Corporation) 81 (3) 3448-3111; Mexico, Central America, & Caribbean 52 (5) 666-6333; The Netherlands +31 23 56 95555; Norway +47 22 07 07 00; People's Republic of China 86 (10) 6235 1230; Republic of Korea 82 (2) 528-5299; South Africa (27 11)651-5222; Spain & Portugal +34 91 372 6000; Sweden +46 8 477 65 00; Switzerland +41 (41) 729 36 40; Taiwan 886 (2) 2722-9622; United Kingdom & Eire +44 (0)1344 392000; USA 1 (800) 426-2200.



From other areas, contact: Tektronix, Inc. Export Sales, P.O. Box 500, M/S 50-255, Beaverton, Oregon 97077-0001, USA 1 (503) 627-6877.

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