

DESCRIPTION

This circuit is intended for use in instruments in the medium to high frequency range. It employs SHF II processing, ECL circuitry, requires one power supply and dissipates about 500 mW. The circuit should be driven differentially. Hysteresis at low frequencies is 40 mV and increases by a factor of 3 at 400 MHz. DC trigger input levels are at about 3.8 volts. A slope select control is included. Input signal conditioning (ie. preamplification and source selection) is done external to the Integrated Circuit. Another Integrated Circuit, the M121 (155-0126-00), has been designed to accomplish this. Other inputs are hold-off and end-sweep (tied together in typical application). A free run input is also provided. Gate out is at an ECL level.

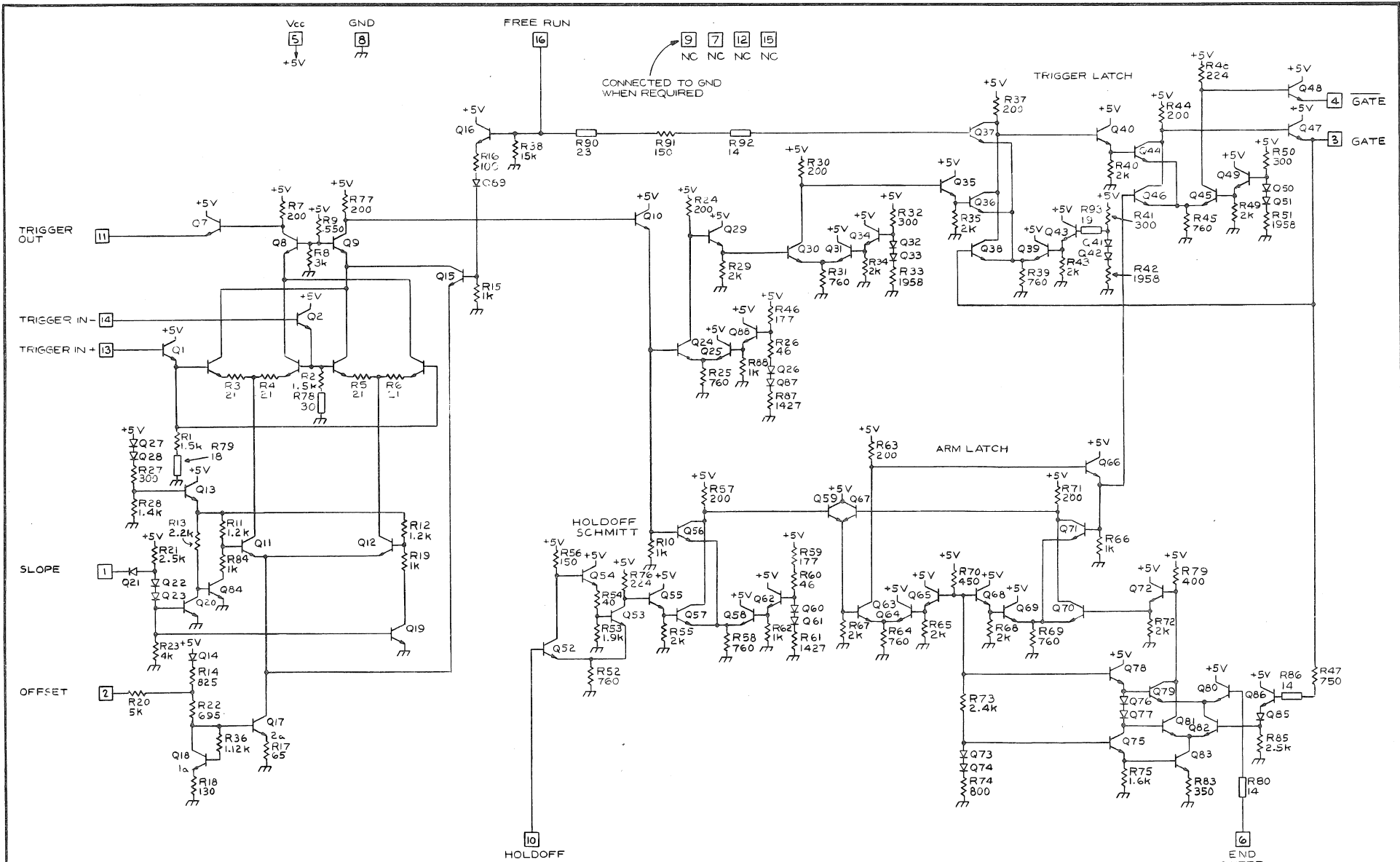
PROCESS SHF II

POWER SUPPLY. V_{CC} +5 Volts

PACKAGE 16 DIP (Kovar-Copper-Kovar)

DESIGNER Gary Vance

INSTRUMENT USAGE	5B31	7B81P
	5B40	7B80
	5B44	7B85
	SC502	7B50A
	5S14N	7S14



9 7 12 15
 NC NC NC NC
 CONNECTED TO GND
 WHEN REQUIRED

NOTE:
 1.  INDICATES CROSS-UNDER
 RESISTOR.

ENGR	<i>Walt Demuth</i>	9-26-75	PROCESS	SH II
DWN BY	J. Langley	1-17-75	PACKAGE	16 PIN DIP
CHK BY	C. WESTON	7-30-76	DIE SIZE	65 mil x 65 mil
TYPE	MONOLITHIC	TRIGGER		MI20B
INTEGRATED CIRCUIT ENG/MFGS TEKTRONIX, INC. BEAVERTON, OREGON, U.S.A.		PART NO. 155-0109-01		