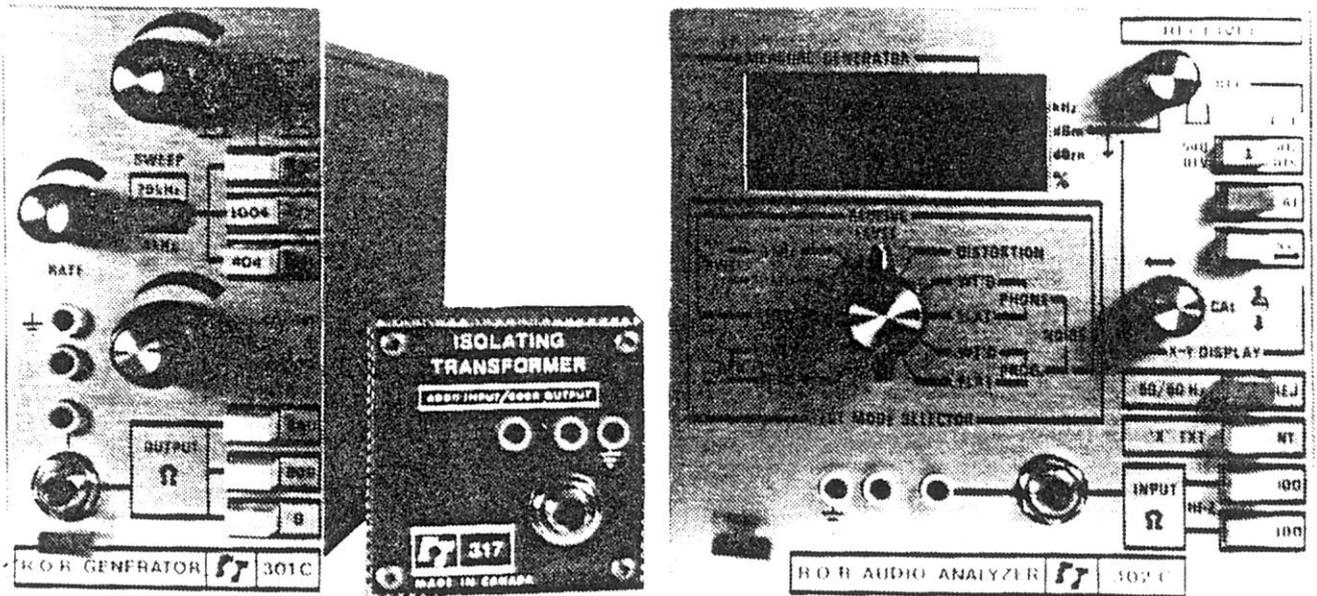


# Consultronics

## R-O-R Type 300 Audio Analyser System

### Service Manual



1.2.1 model 301 GENERATOR  
PLUG-IN

1.2.2 model 302 RECEIVER  
PLUG-IN

1.2.3 model 317 ISOLATING  
TRANSFORMER

Fig. 1.2 BASIC PLUG-IN UNITS for the R-O-R 300 SYSTEM

# OPERATING FUNCTIONS OF THE BASIC 300 SYSTEM

The position of the TEST MODE SELECTOR switch determines what the instrument measures and indicates.

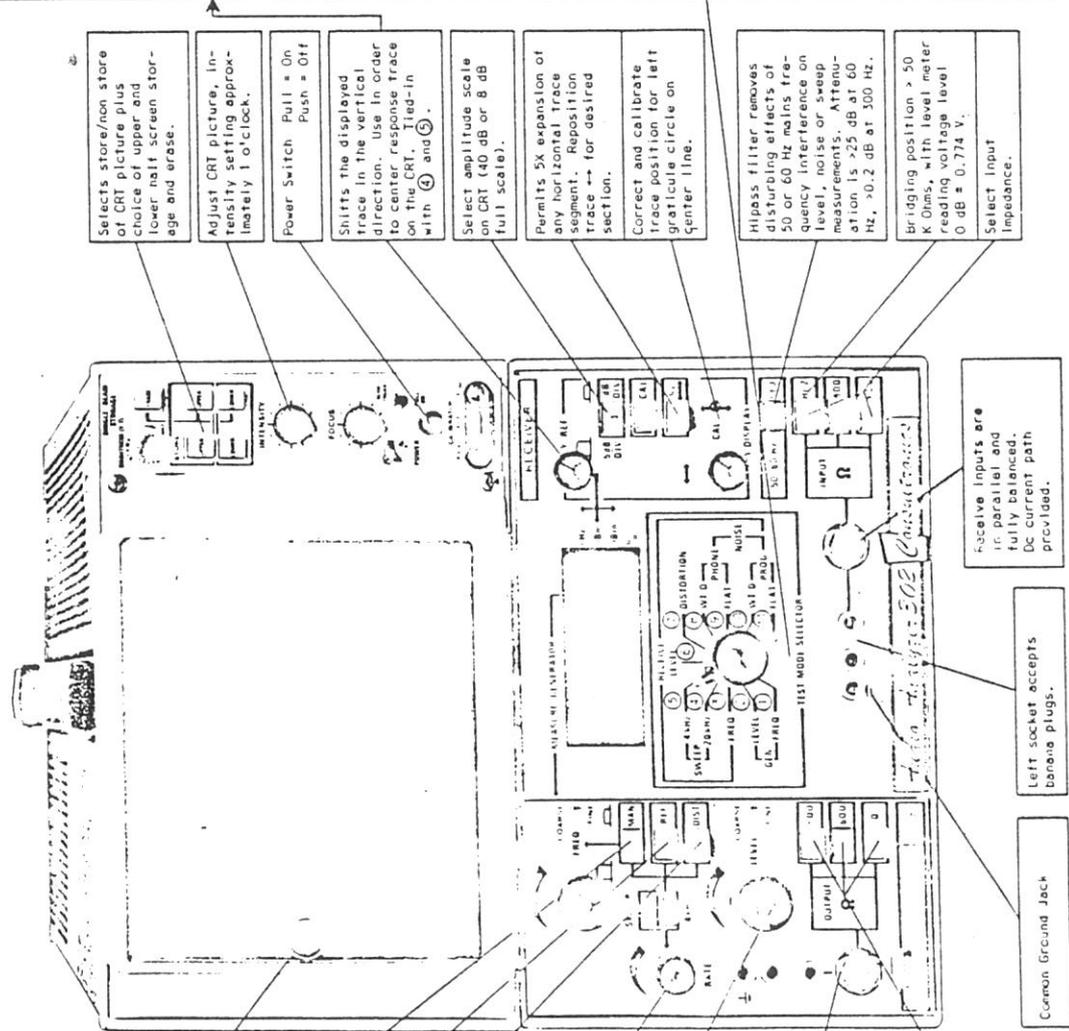
**POSITION FUNCTION**

- Digital readout shows send frequency (kHz), no trace on CRT.
- Digital readout shows send level (dbm), no trace on CRT.
- Digital readout shows frequency of received tone (kHz), trace display on 20 kHz log scale.
- Trace display on 20 kHz log frequency scale, digital readout showing level of center line of CRT (dbm).
- Trace display on 4 kHz linear frequency scale, digital readout showing level of center line on CRT (dbm).
- Digital readout of received tone in dbm, trace display on 20 kHz log scale.
- Digital readout of total distortion of one fixed test tone (404 Hz) in %.
- Digital display of noise weighted with psophometric filter (CCITT White Book V, Rec. P53, 1968)
- Digital display of noise (dbm) weighted with 3.5 kHz low pass filter.
- Digital display of noise (dbm) weighted with Radio Program channel filter (new) CCITT Com-IV - No.76.
- Digital display of noise (dbm) weighted with 20 kHz bandpass, IEC Publications 268-1 and 225.

300 System Audio Analyzers sold in North America contain the following noise filters:

- Digital display of noise (dbm) weighted with C-message filter.
- Digital display of noise (dbm) weighted with 3 kHz flat filter.
- Digital display of noise (dbm) weighted with program channel filter.
- Digital display of noise (dbm) weighted with 15 kHz flat filter.

(All filters in positions 8 to 11 comply with the Bell System Practices.)



**CALIBRATION POSITION**  
Press red CAL button and correct beam position.

Press MAN pushbutton for manual tuning of frequency. Read output frequency in position ① of Test Mode Selector.

Pushing the REF button will tune to 804 or 1004 Hz.

For distortion measurements use DIST pushbutton generator will send spectral high purity test tone.

Release all trace push-buttons for automatic sweep of generator.

Select sweep rate from 2 sec/sweep to 100 sec/sweep.

Adjusts output level of generator between +16 and -40 dbm. Read output level in position ② of Test Mode Selector.

Transmit outputs are in parallel, balanced with grounded center. If one side is grounded, output level is 3 db lower the digital meter indication. Do not use both outputs at the same time (about termination).

Select output impedance. 0 Ohm position permits the use of series resistor in series for output impedances other than 0 Ohm.

Common Ground Jack

Left socket accepts banana plugs.

Receive inputs are in parallel and fully balanced. DC current path provided.

Selects store/non store of CRT picture plus choice of upper and lower half screen storage and erase.

Adjusts CRT picture, intensity setting approximately 1 o'clock.

Power Switch Pull = On Push = Off

Shifts the displayed trace in the vertical direction. Use in order to center response trace on the CRT. Tied-in with ④ and ⑤.

Select amplitude scale on CRT (40 db or 8 db full scale).

Permits 5X expansion of any horizontal trace segment. Reposition trace ← for desired section.

Correct and calibrate trace position for left graticule circle on center line.

Hi-pass filter removes disturbing effects of 50 or 60 Hz mains frequency interference on level, noise or sweep measurements. Attenuation is >25 db at 60 Hz, >0.2 db at 300 Hz.

Bridging position > 50 K Ohms, with level meter reading voltage level 0 db = 0.774 V.

Select input Impedance.

600R

1004

TECHNICAL SPECIFICATIONS & PERFORMANCE DATA  
(Subject to Change Without Notice)

0dB - 2Vpp  
3dB - 2.80Vpp  
10dB - 5.5Vpp  
15dB - 12Vpp  
- 3dB - 1.5  
- 10dB - .64  
- 20dB - .190  
- 30dB - 64mV  
- 40dB - 20mV

1.0 MODEL 301 AUDIO SWEEP GENERATOR PLUG-IN

Frequency range	40 Hz to 20 kHz min.
Frequency control	Manual or internal sweep
Preset frequencies	404 Hz (low distortion) & 1004 Hz
or	404 Hz ( " " ) & 804 Hz
Preset frequency tolerance at 25 °C	± 5 Hz
at 0 to 50° C	±10 Hz
long term drift	±10 Hz
Sweep rate	Adjustable from approx. 0.5 sec to 25 sec/decade.
Sweep range 1	40 Hz to 4 kHz min logarithmic
2	40 Hz to 20 kHz min "
Manual Frequency adjust	Coarse and fine control (fine control has approx. 2:1 range)
Output level	-40 dBm to +10 dBm minimum range. (+18 dBm available with increased distortion)
Output level adjust	Coarse and fine control (fine control has approx. 6 dB range)
Output waveform	Sinusoidal
Output distortion from -20 to +10 dBm:	at 25° C      at 0 to 50° C
40 Hz to 100 Hz	1% max      2% max
100 Hz to 1 kHz	0.3% max      0.6% max
1 kHz to 20 kHz	1% max      2% max
404 Hz preset	0.1% max      0.1% max
Level change from manual to preset	±0.1 dB max
Frequency response 0 to 50° C ref 1 k Hz:	
-30 to +10 dBm, 100 Hz to 10 kHz	±0.1 dB
" " , 40 Hz to 20 kHz	±0.25 dB
Output impedance at 1 kHz	600 ohm, balanced center gnd. Tolerance ± 1% 150 ohm, balanced center gnd. Tolerance ± 1% 0 ohm, balanced center gnd. Tolerance + 3 ohm.
Output connectors	WECO 310 & Siemens 3 pin

The model 301 Generator is designed to be plugged into a Tektronix 5111 oscilloscope mainframe.

TECHNICAL SPECIFICATIONS & PERFORMANCE DATA

(Subject to Change Without Notice)

1.0 MODEL 302 AUDIO ANALYZER PLUG-IN

Input Impedance ref. 1 kHz.	1.	150 ohm	$\pm 1\%$ balanced.
	2.	600 "	" "
	3.	100 kOhm	" "
Input holding current capability		70 mA DC max	
Input overload protection		200 V 60 Hz AC	
		300 V DC	
		600 V 10 micro-sec. max	
Input return loss		Over 30 dB to 6 kHz	
Input connectors		Weco 310 & Siemens 3 pin	
Audio monitor gain		+ 20 dB	
Audio monitor output limit		+ 5 dBm	
High-pass filter, switchable, Attenuation at 300 Hz		less than 0.5 dB	
60 Hz		more than 25 dB	

DIGITAL READOUT SPECIFICATIONS

Level measurements

Frequency range	40 Hz to 20 kHz	
Level measuring range	-50 dBm to + 18 dBm minimum	
Level readout resolution	0.1 dB	
Level reading accuracy at 1 kHz (abs)	at 25° C	0 to 50° C
+ 15 to + 18 dBm	$\pm 0.5$ dB	$\pm 0.6$ dB
+ 10 to + 15 "	$\pm 0.25$ "	$\pm 0.3$ "
- 20 to + 10 "	$\pm 0.1$ "	$\pm 0.15$ "
-35 to -20 "	$\pm 0.15$ "	$\pm 0.2$ "
-42 to -35 "	$\pm 0.2$ "	$\pm 0.5$ "
-50 to -42 "	$\pm 0.3$ "	$\pm 1$ +

Level reading flatness

Ref. 1 kHz, 25° C	40 Hz to 100 Hz	to	10 kHz	to	20 kHz
+15 to +18 dBm	-0.2 dB	$\pm 0.1$ dB		-1 dB	
+10 to +15 "	" "	$\pm 0.05$ "		$\pm 0.2$ "	
-20 to +10 "	" "	" "		$\pm 0.1$ "	
-35 to -20 "	" "	$\pm 0.1$ "		$\pm 0.15$ "	
-42 to -35 "	" "	" "		$\pm 0.3$ "	
-50 to -42 "	-0.5 "	$\pm 0.2$ "		$\pm 1.$ "	

FREQUENCY MEASUREMENT (Gen or Receive)

Frequency Range	10 Hz to 20.00 kHz min.
Level range (ext. tracking)	-50 to +18 dBm min.
Resolution	10 Hz
Accuracy	$\pm$ 5 Hz
Level range (int. tracking)	-30 dBm to +18 dBm min.

CRT DISPLAY SPECIFICATIONS (using Tektronix 5111 oscilloscope)

HORIZONTAL

Range 1.	0 to 4 kHz, Linear
2.	40 Hz to 20 kHz, Logarithmic
Display accuracy at 25° C	$\pm$ 1 mm 100 Hz to 10 kHz
0 to 50° C	$\pm$ 2 mm " " "
Expansion range	approx. 5:1
"X" Shift Control range	40 Hz to 22 kHz
Display calibration	Single pushbutton checks X & Y axis position and scaling

VERTICAL

Sensitivity	opt:	1 & 5 dB/division or 0.5 & 5 dB/division
"Y" Reference control range		approx. -40 to +18 dBm
Resolution		0.05 dB
Display accuracy		$\pm$ 3% of deflection
Display Calibration:		Single push-button checks X & Y axis position and scaling.

The model 302 is designed to plug into a Tektronix 5111 mainframe.

## C I R C U I T D E S C R I P T I O N

### MODEL 301 GENERATOR

#### Generator Board

- 8.0 The generator consists of a voltage controlled oscillator (VCO) which produces a saw-tooth and a square waveform, a sine converter, a log sweep generator, a bandpass filter and a power output stage.
- 1) The VCO comprises IC's 5, 6, 7, 8 and part of IC 9 as well as Q1. When IC 5 output is positive, C 4 is charging towards the negative supply. When this charge reaches the same voltage as the output of IC 8 at the junction of R 22 and D 7 the comparator (IC 7) changes its output polarity. This reverses the voltage on the output of IC 5 reversing the direction of the saw-tooth signal on C 4 and changing the reference voltage on IC 8 until the charge once again reaches this level and then the cycle begins again. The VCO drive voltage at the output of IC 4 determines the speed of charge hence the frequency. This drive voltage comes from fixed or variable sources according to the selection on the function switch.
- 8.1 The sine converter processes the saw-tooth wave by performing the mathematical equivalent of the series expansion formula for a sine wave. IC 11, 12, 14, 15 and 16 convert the saw-tooth into a series of half sine waves. IC 17 and the second half of IC 9 invert every second half cycle to produce a proper sine wave.
- 8.2 The log sweep generator comprising IC 1, 2 and 3 is a self oscillating circuit, whose linear change of charge is converted by diode D 2 into a log -1 rate of change at the output of IC 2 which drives the VCO in the sweep position.
- 8.3 The bandpass filter (IC 18, 19 and 20) is tuned to 400 Hz. It provides a highly purified sinewave output for harmonic distortion measurements.

8.4

The power output stage of IC 21, 22, 23, 24 and 25 perform conversion of the single ended input signal to a balanced output level corrected for the selected impedance.

## MODEL 302 AUDIO ANALYZER

### 8.0 "X" BOARD

The terminated signal enters the receiver through a differential input stage consisting of IC 1, 2 and 3. From here the signal is split to further amplitude (V) processing by IC 6 and "X" decoding. This decoding is performed by converting the sinusoidal signal of varying amplitude to a square-wave signal of fixed amplitude by IC 4 and 5. This drives a one-shot comprising Q 1 and IC 13 whose output is integrated by IC 8 or IC 9 to a DC level proportional to input frequency. IC 10, 11 and 14 convert this linear output to a logarithmic drive which drives the CRT "X" deflection through IC 12. The presence or absence of a +15V level on LIN DRIVE determines if IC 12 follows the linear or logarithmic drive.

### 8.1 NOTCH BOARD

The notch board consists of a bank-reject filter and circuitry to self-tune this filter to any frequency between 360 to 440 Hz and adjust the notch depth for maximum rejection. IC's 1, 2 and 3 form an inverting bandpass filter whose output cancels the out of phase fundamental frequency of the input tone in IC 5, leaving only the harmonics and noise. IC 4 and Q 2 integrate tuning errors to drive the frequency adjusting photo coupler IC 9. IC 6 and Q 1 integrate amplitude errors to drive the gain adjusting photo coupler IC 8. IC 7 is a semi-conducting 4 pole switch whose function is to select the proper signal for further processing.

### 8.2 FILTER BOARD

The filter board consists of a 40 dB gain stage, four active filters and a solid state 4 pole switch to select the desired output. IC 1 and 2 provide the 40 dB gain as well as the positive DC offset required by IC 12. The 3 kHz flat filter uses IC 3 and is shared by the "C" message filter comprising IC 3, 6, 8 and 10. The 15 kHz flat filter uses IC 5. IC 4, 7, 9 and 11 form the program channel filter.

8.3

### "Y" BOARD

The "Y" board contains circuitry to rectify and filter the "Y" signal, take the logarithm of the result, calculate percentage distortion and electronically switch the proper output for driving the digital display. The "Y" board also contains the power supply regulators for all the boards in the Model 301 Receiver. IC 1 amplifies the AC signal to improve rectifier response. IC 2 and 3 form a full wave rectifier circuit (average detector). After filtering the signal is applied to the log converter IC 4 and 14e. IC 7 and 14d supplies the reference 0 dB level. IC 5 amplifies the resulting level to IV/10 dB. IC 6 adds or subtracts the "Y" reference level from the dB signal level enabling the user to center the trace on the oscilloscope screen. When the trace is centered the "Y" reference voltage is equal to the signal level. This simplifies measuring of harmonic distortion as the signal is first "zeroed" with "Y" reference and then rejected by the notch filter. The ratio of the remaining harmonics to the stable "V" reference voltage is calculated by IC 8, 9 and 14c at IV/10%. IC 10, 11, 12 and 13 perform electronic switching. Only one of IC 10, 11 or 12 is allowed to amplify its input at one time. The others receive a positive input bias current reverse biasing their respective output coupling diodes. IC 10, 11 and 12 needed a positive offset and a voltage division by 2 to perform their function. IC 13 cancels the offset and re-amplifies the signal to the original level. Q 1 and D 20 regulate the positive supply to +14.5V. Q 2 and D 21 regulate the negative supply to -14.5V. D 19 is a temperature compensated zener diode which together with IC 15 and 16 produce a precision +1.00V and -1.00V supply.

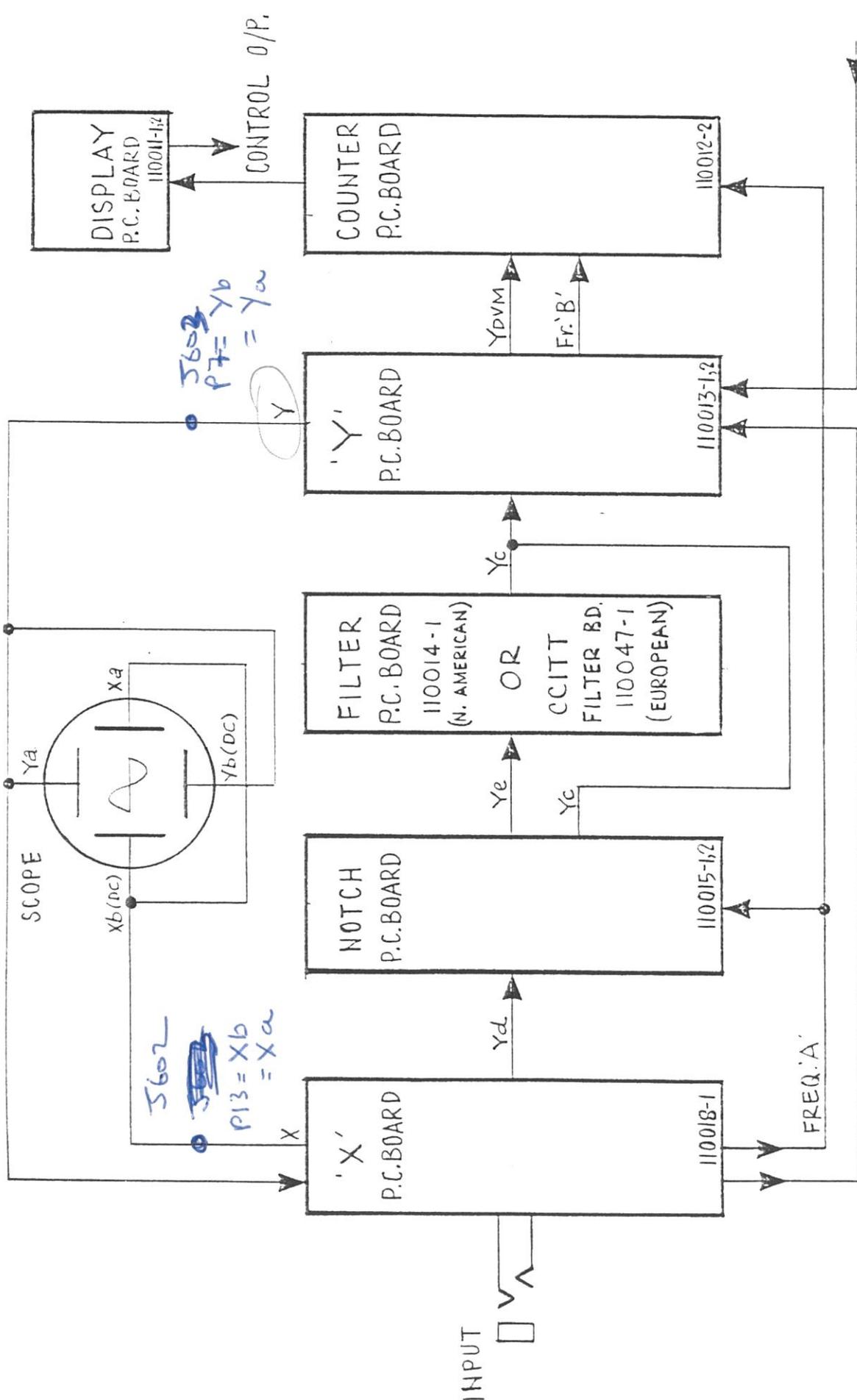
8.4

### THE COUNTER BOARD

The counter board performs the function of analog to digital conversion, frequency counting and display driving.

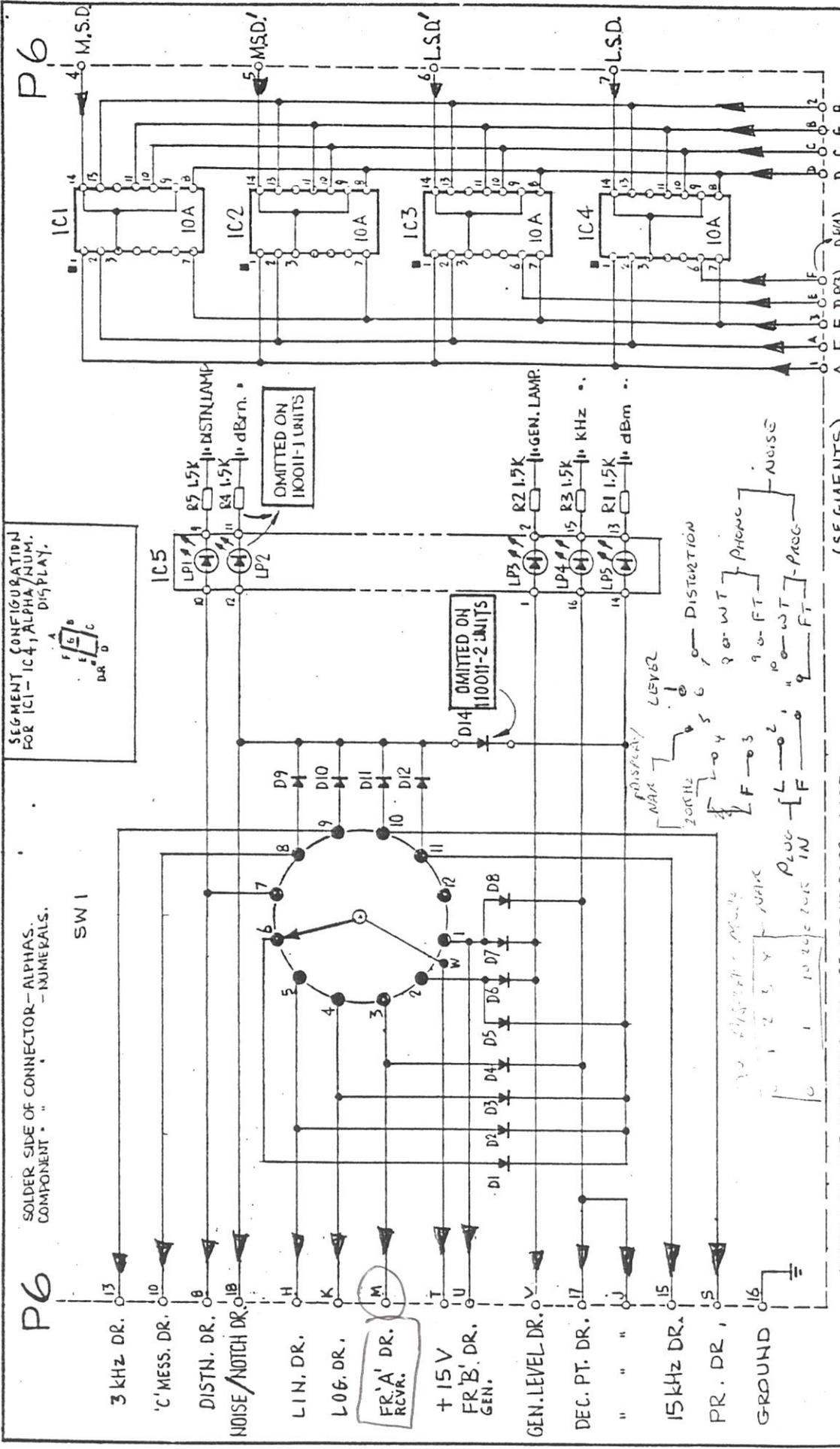
IC 10 and 11 convert the incoming DC signal to its absolute value. If the input is positive Q 5 turns on and through IC's 8 d, 3 d and 5 prevent the display of a negative sign. The negative sign drive is taken from

the most significant digit (MSD) drive. IC 12 and IC 13 converts the DC input into a pulse train whose frequency is proportional to the input voltage. This pulse train or one of the frequency inputs can be selected by IC 3 and IC 9 to go to IC 4. IC 4 is a counter, seven segment display driver/scanner. The scanning frequency is generated with the aid of C 5. The counting interval of IC 4 is controlled by IC 1 which generates a 1.0 Hz. square wave from the division of a 1 MHz. crystal oscillator on the same chip. IC 2 further divides the 10 Hz. to 5 Hz., driving two one shots, one from the leading and one from the trailing edge of the square wave. The seven segment and digit drive outputs are amplified by IC's 5, 6 and 7 which drive the display digits from a +5V source.



GENERATOR LEVEL & FREQ. 'B'  
FROM MODEL #301

CONSULTRONICS LTD. TORONTO, CANADA	BLOCK SCHEMATIC AUDIO ANALYZER (MODEL #302)	DRN BY	LR	SCALE	---
		CH'D BY	J.P.	DWG. No.	100021-1B
No. REC'D		DATE		CLASS	
		MAR 7, 1974			



NOTES: 1. D1 - D15 ARE IN 914; LPI - LP5 ARE HP 5082 - 4487;  
 IC1 - IC4 ARE DATA-LIT 10A OR EQUIV.  
 2. ALL RESISTANCE VALUES IN OHM.  
 3. ALL RESISTORS ARE 1/4W, 5%.

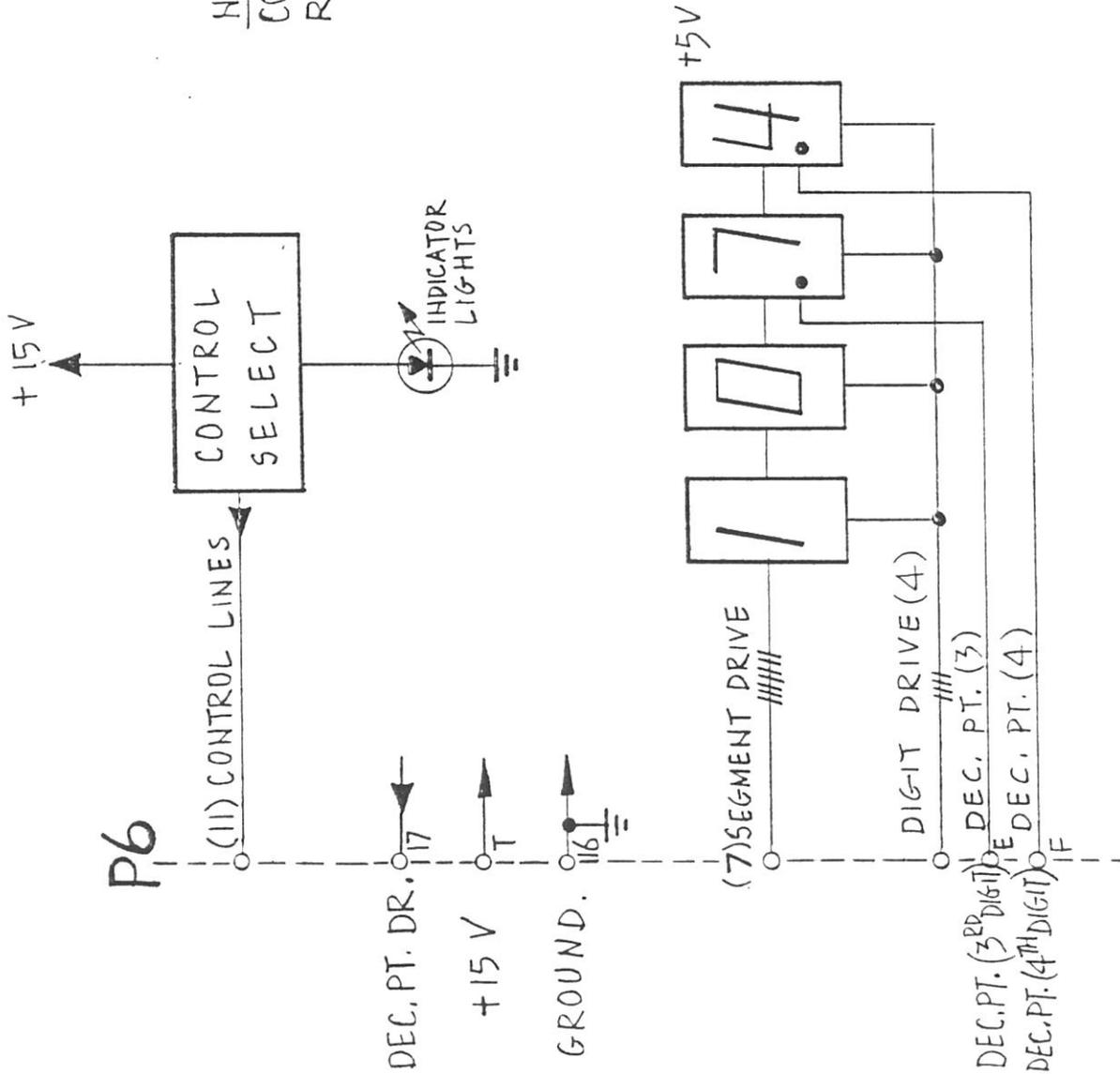
CONSULTRONICS LTD.  
 TORONTO, CANADA

DISPLAY P.C.B. - SCHEMATIC  
 MODEL # 302

MATERIAL 400020 - F.P.C.B. IN. MOD. CLASS E/AFK77 DATE MAR 13, 1974

SCALE 1:1  
 DRAWN BY J.R.  
 CHECKED BY J.R.

110011-18-29



NOTE:- FOR PIN ALLOCATION OF ALL CONTROL LINES, SEGMENT & DIGIT DRIVES REFER TO SCHEMATIC, DWG. NO 110011-1S

CONSULTRONICS LTD. TORONTO, CANADA,	DISPLAY P.C.B. - BLOCK SCHEMATIC		DR'N BY	LR	SCALE	-
	MODEL #302	CLASS	CH'D BY	J.R.	DWG. No.	110011-1B
MAT'L	No. REQ'D	DATE JAN 31, 1974				

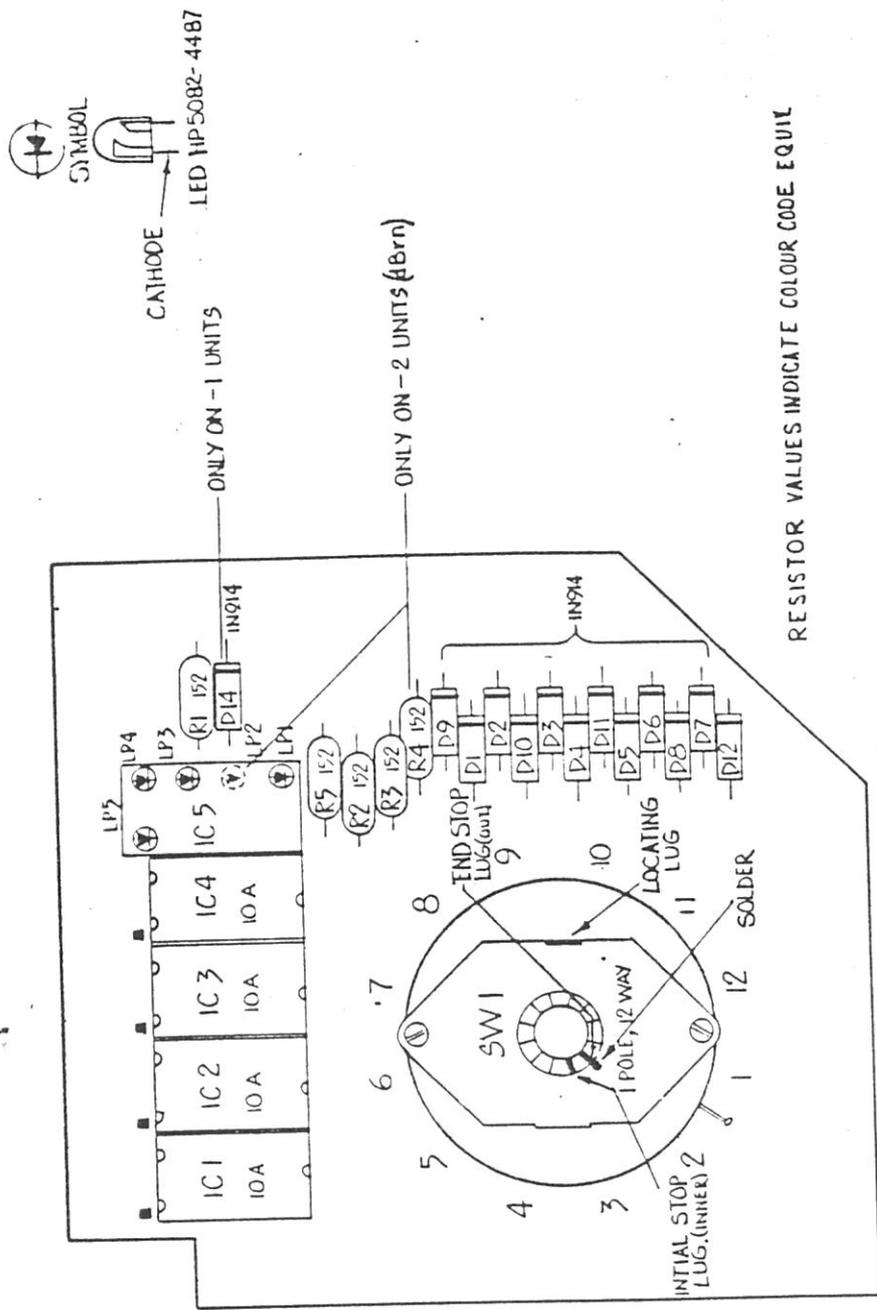
MOUNTING SURFACE  
 DISPLAYS FLUSH WITH MOUNTING SURFACE

4-40 NUTS

SIDE VIEW

NOTE :-

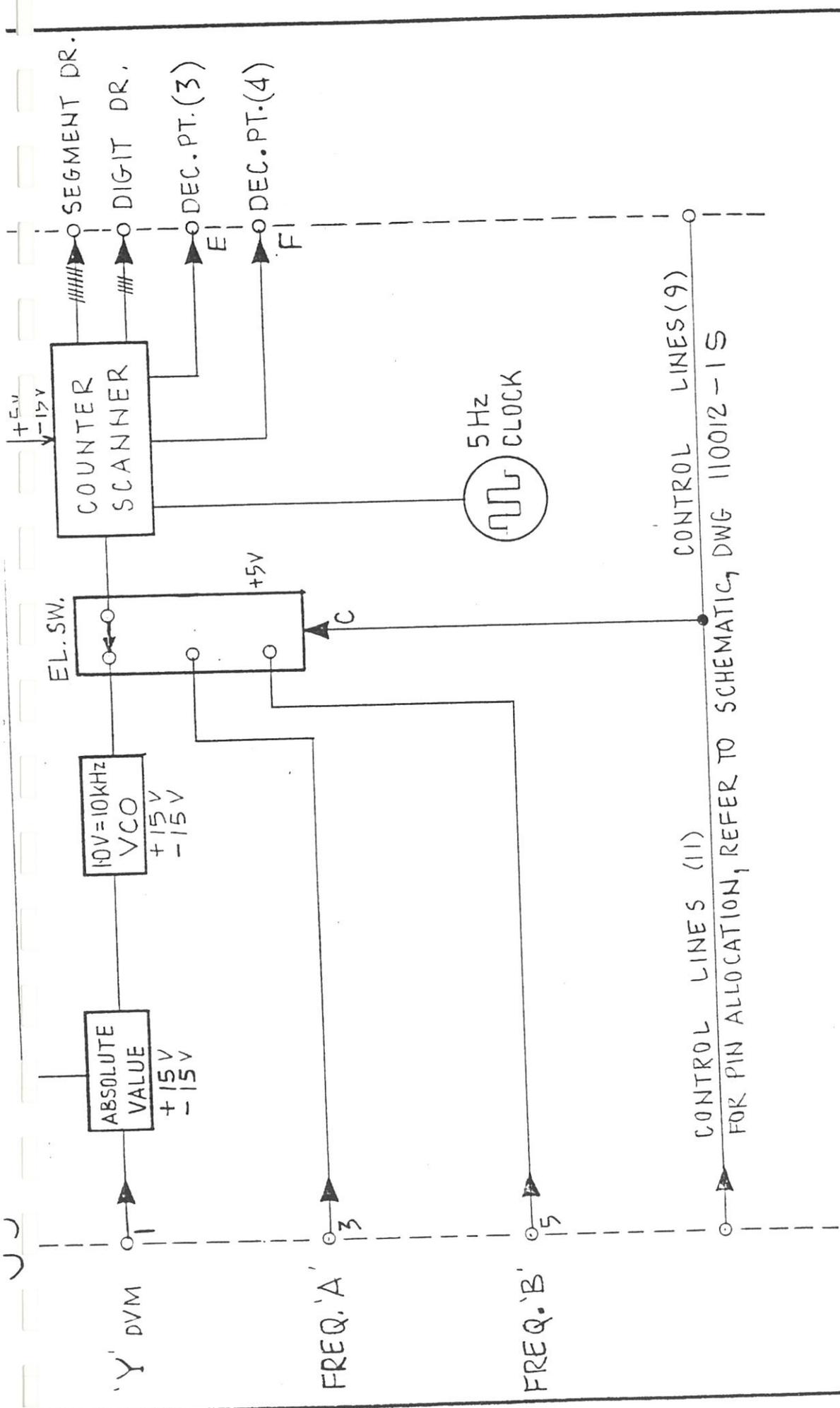
- 1. SOLDER OUTER STOP LUG TO SWITCH PLATE TO RETAIN POSITION.



RESISTOR VALUES INDICATE COLOUR CODE EQUIV

CONSULTRONICS LTD. TORONTO, CANADA.		DISPLAY P.C.B. SUB ASSY. MODEL # 302		SCALE 2:1
MATL P.C.B. 400020-1	IN. RECD	CLASS E/APR 77	DATE APRIL 17, 1975	DRAWN BY RR
				NO. 11-82





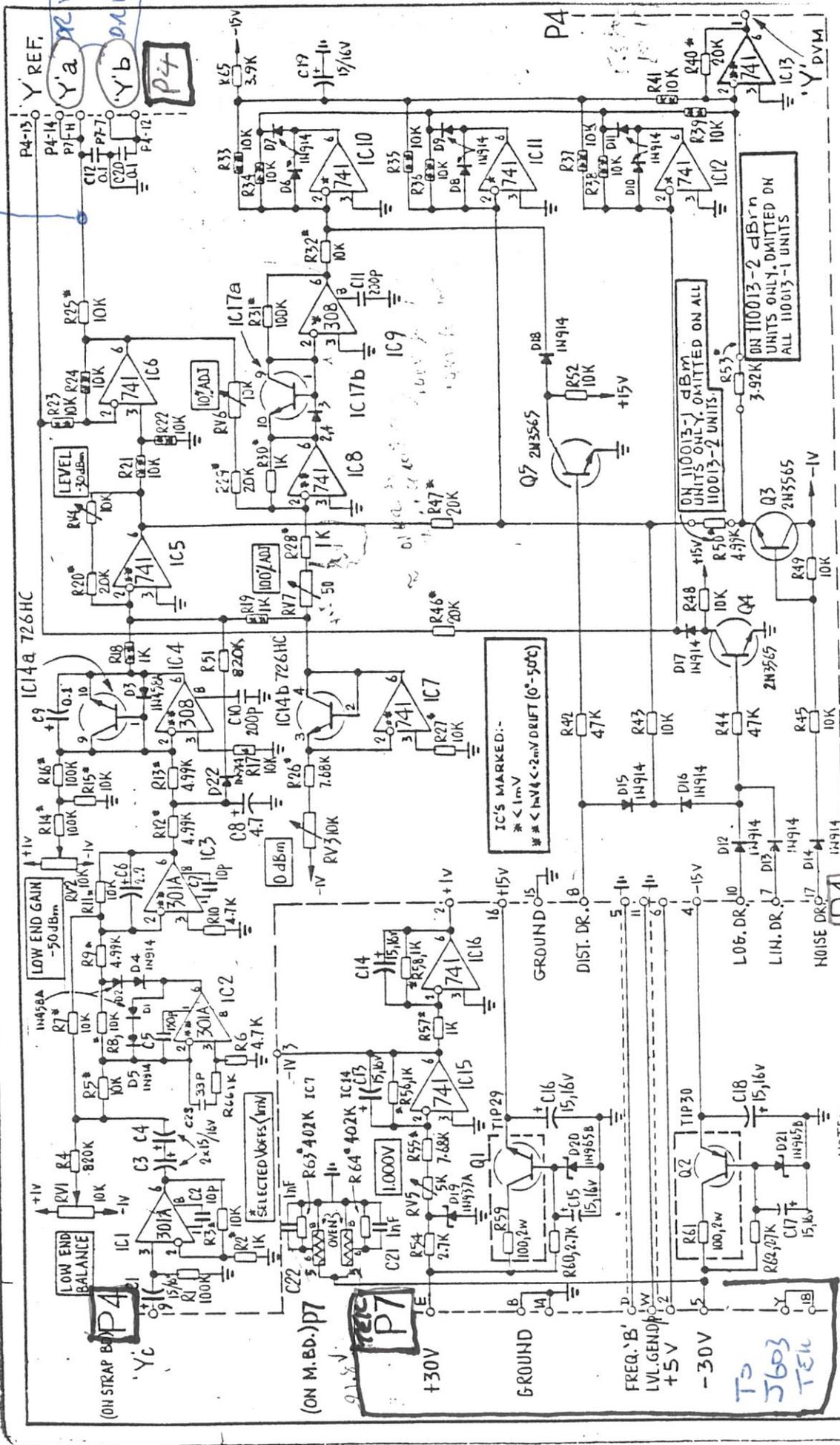
CONTROL LINES (11)  
 FOR PIN ALLOCATION, REFER TO SCHEMATIC, DWG 110012-1S  
 CONTROL LINES (9)

CONSULTRONICS LTD. TORONTO, CANADA.	MATERIAL	MODEL # 302-1306	COUNTER P.C.B. - BLOCK SCHEMATIC		DRN BY	SCALE
			No. REQ'D	CLASS	J.R.	-
DATE JAN 31, 1974			DWG. No.		110012-1B	



CAL

ms to scope



NOTE:-  
 1-UNLESS OTHERWISE SPECIFIED  
 ALL CAPACITANCES IN MICROFARAD.  
 2-ALL RESISTANCES IN OHM.  
 3-RESISTORS MARKED \* ARE 1%  
 ALL OTHERS ARE 5%

RESISTORS MARKED \* ARE ±1%  
 HIGHEST COMPONENT

R	C	D	Q	RV	IC	17
66	23	22	5	7	17	

INSULTRONICS LTD.  
 TORONTO, CANADA

DATE 400023-1 P.C.B. (REV. 02/77)  
 DRAWN BY J.C.  
 CHECKED BY L.R.  
 SCALE

DATE FEB 22, 1974  
 DRAWING NO. 110013-1S2S

(ON STRAP BOARD) P4

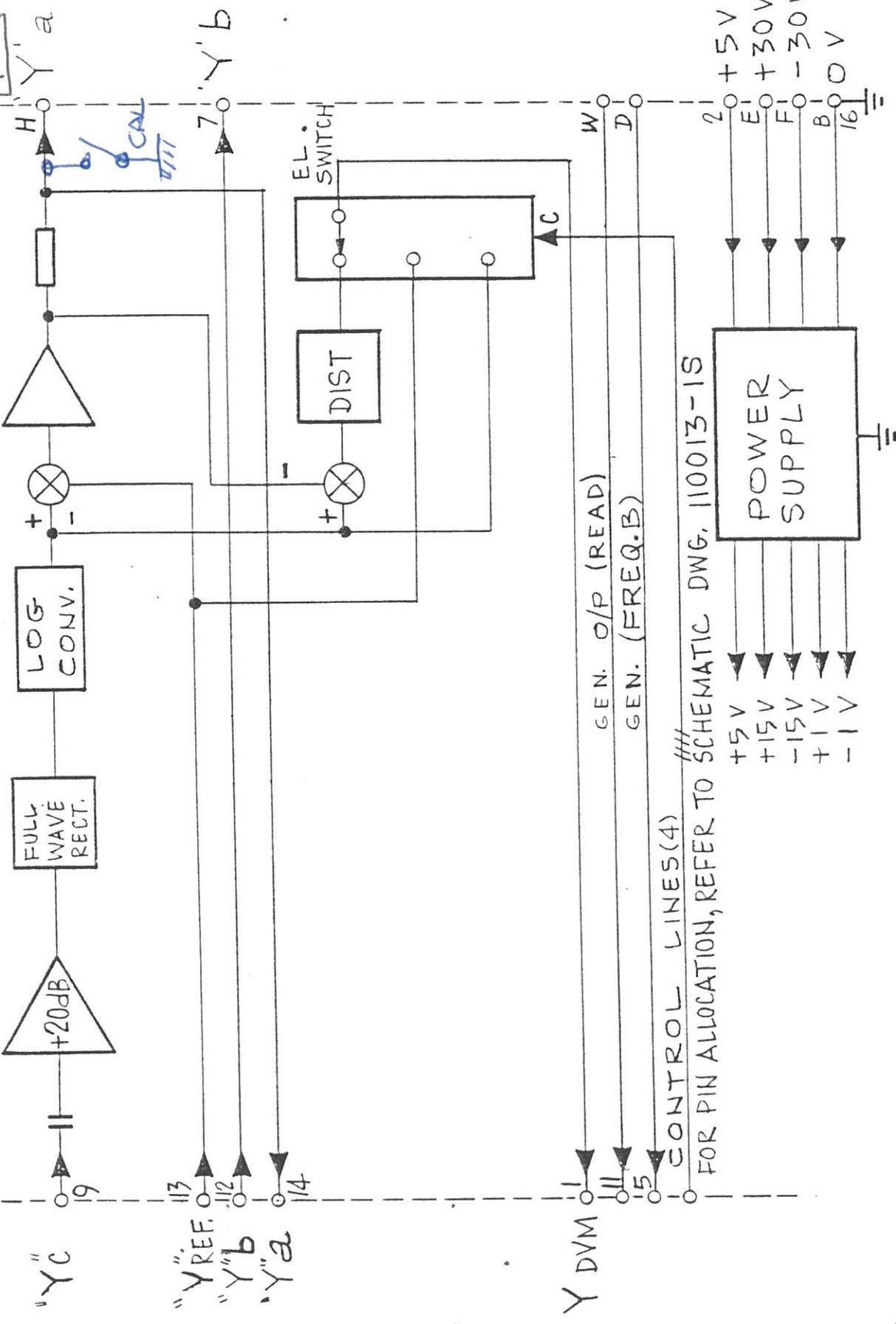
(ON M.B.D.) P7

P4

T0  
 J603  
 T51C

P4

P7



FOR PIN ALLOCATION, REFER TO SCHEMATIC DWG. 110013-1S

CONSULTRONICS LTD.  
TORONTO, CANADA

"Y" P.C.B. - BLOCK SCHEMATIC  
MODEL # 302

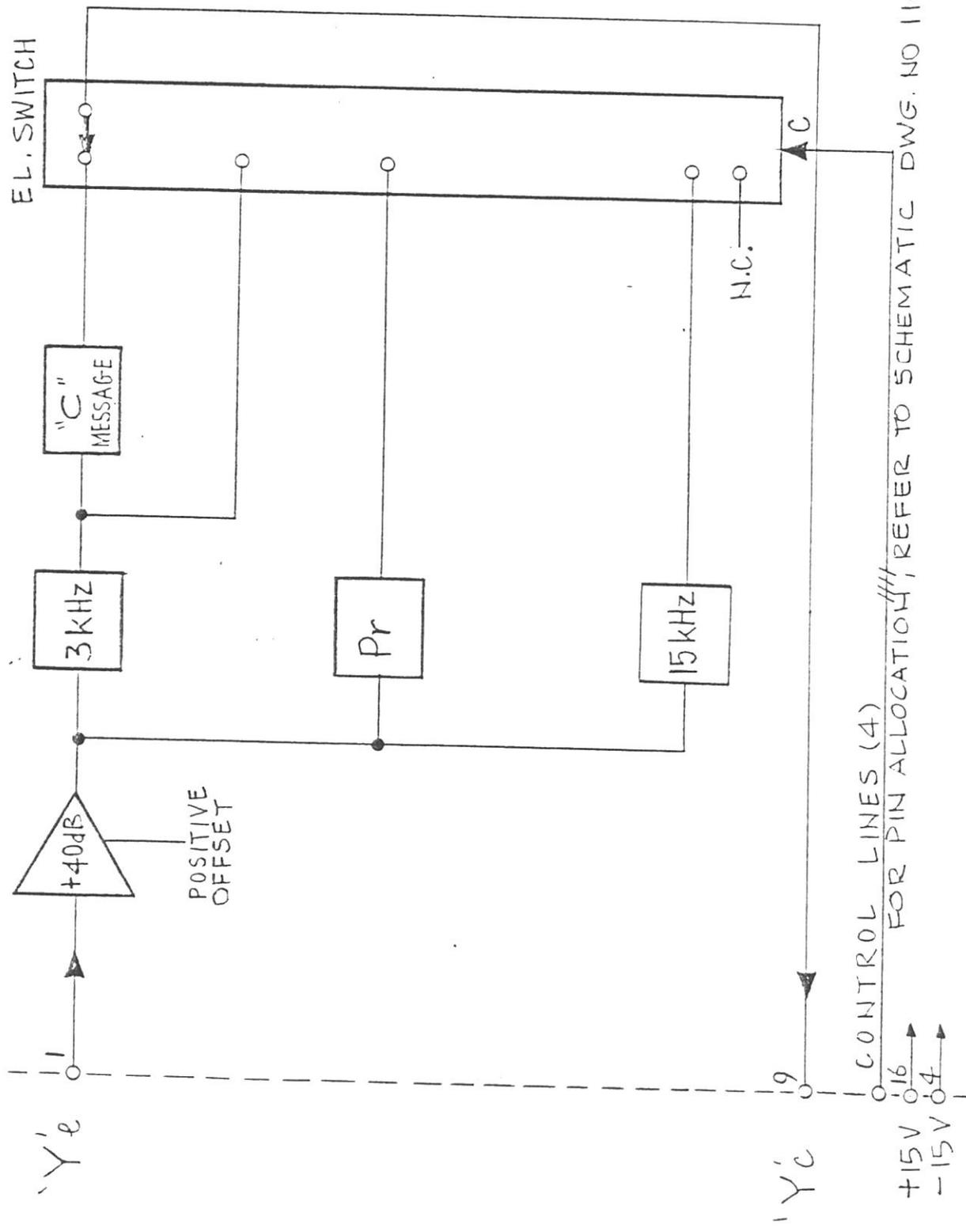
DR'N BY L.R.  
CH'D BY J.R.  
DATE JAN 31, 1974

SCALE  
DWG. No. 110013-1B





H3



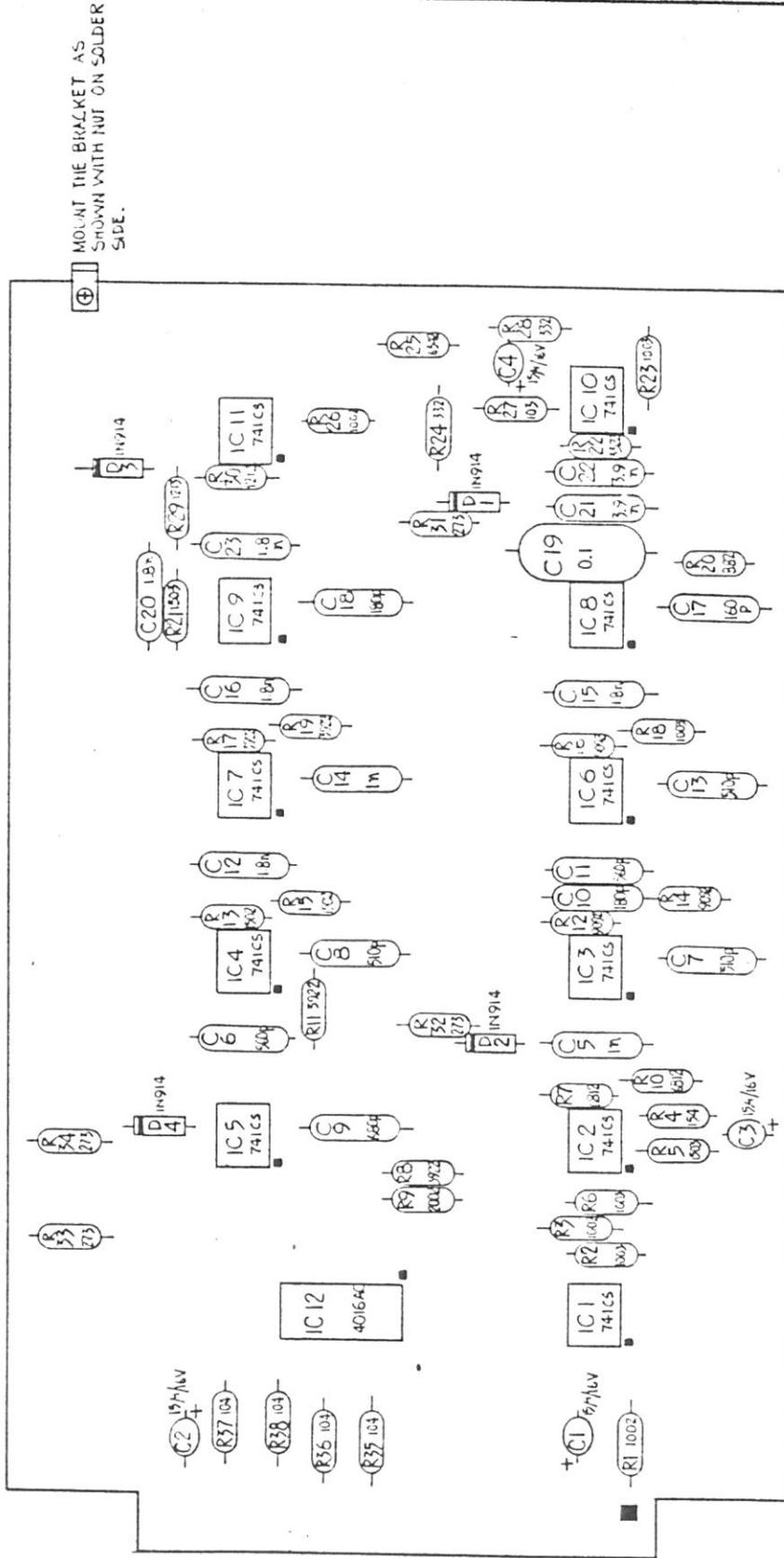
CONTROL LINES (4)  
FOR PIN ALLOCATION, REFER TO SCHEMATIC DWG. NO 110014-15

CONSULTRONICS LTD.  
TORONTO, CANADA

FILTER P.C.B. - BLOCK SCHEMATIC  
MODEL # 302

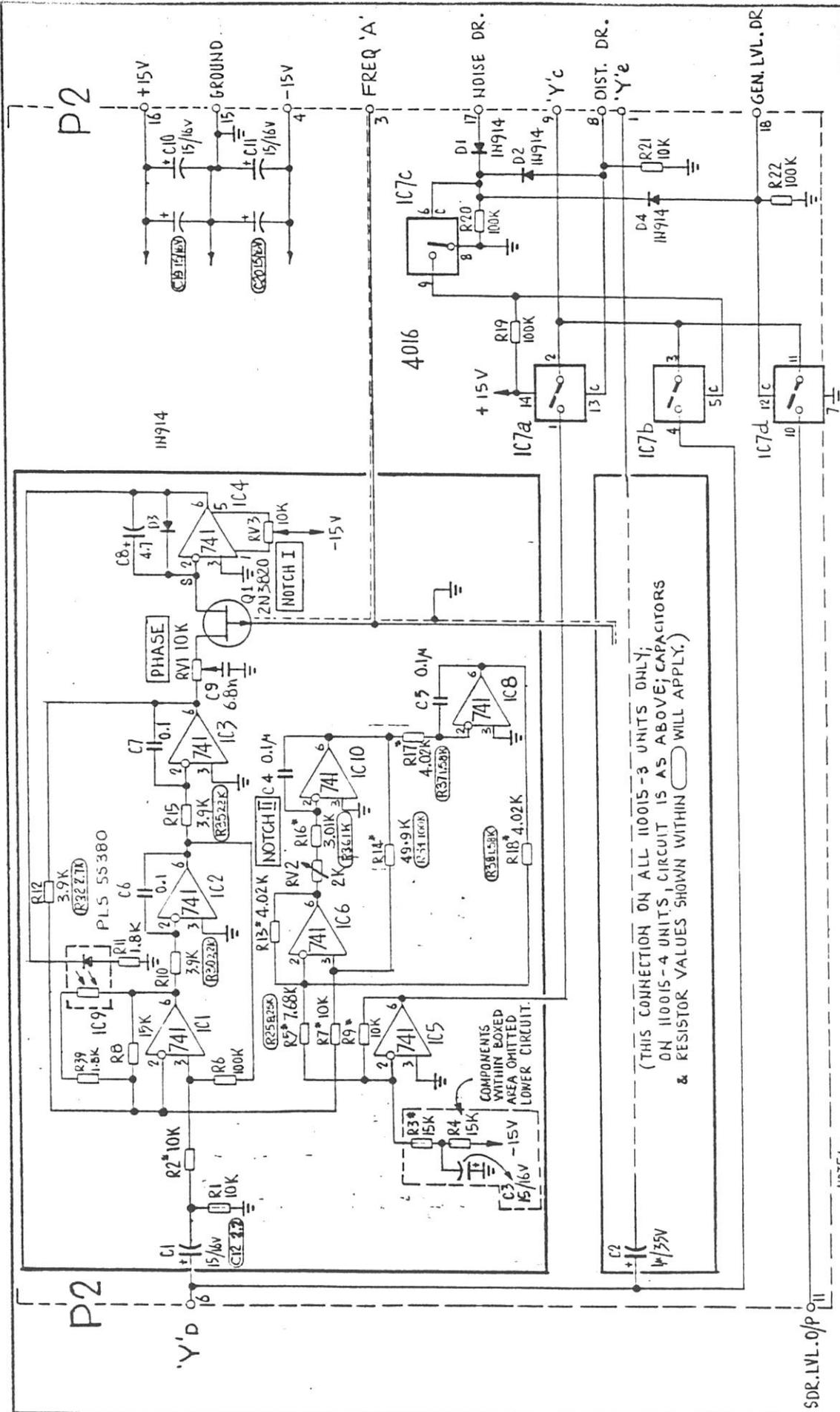
DR'N BY	Y.R.	SCALE	-
CH'D BY	J.R.	DWG. No.	110014-1B
DATE	JAN 31, 1974		

FRONT OF UNIT



RESISTOR VALUES INDICATE COLOUR CODE EQUIV.

INSULTRONICS LTD. TORONTO, CANADA.	FILTER BOARD P.C.B. ASSY.		SCALE 2:1
	MODEL # 302	DATE APR. 21. 1975	DRN BY RR
REV. 400025-1	REV. 15	REV. 15	110014-1



SDR. LVL. 0/p 11

HIGHEST COMPONENT	R	C	D	Q	RV	IC
39	13	4	1	3	10	

NOTE: -  
 1. UNLESS OTHERWISE SPECIFIED ALL CAPACITANCES IN MICROFARAD, ALL RESISTANCES IN OHM.  
 2. IC'S 1 TO 6, PIN 4 TO -15V; PIN 7 TO +15V.  
 3. RESISTORS MARKED \* ARE 1/2% ALL OTHERS 5%.

CONSULTRONICS LTD., TORONTO, CANADA

MODEL # 302

DATE 10 JAN 1978

MATL 400027-1 P.C.B. IN. NEED

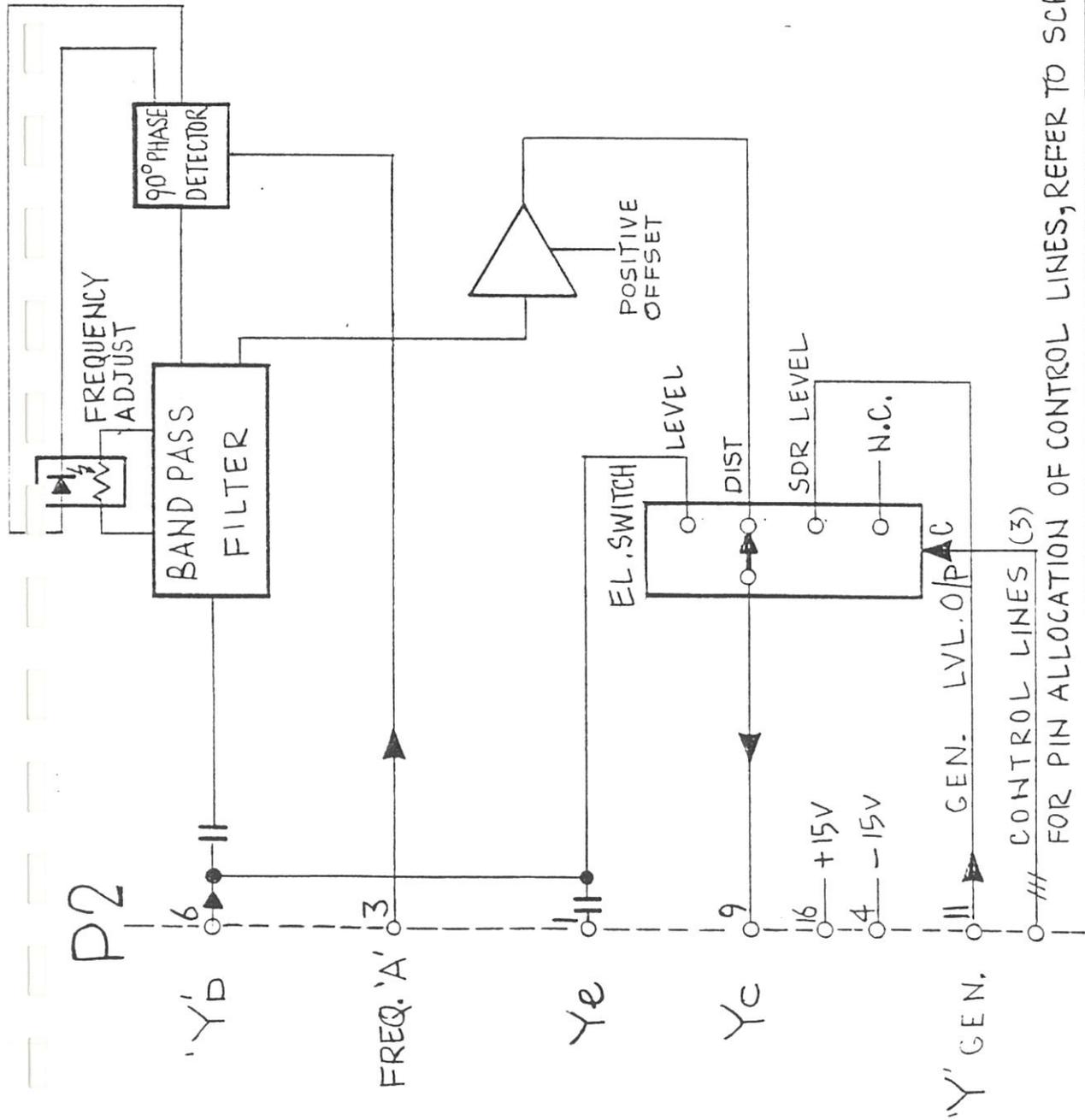
SCALE LR

DRAWN BY LR

CHK'D BY J

DATE MAR 4 1974

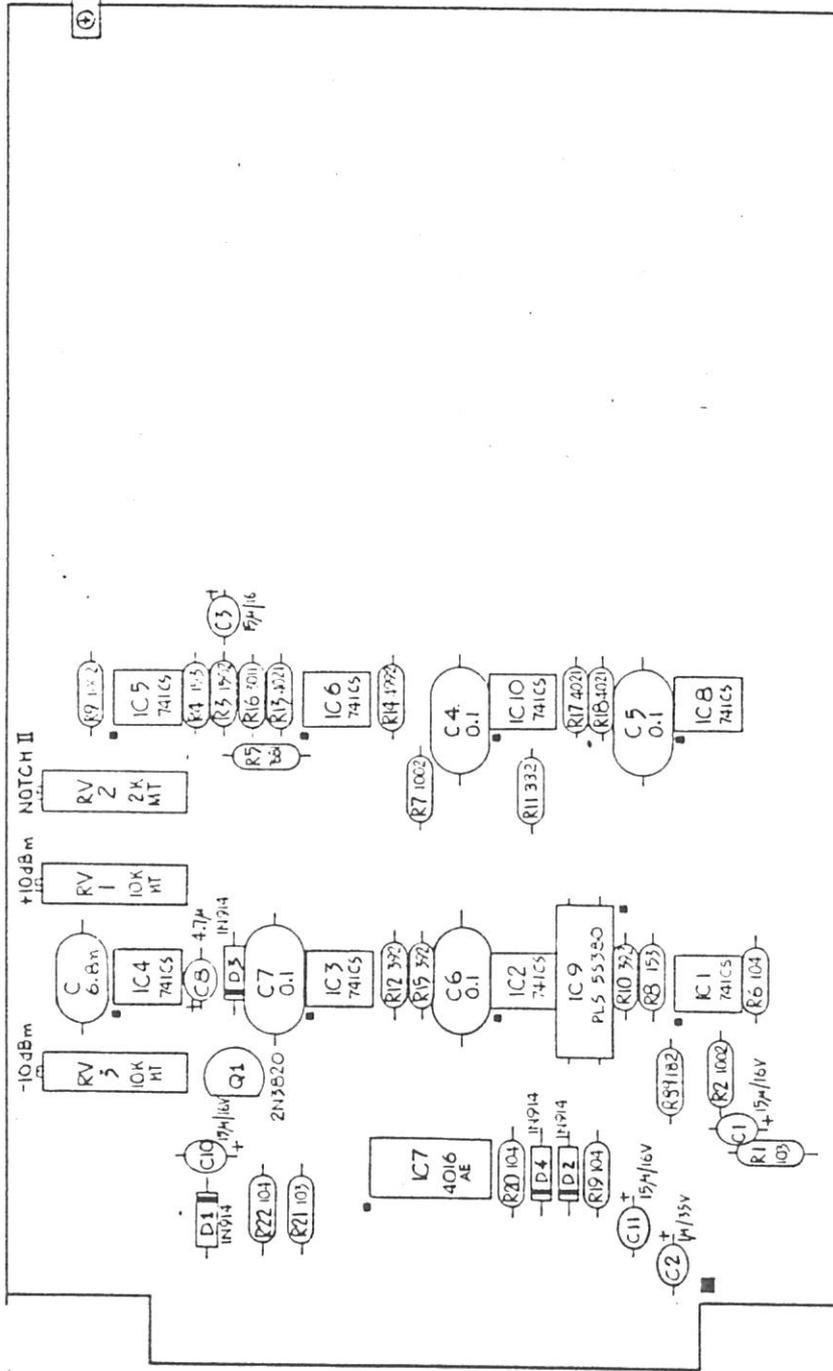
110015-3S-4S



FOR PIN ALLOCATION OF CONTROL LINES, REFER TO SCHEMATIC DWG. NO 110015-1S.

CONSULTRONICS LTD TORONTO CANADA	NOTCH P.C.B. - BLOCK SCHEMATIC MODEL #302	DR'N BY	LR	SCALE	-
		CH'D BY	A.K.	DWG. No.	110015-1B
MAT'L	No. REQ'D	DATE	FEB 1, 1974		
CLASS					

DISTORTION NOTCH



MOUNT THE BRACKET AS SHOWN WITH NUT ON SOLDER SIDE

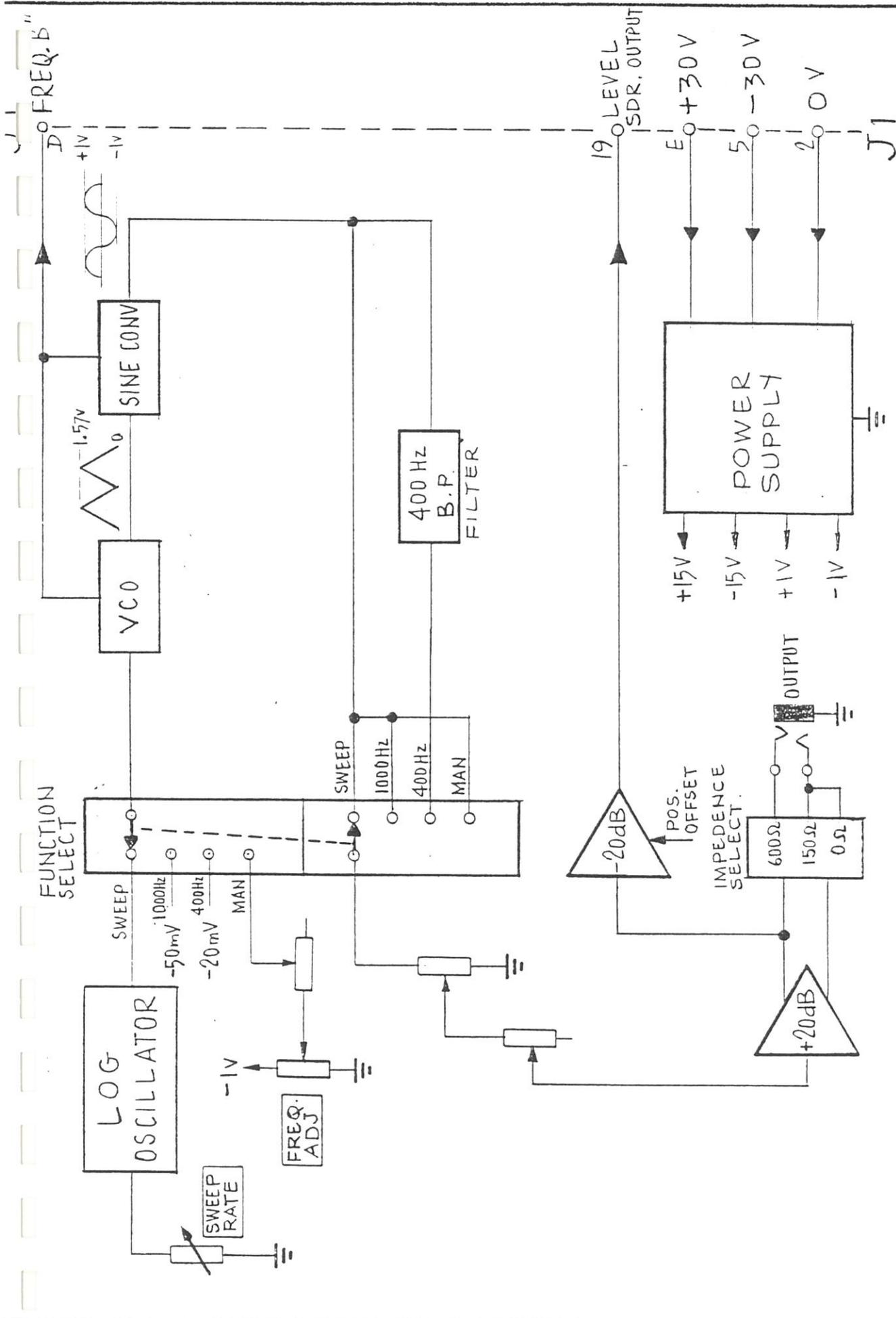
FRONT OF UNIT

COMPONENT SIDE

RESISTOR VALUES INDICATE COLOUR CODE EQUIV.

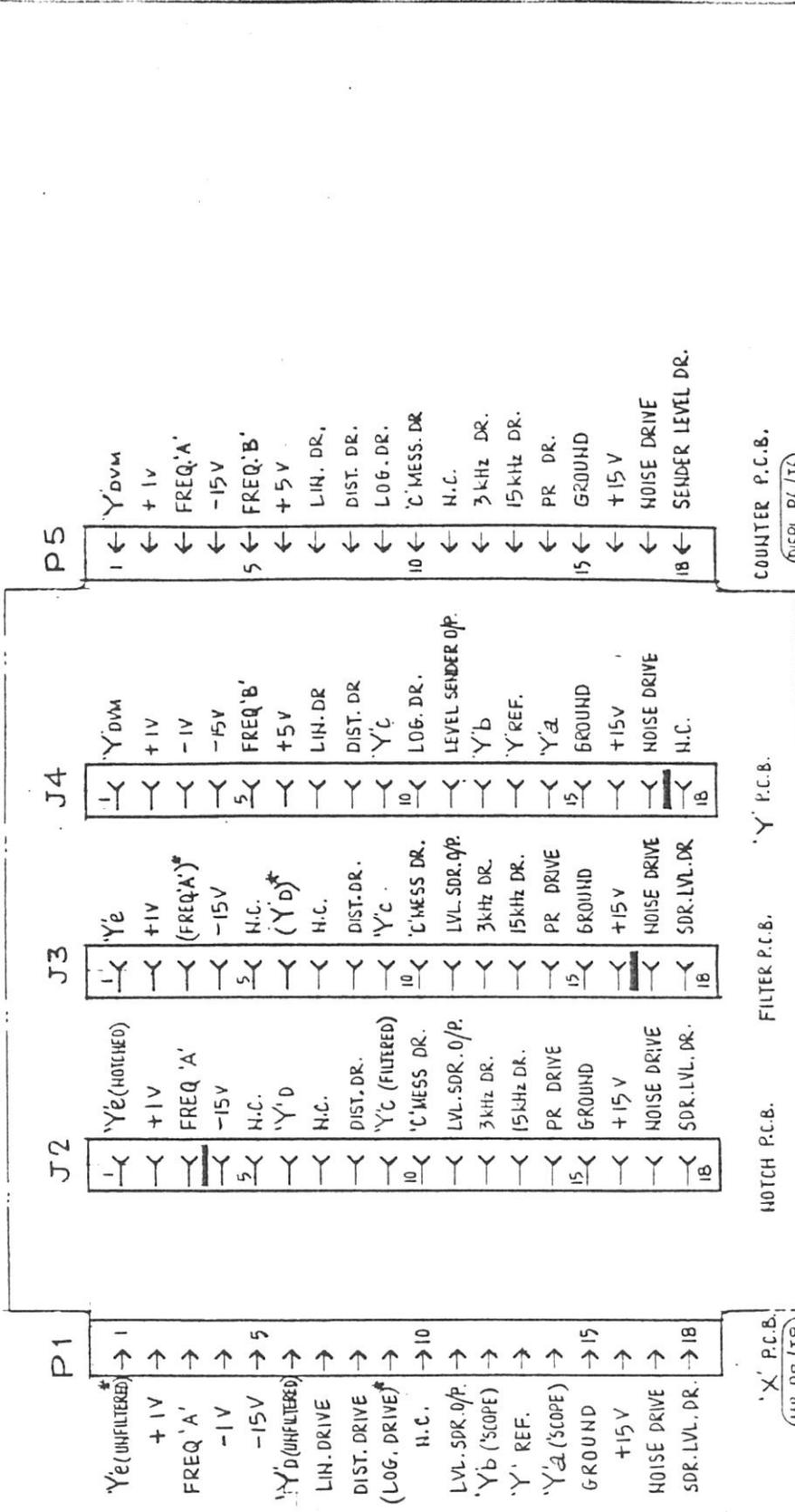
CONSULTRONICS LTD. TORONTO, CANADA.	NOTCH BOARD - P.C.B. ASSY. MODEL #302	DATE APRIL 16, 1975	SCALE 2:1
MATERIAL 400027-1	REV. REV. 0	DATE APRIL 16, 1975	110015-3





CONSULTRONICS LTD. TORONTO, CANADA	GENERATOR UNIT - BLOCK SCHEMATIC, AUDIO ANALYZER MODEL # 301		SCALE	110016-1B
	MAT'L	No. REQ'D	CLASS	
DR'N BY <i>JLR</i>		CH'D BY <i>JLR</i>	DATE <b>JAN 30, 1974</b>	DWG. No.





HOTCH P.C.B.      FILTER P.C.B.      COUNTER P.C.B.  
 (MB. P8/J8)      (MB. P7/J7)      (DISP. P6/J6)  
 ON TEKTRONIX      ON TEKTRONIX      ON COUNTER

NOTE:-  
 1. \* INDICATE UNUSED, BUT INTERNALLY CONNECTED PINS.  
 2. POLARIZING K51 + LOCATION

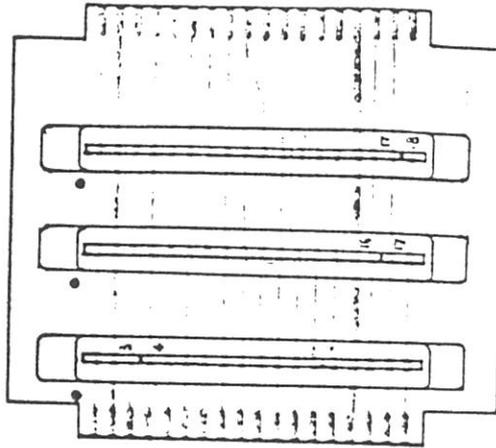
DRAWN BY LR      SCALE 1  
 CHECKED BY J.P.      DATE FEB. 7, 1974  
 110017-15

CONSULTRONICS LTD.      STRAP P.C.B.      SCHEMATIC  
 TORONTO, CANADA      MODEL # 302  
 PART 400022-1 P.C.B.      CLAIM

NOTE:-

1. THREE CONNECTORS REQD ON THIS ASSEMBLY.

2. - SHOWS THE POSITION OF POLARISING KEYS



CONSULTRONICS LTD.  
TORONTO, ONTARIO

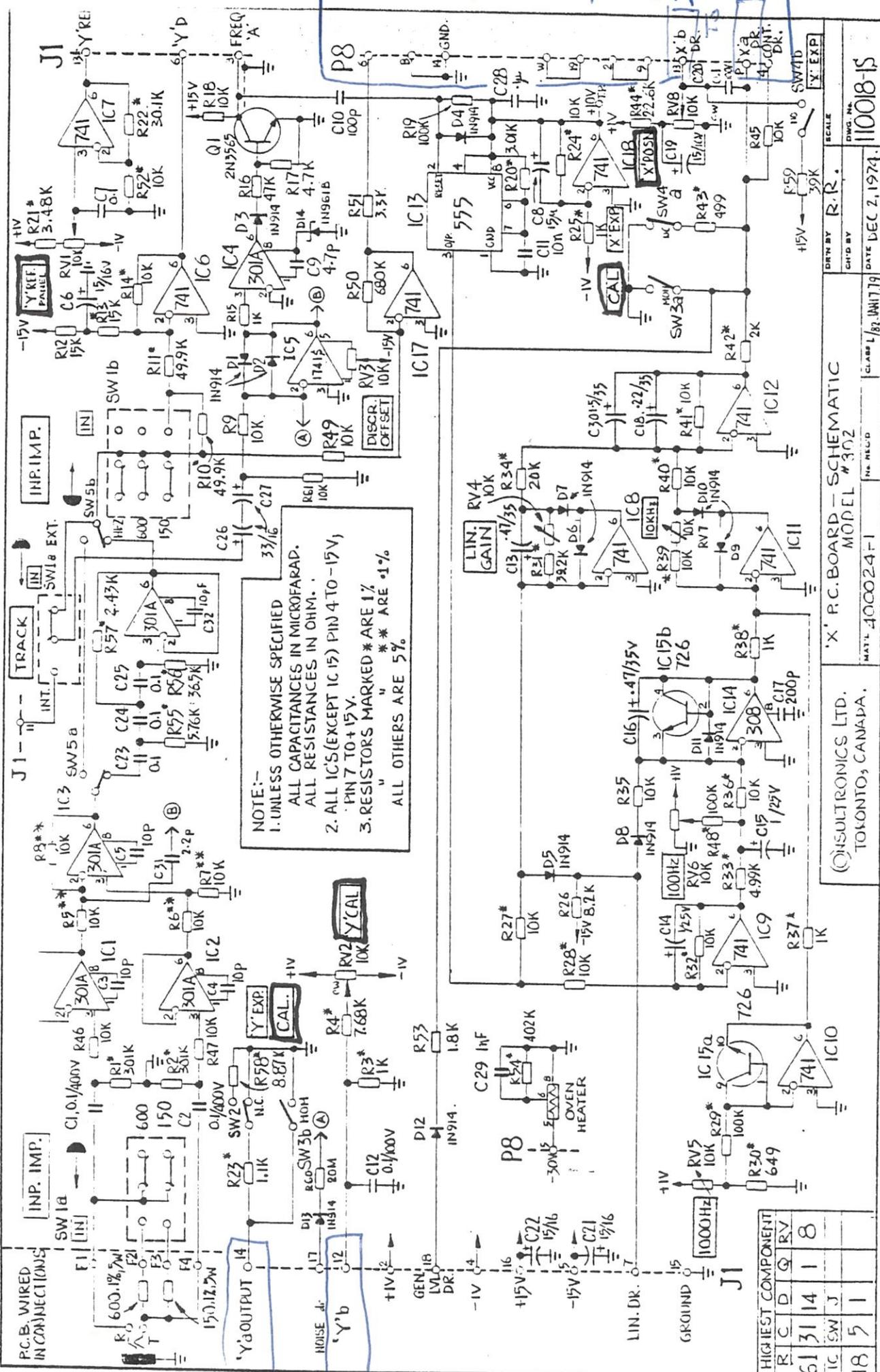
STRAP P.C.B. -

ASSEMBLY  
MODEL 302

400029-1

APR 11 1964  
A.P.  
110017-1

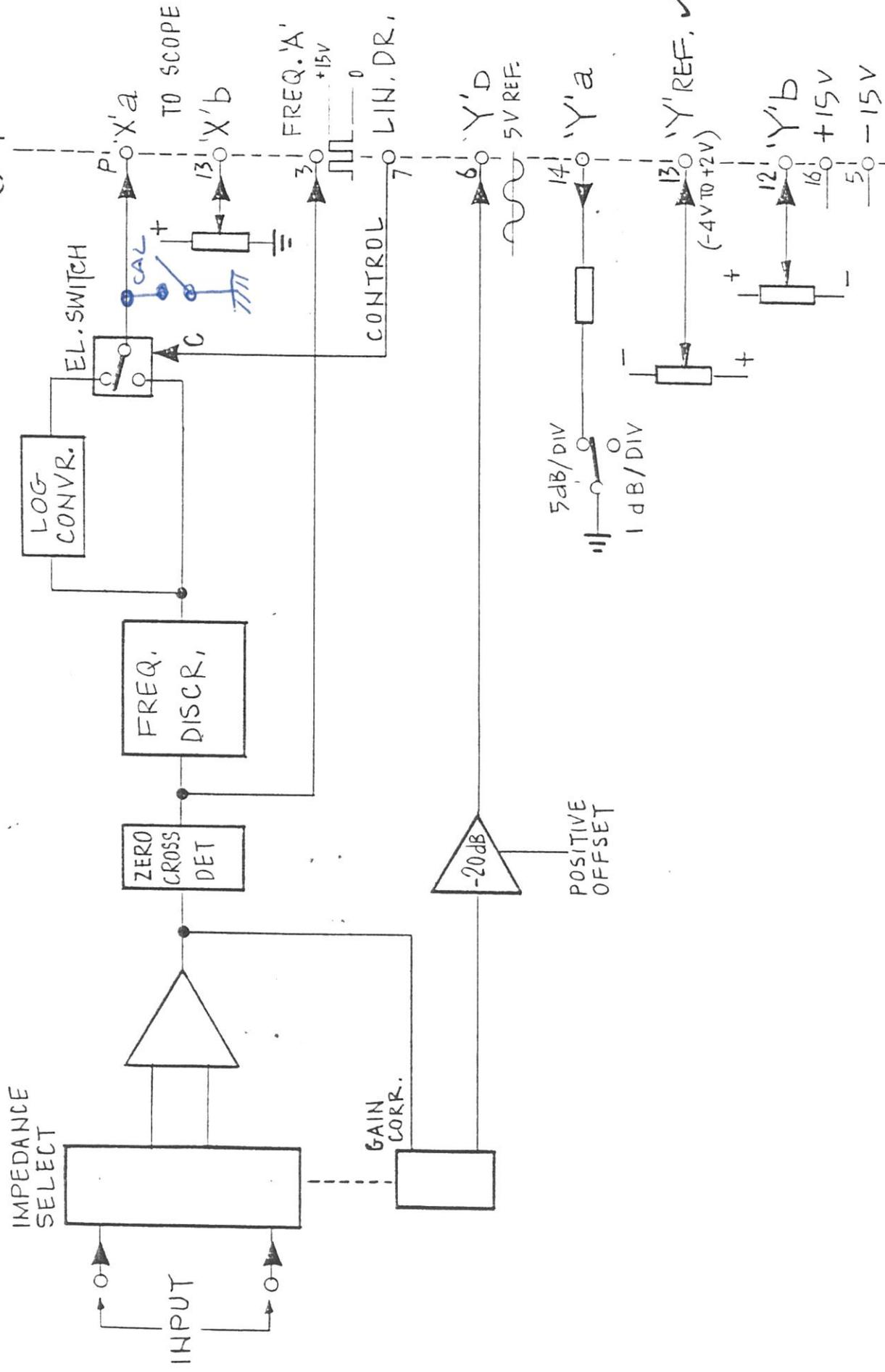
TEK 700 Scope



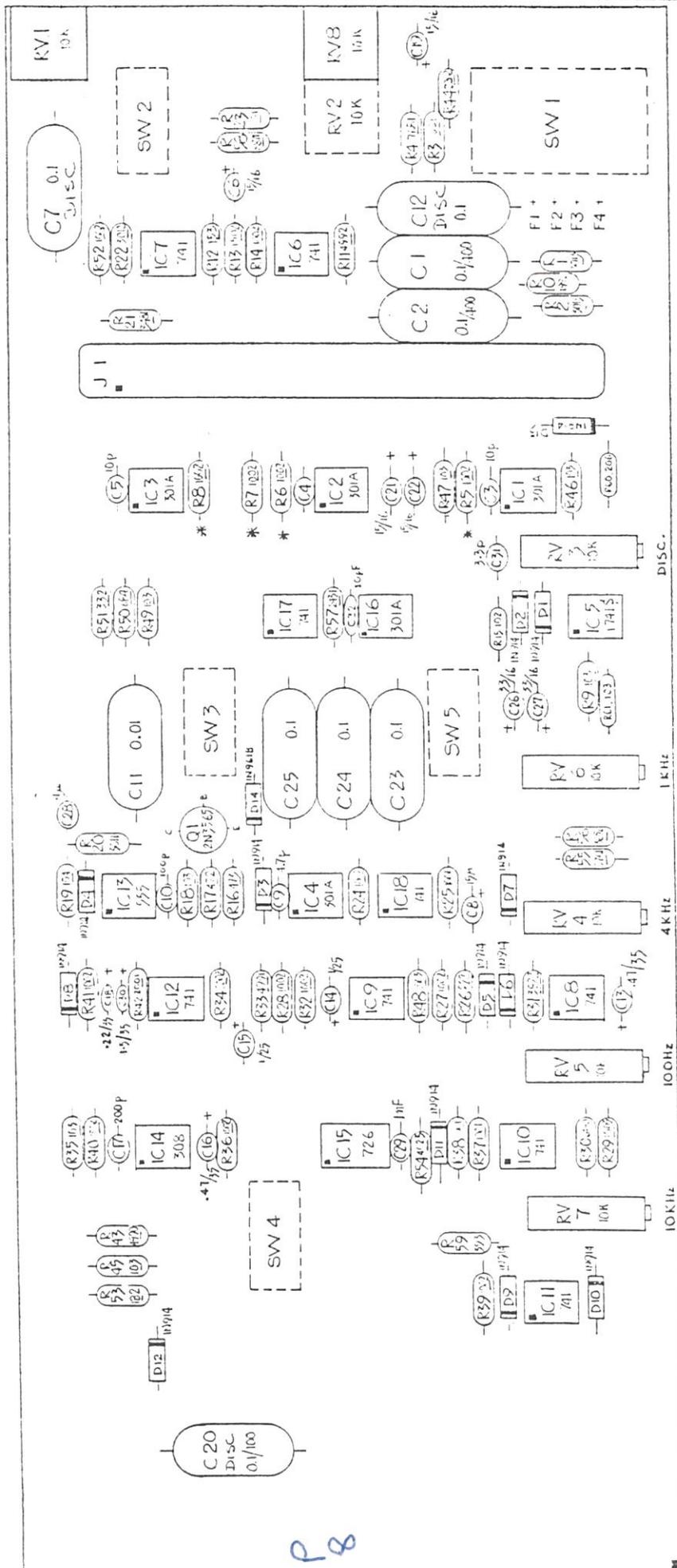
CONSULTRONICS LTD. TORONTO, CANADA.		SCALE DRAWN BY R.R.	
MODEL #302		DATE DEC 2, 1974.	
MATERIAL 400024-1		CLASS 1/82-1117/9	
DRAWING NO. 110018-15		REV. NO. X' EXP.	

HIGHEST COMPONENT			
R	C	D	RV
61	31	14	1
18	5	1	8

J1



CONSULTRONICS LTD TORONTO CANADA	"X" P.C.B. - BLOCK SCHEMATIC MODEL # 302	DR'N BY	LR	SCALE	110018-1B
		CH'D BY	J.R.	DWG. No.	
MAT'L	No. REQ'D	CLASS REV A	JUNE 71	DATE	FEB 1, 1974

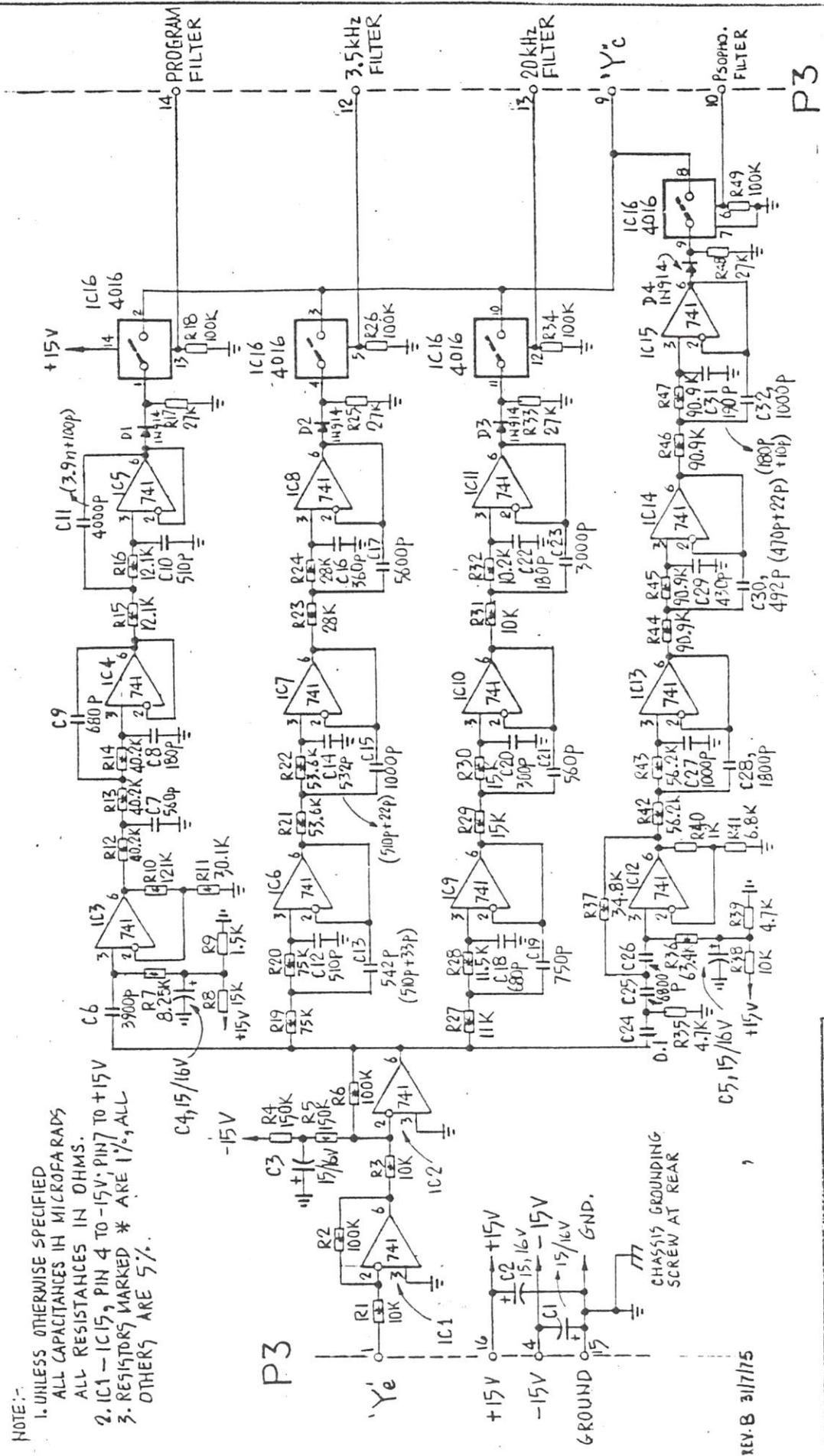


FRONT OF UNIT →

COMPONENT SIDE

RESISTOR VALUES INDICATE COLOUR CODE EQUIV.  
RESISTORS MARKED \* ARE ±1% SELECTED TYPE.

NOTE:-  
 1. UNLESS OTHERWISE SPECIFIED  
 ALL CAPACITANCES IN MICROFARADS  
 ALL RESISTANCES IN OHMS.  
 2. IC1 - IC15, PIN 4 TO -15V, PIN 7 TO +15V  
 3. RESISTORS MARKED \* ARE 1%, ALL  
 OTHERS ARE 5%.



SCALE: \_\_\_\_\_  
 DESIGNED BY: \_\_\_\_\_  
 CHECKED BY: \_\_\_\_\_  
 DATE: MAR. 20, '75

110047-15

DATE: \_\_\_\_\_  
 CLASS: \_\_\_\_\_  
 No. REC'D: \_\_\_\_\_  
 MATE: \_\_\_\_\_

SCHEMATIC - CCITT FILTER PCB.  
 MODEL #302

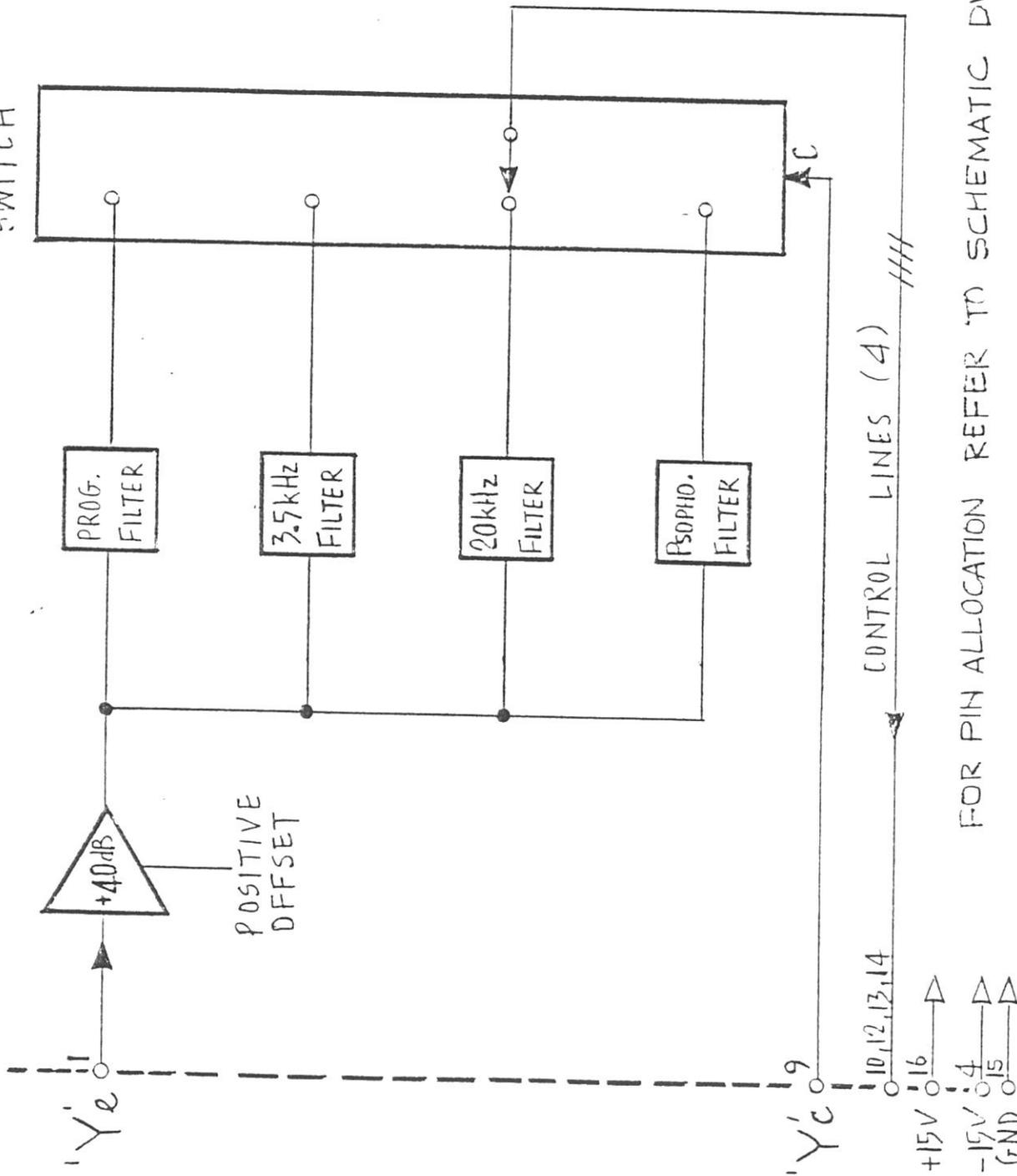
CONSULTRONICS LTD  
 TORONTO, CANADA.

HIGHEST COMPONENT	
R	IC
49	32
4	16

REV. B 3/17/75

F3

ELECTRONIC SWITCH



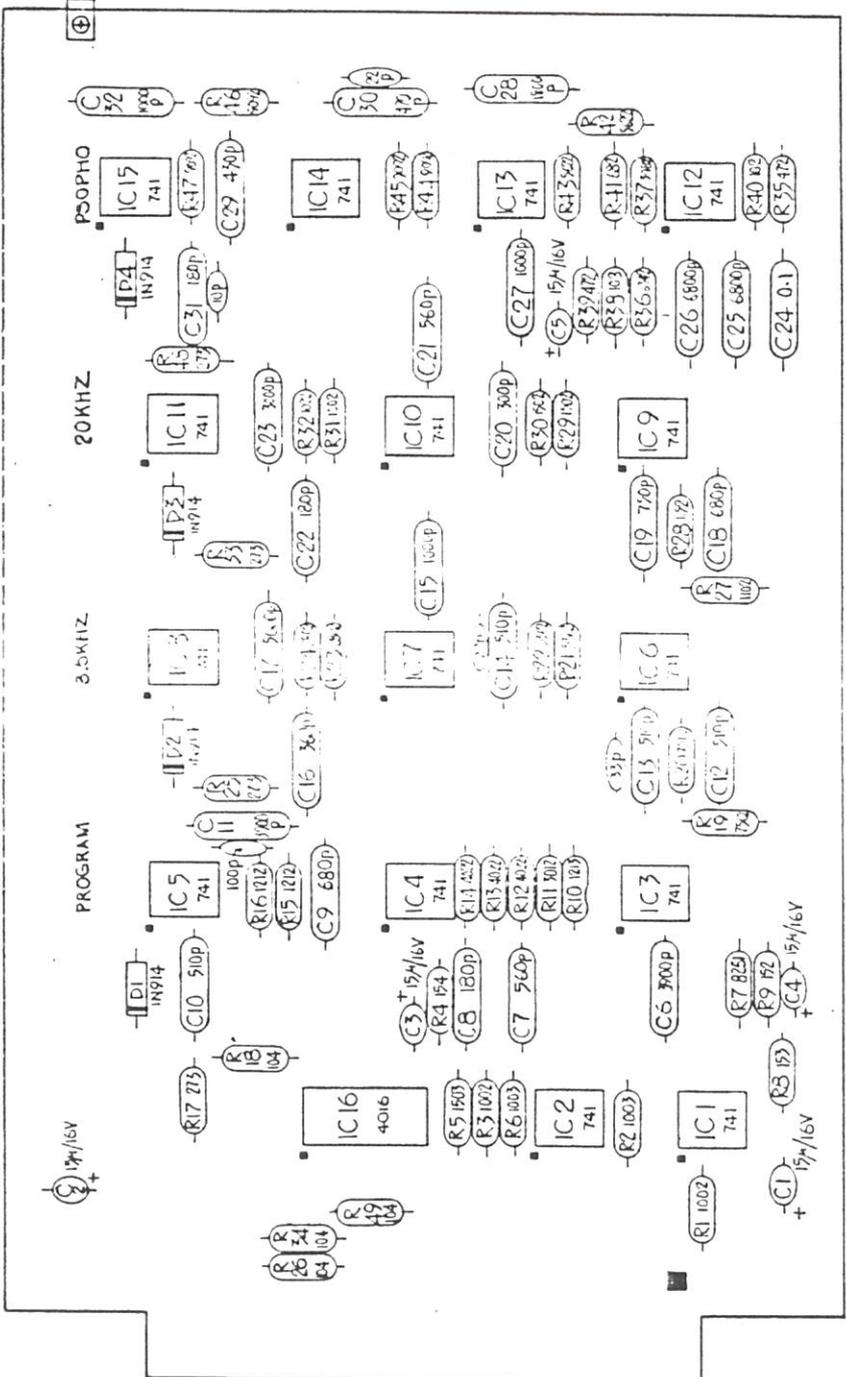
FOR PIN ALLOCATION REFER TO SCHEMATIC DWG. NO. 110047-1S

CONSULTRONICS LTD,  
TORONTO, CANADA.

CCITT FILTER - BLOCK SCHEMATIC  
MODEL 302

DR'N BY	LR	SCALE	-
CH'D BY	A.R.	DWG. No.	110047-1B
DATE	JUNE 14, 1977		

MAT'L No. REQ'D CLASS



MOUNT THE BRACKET AS SHOWN WITH NUT ON SOLDER SIDE.

RESISTOR VALUES INDICATE COLOUR CODE EQUIV.

REV. B. 31/7/75	SCALE 2:1	DATE 30.10.75	110047-1
REV. A. 31/7/75			
DESIGNED BY R.R.	SCALE 2:1	DATE 30.10.75	110047-1
CHECKED BY V.G.			
C.C.I.T.T. RC-B ASSY. MODEL # 302		DATE 30.10.75	
CONSULTRONICS LTD. TORONTO, CANADA.		DATE 30.10.75	